

## 7. LCM&TOUCH\_PANEL

## 8. MEMORY\_CARD

## BLUETOOTH

### 3. POWER

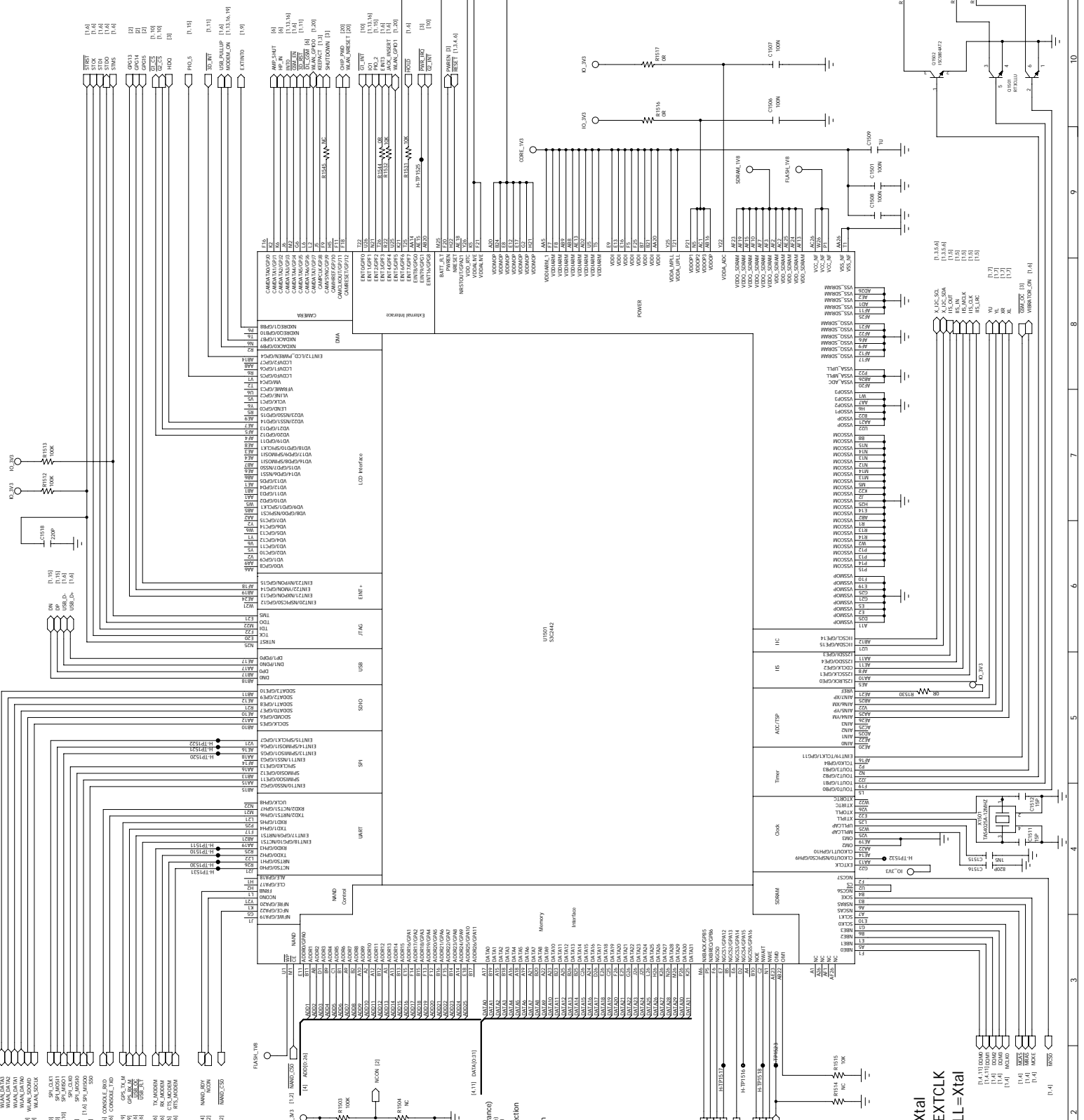
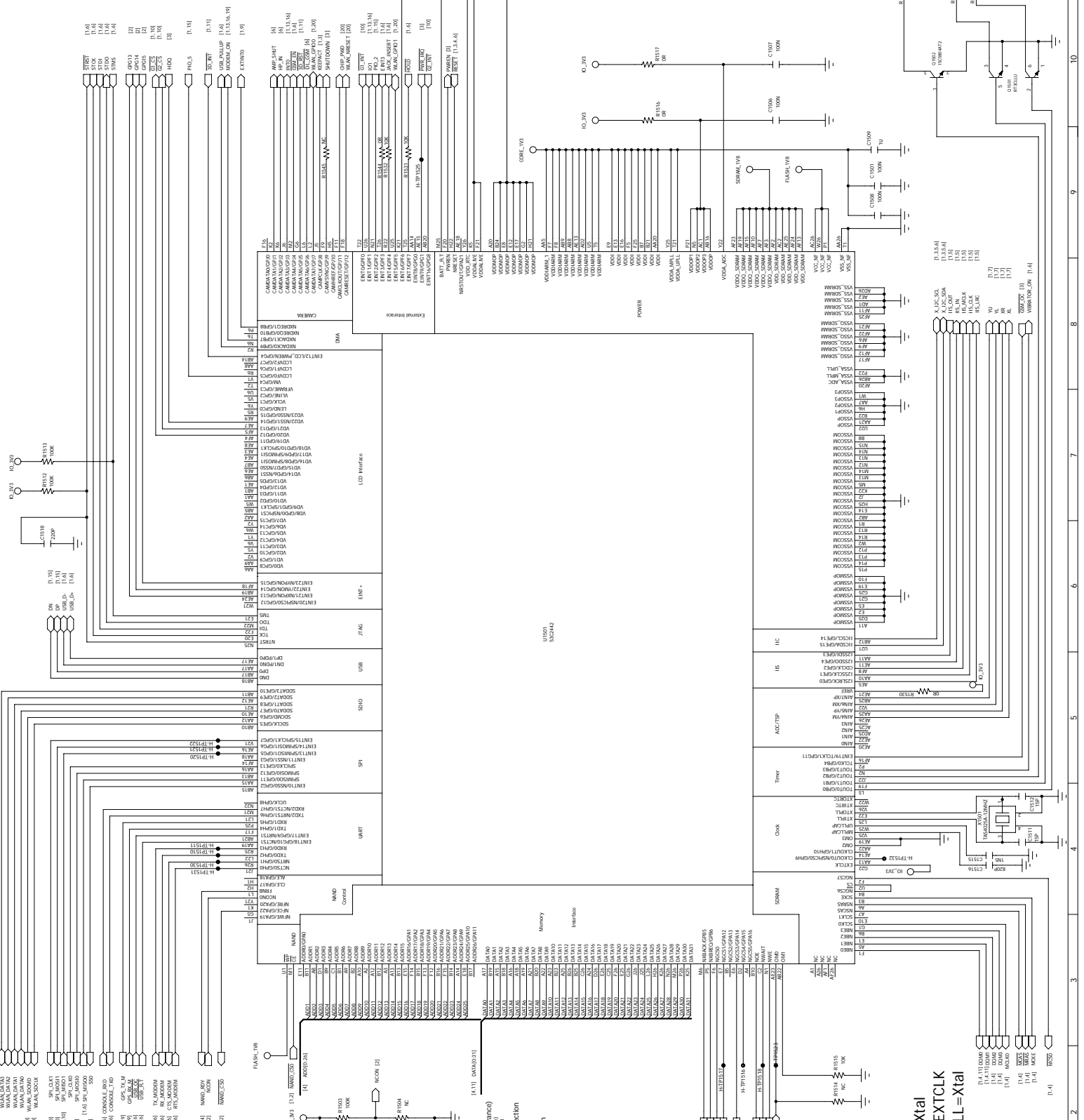
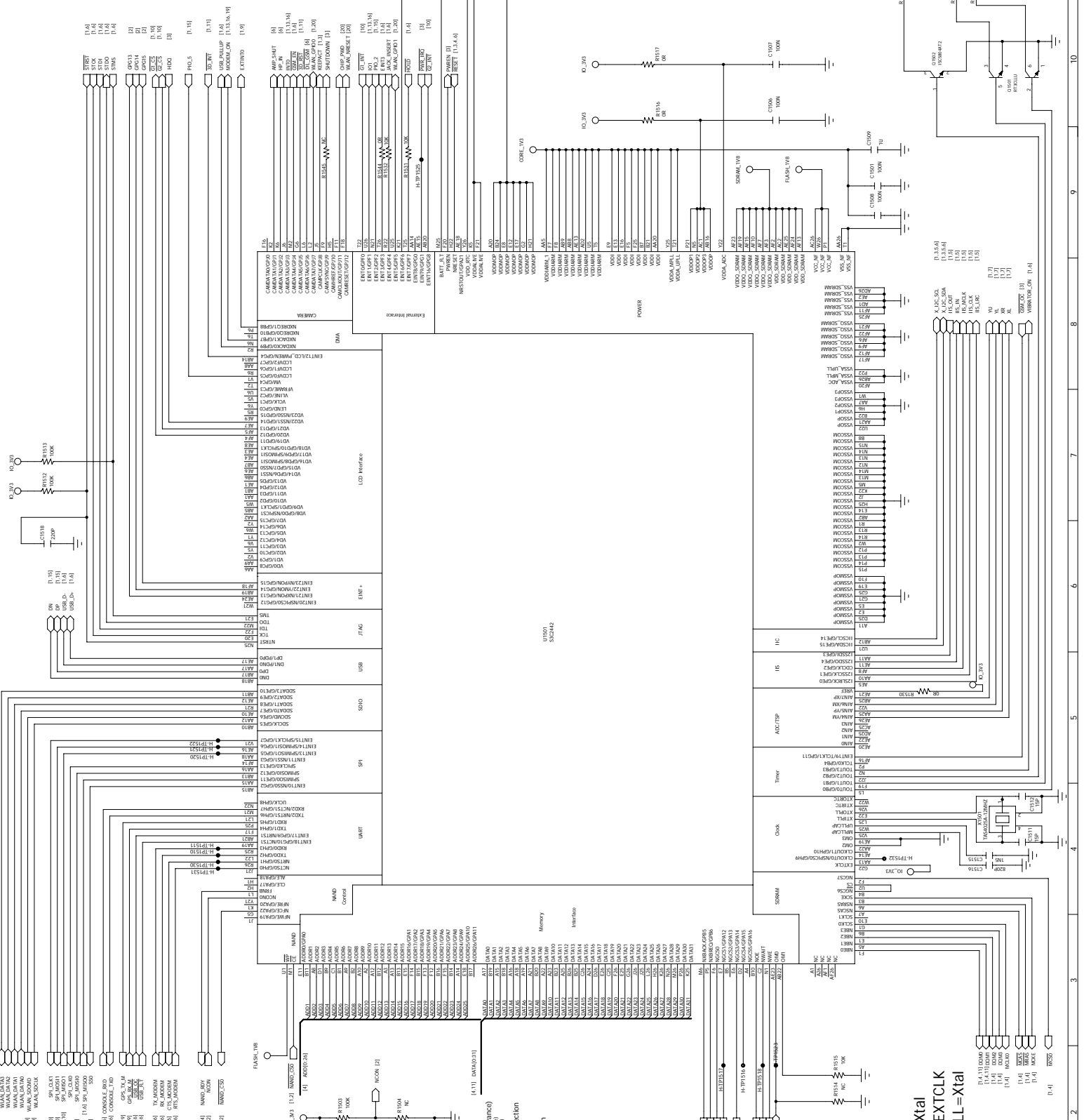
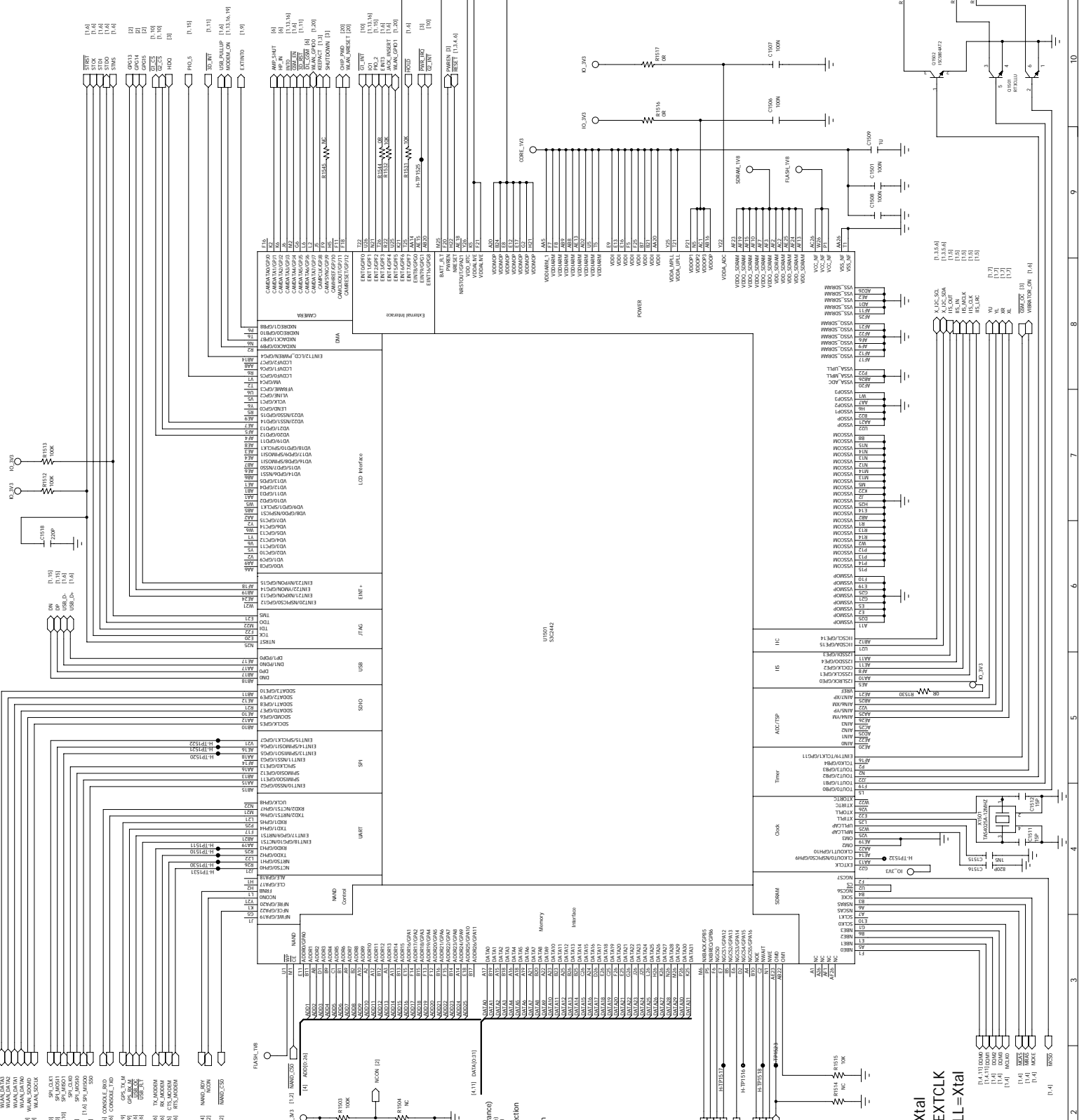
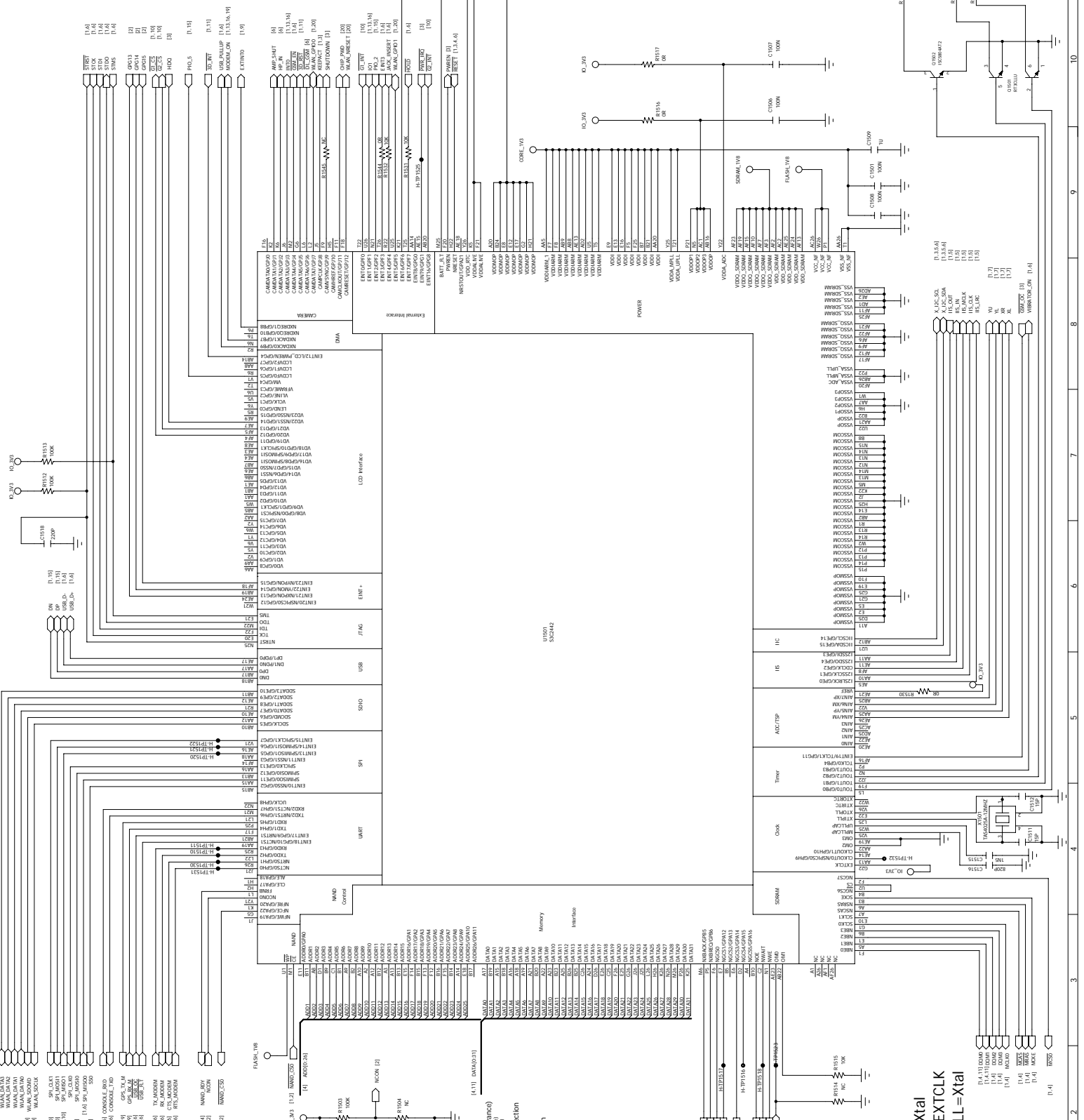
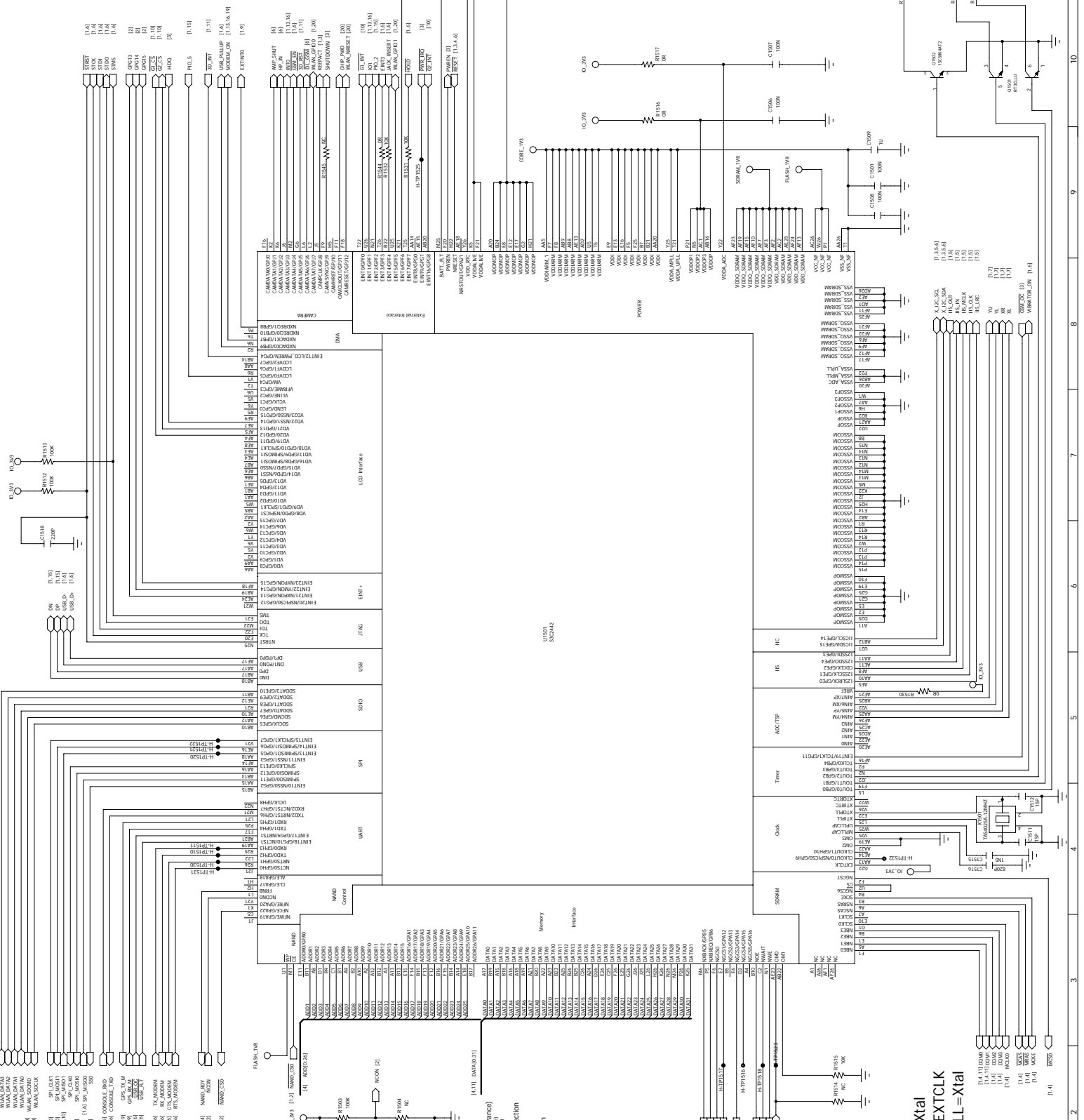
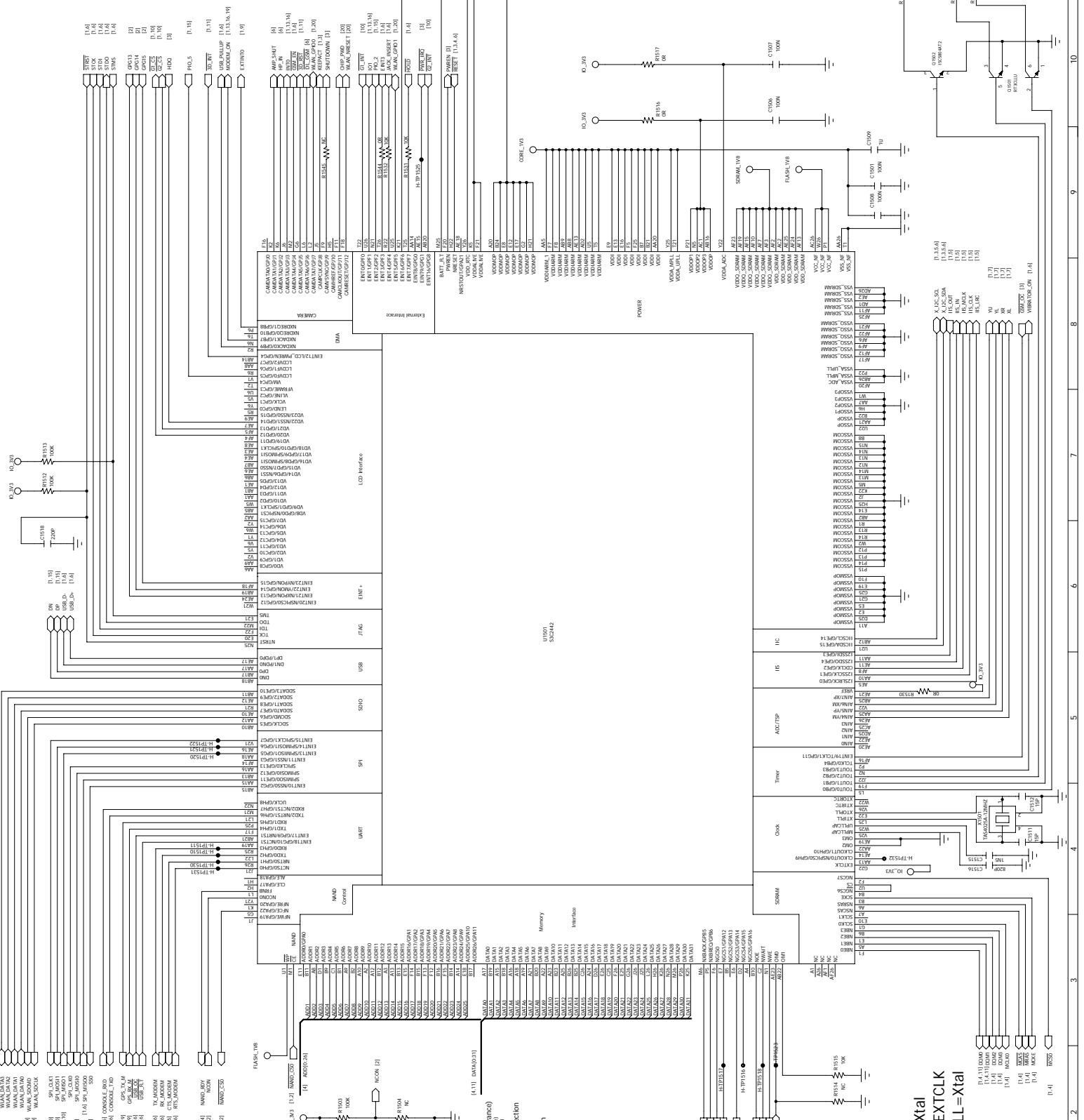
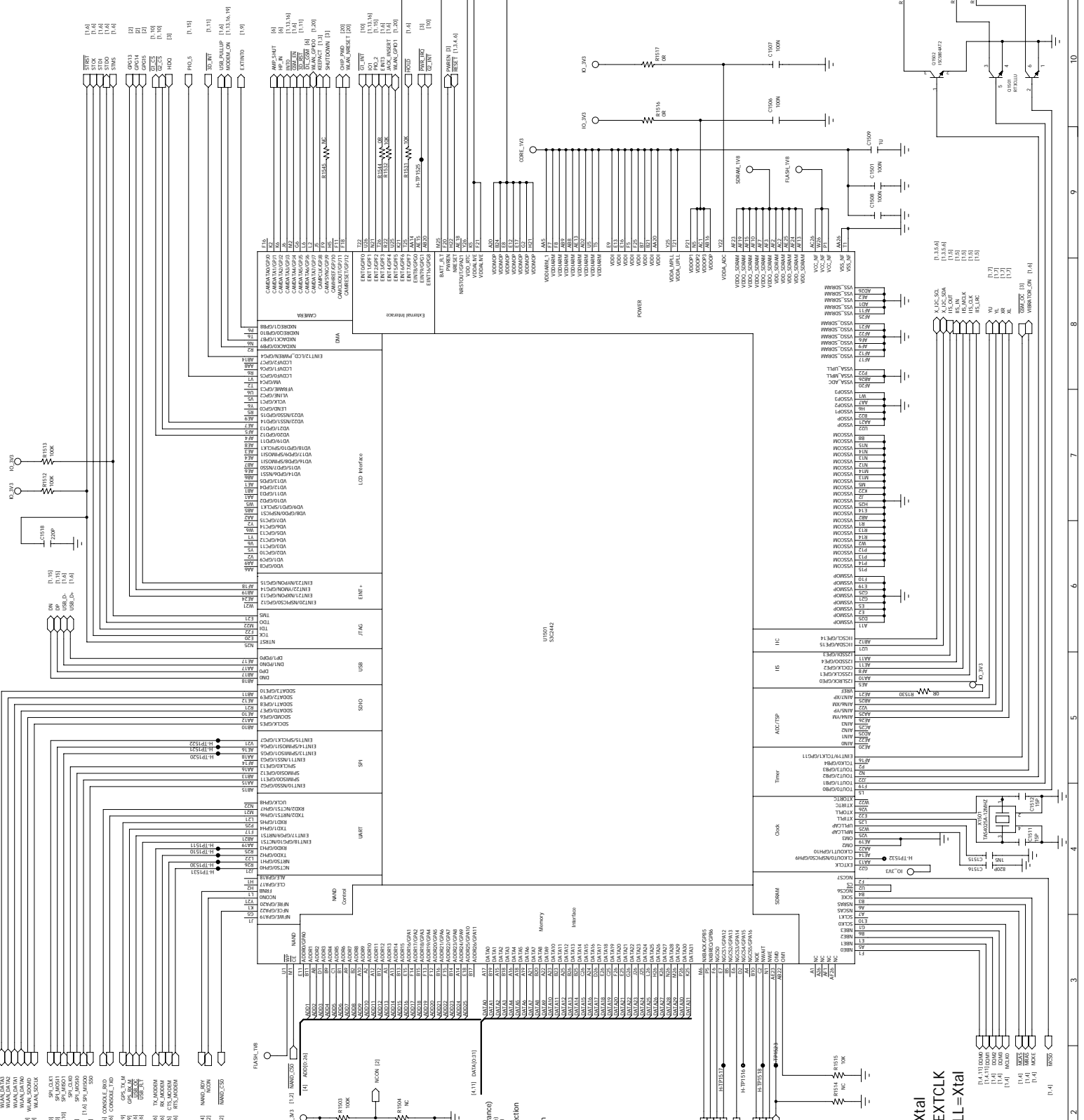
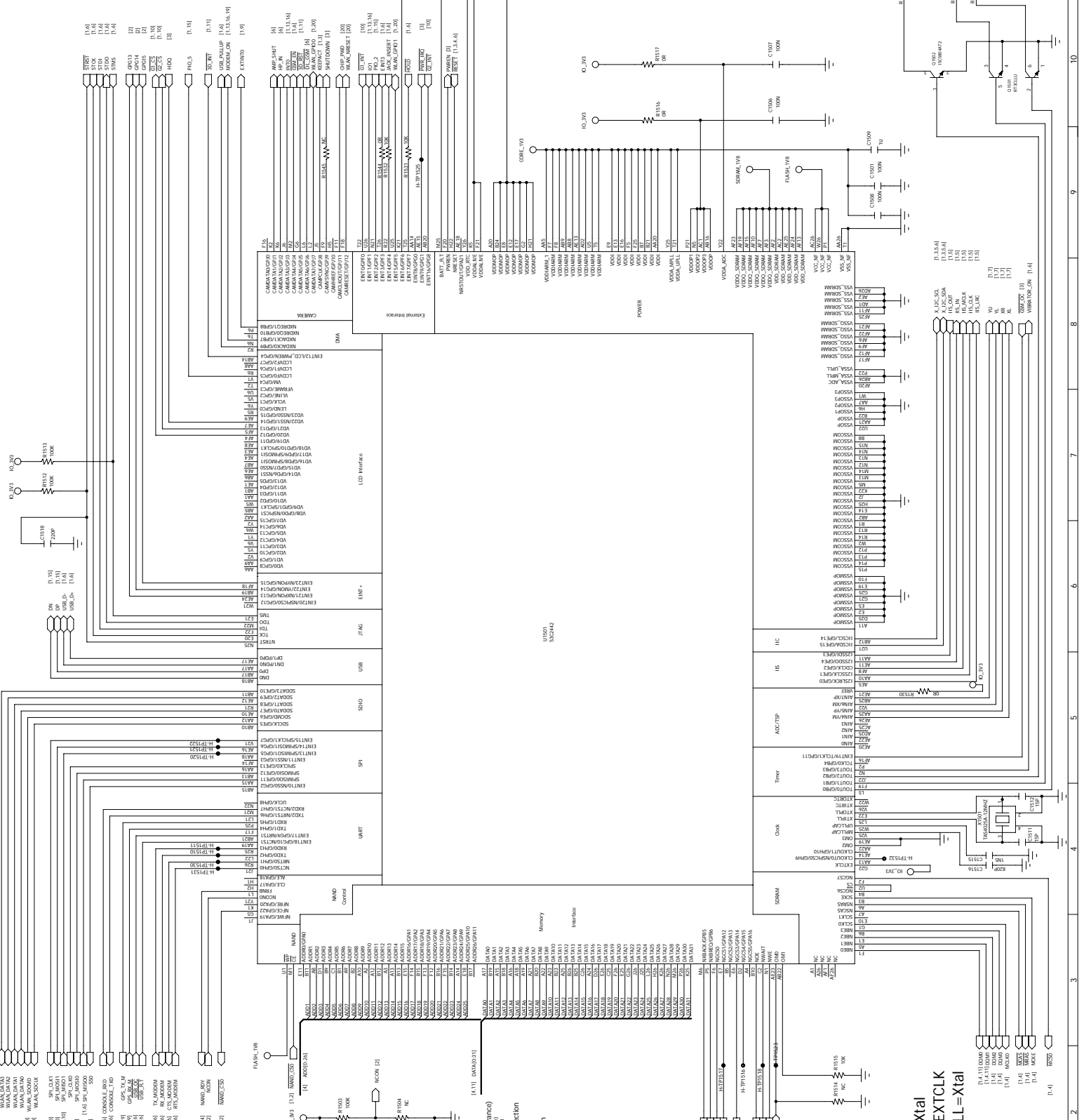
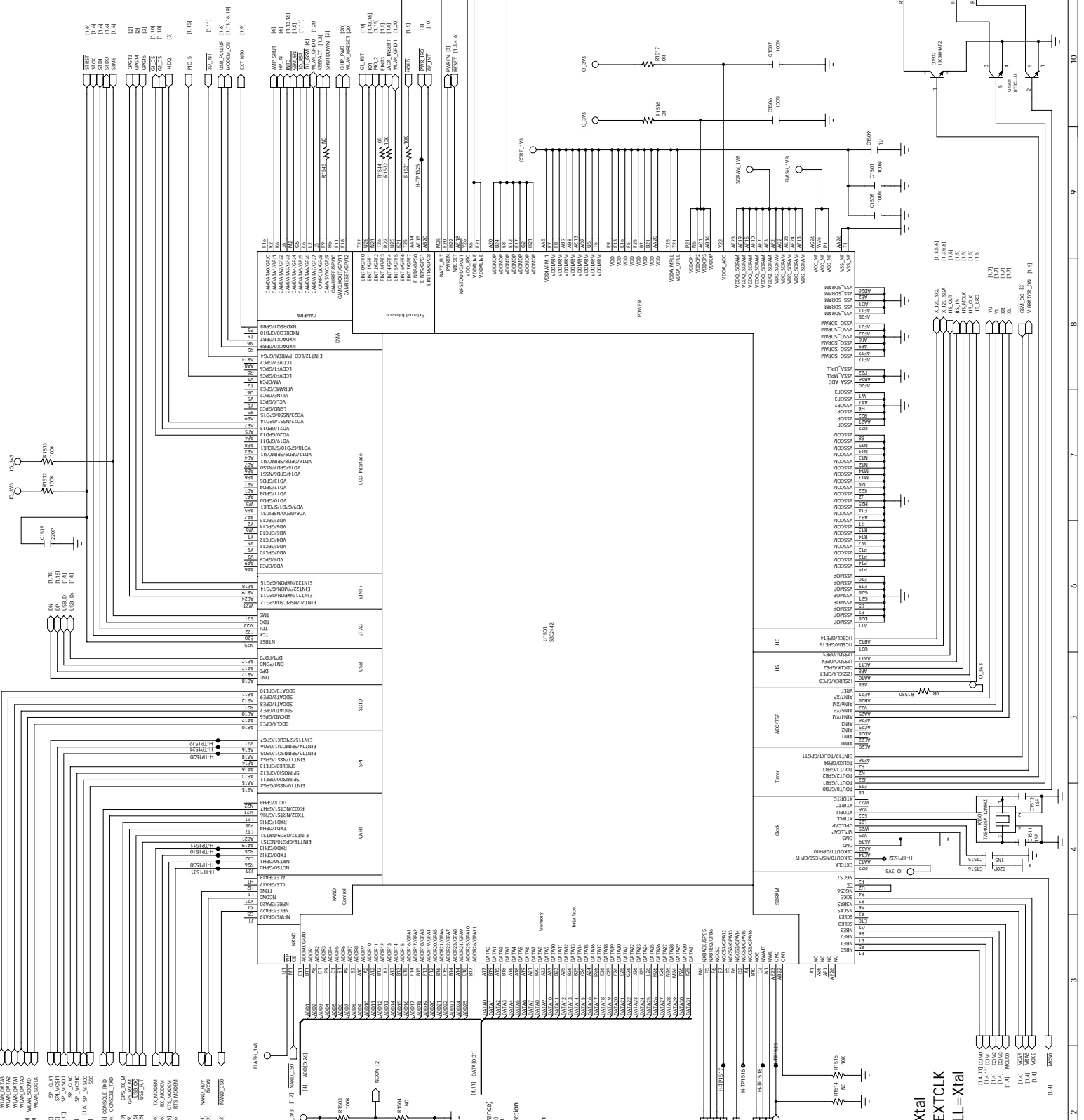
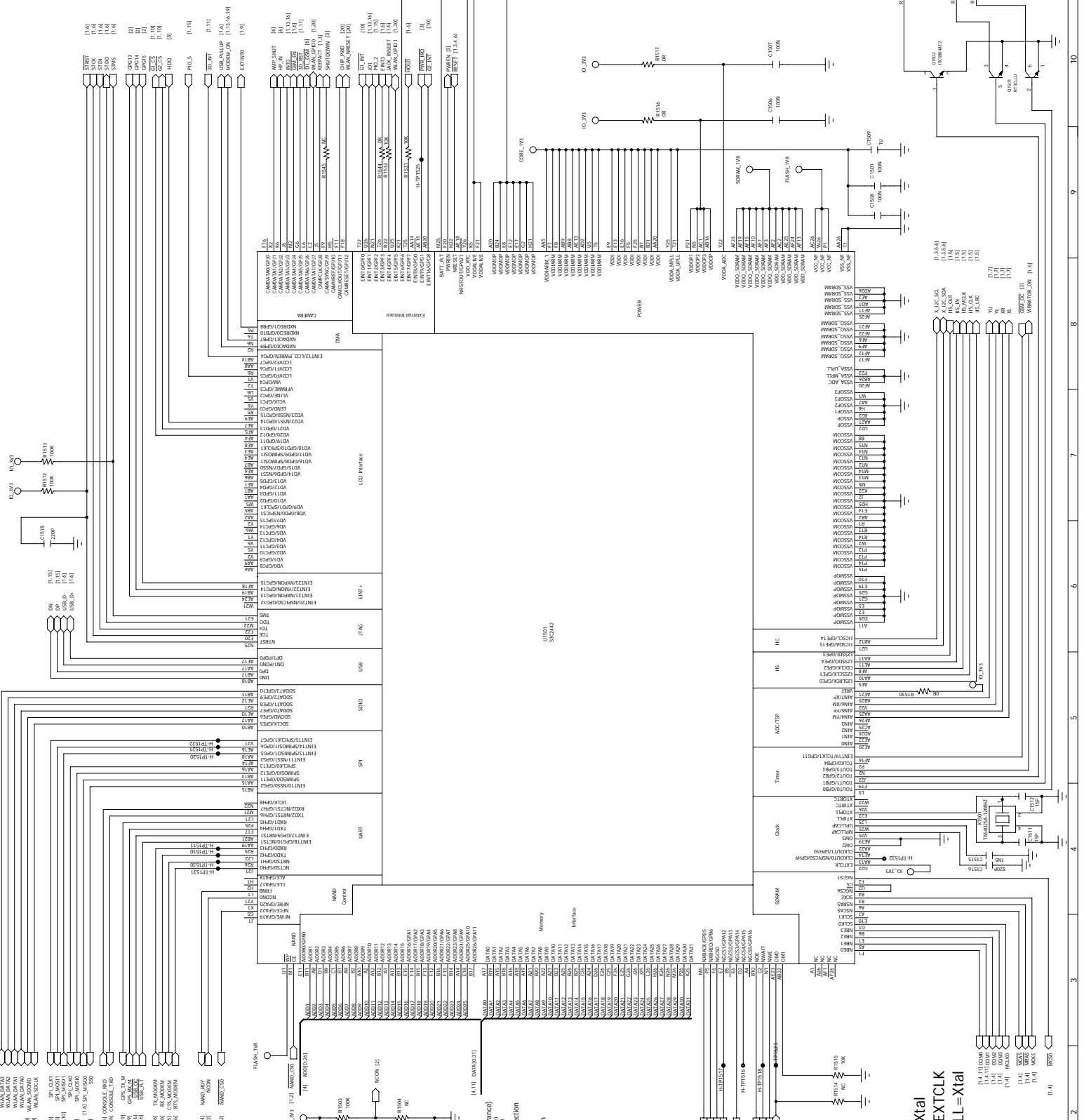
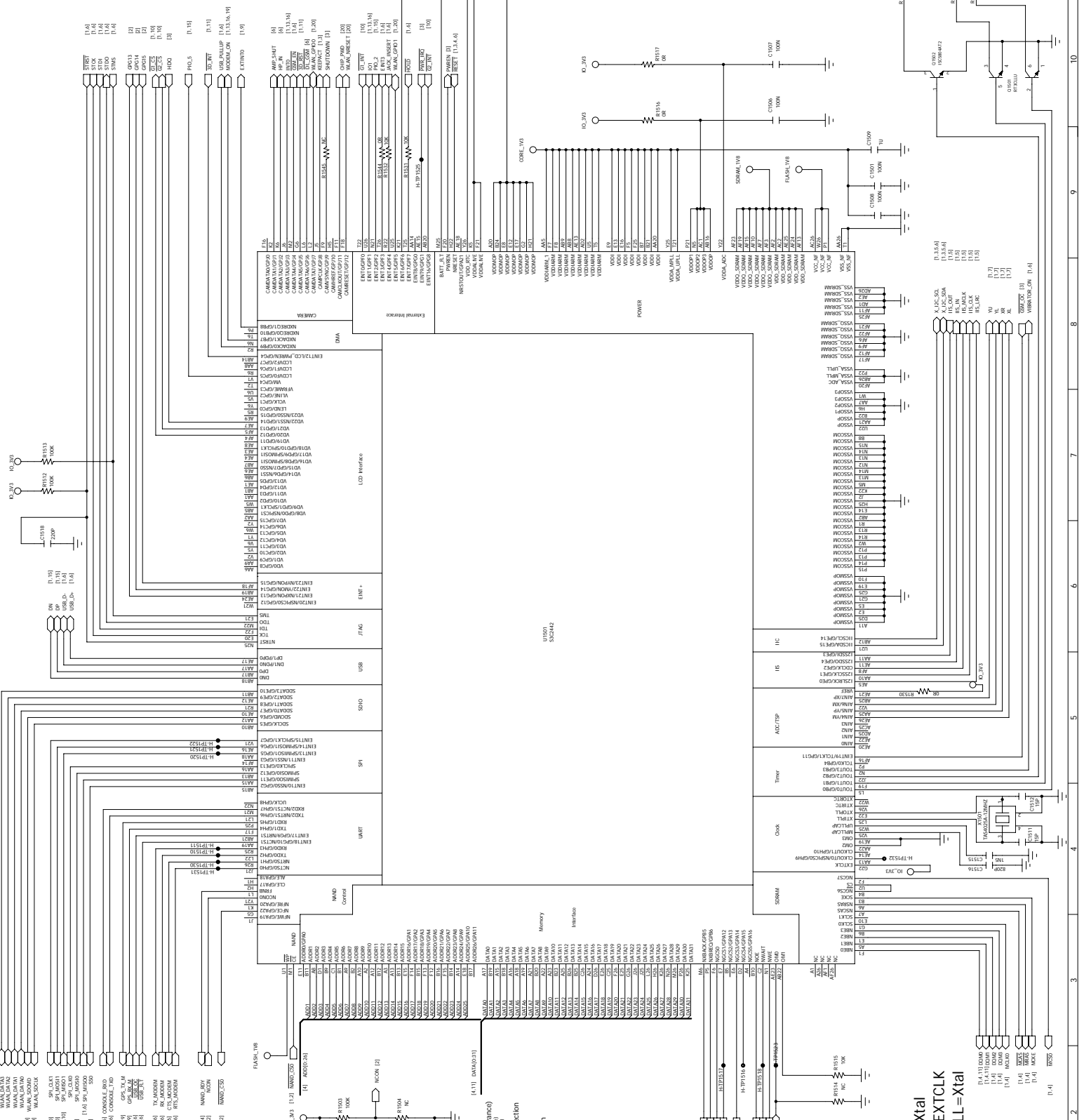
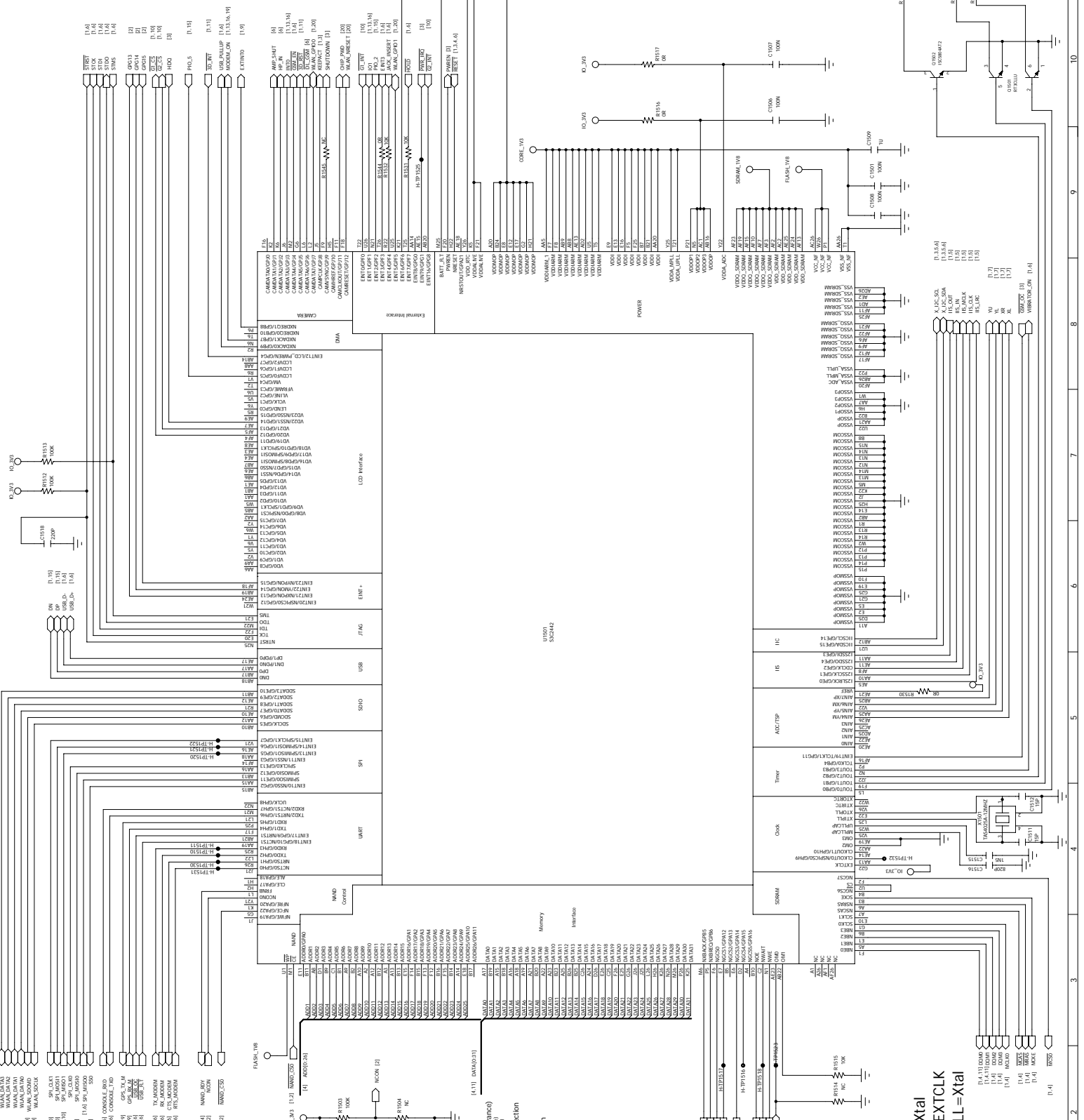
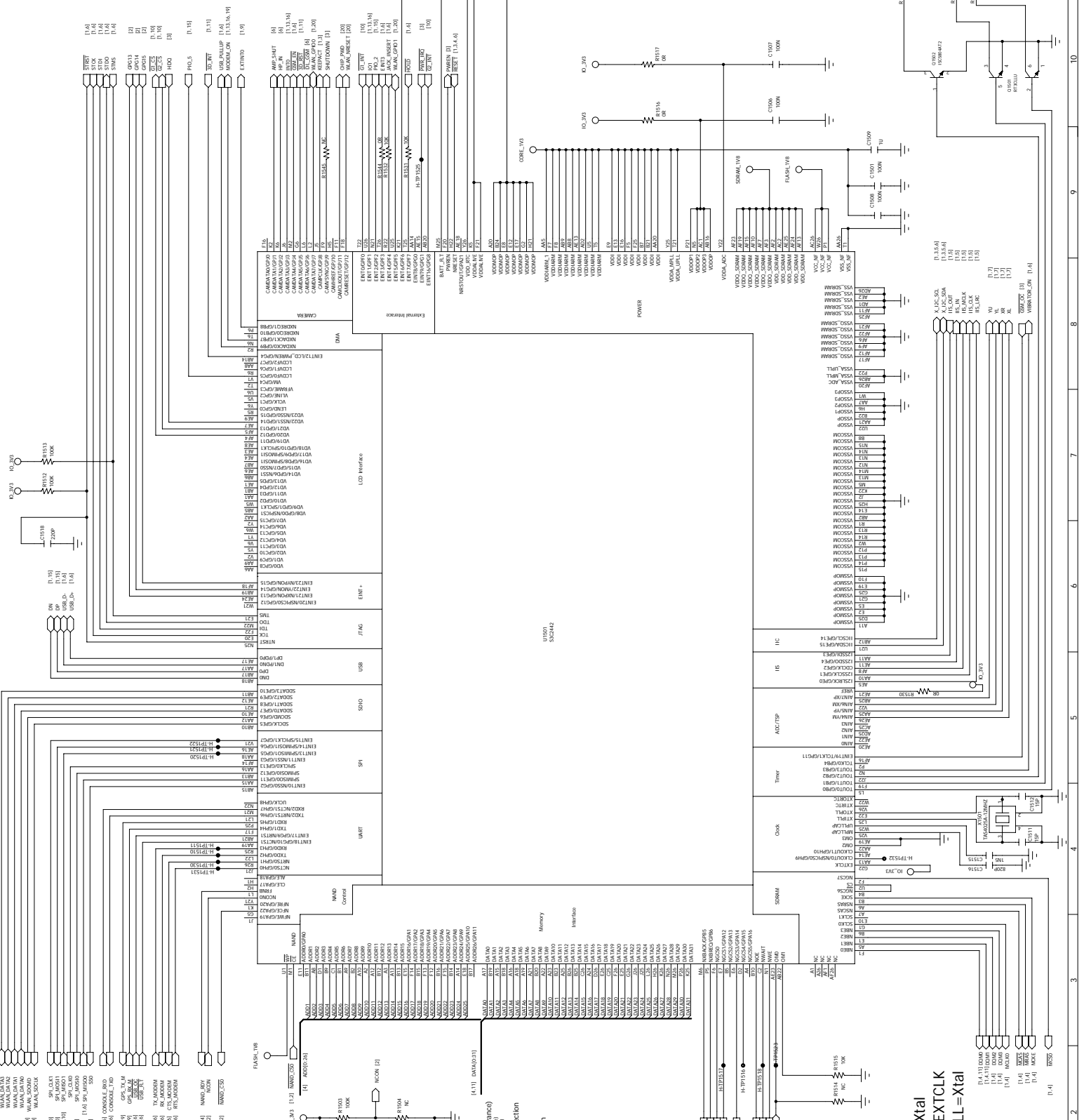
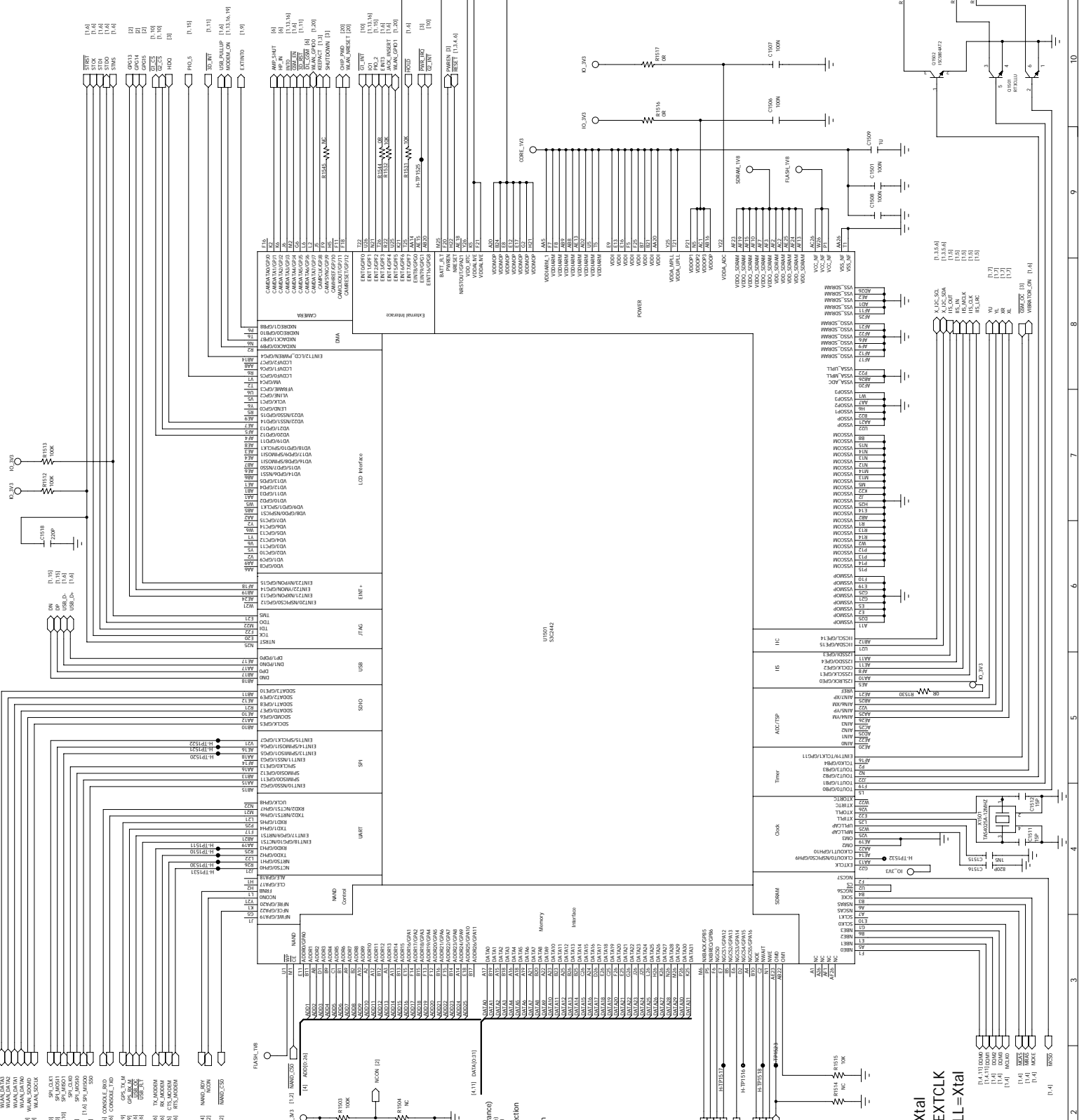
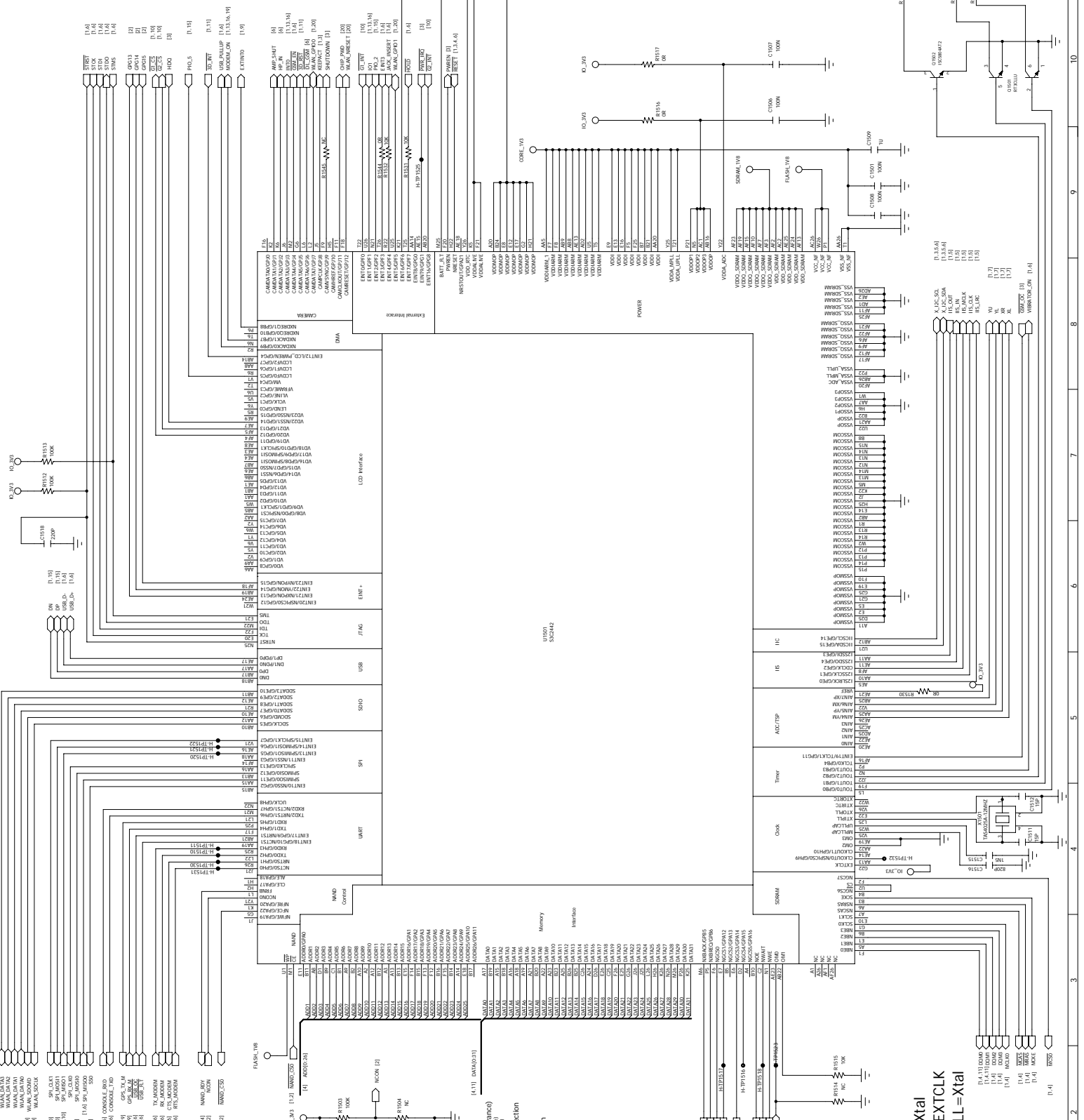
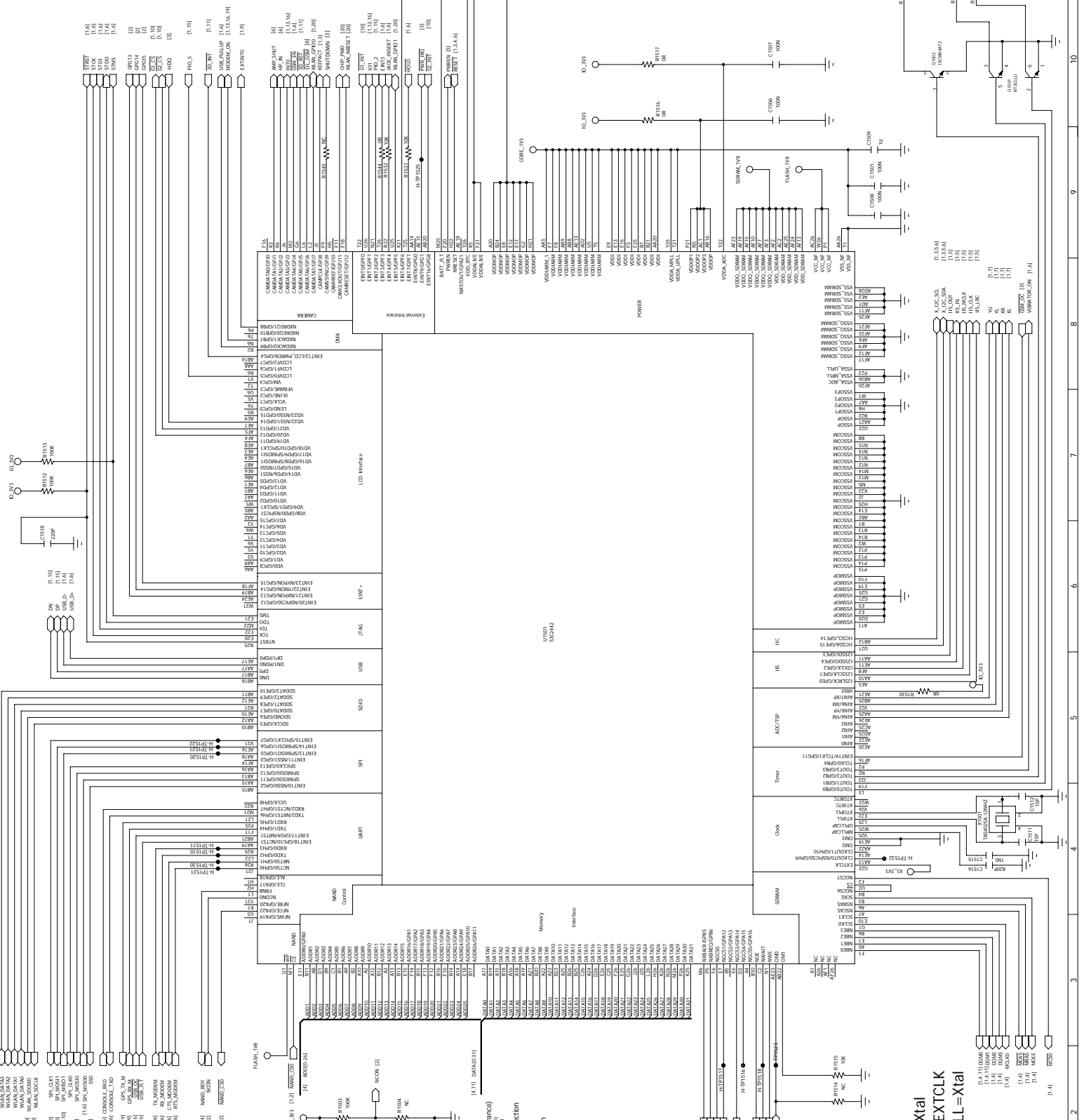
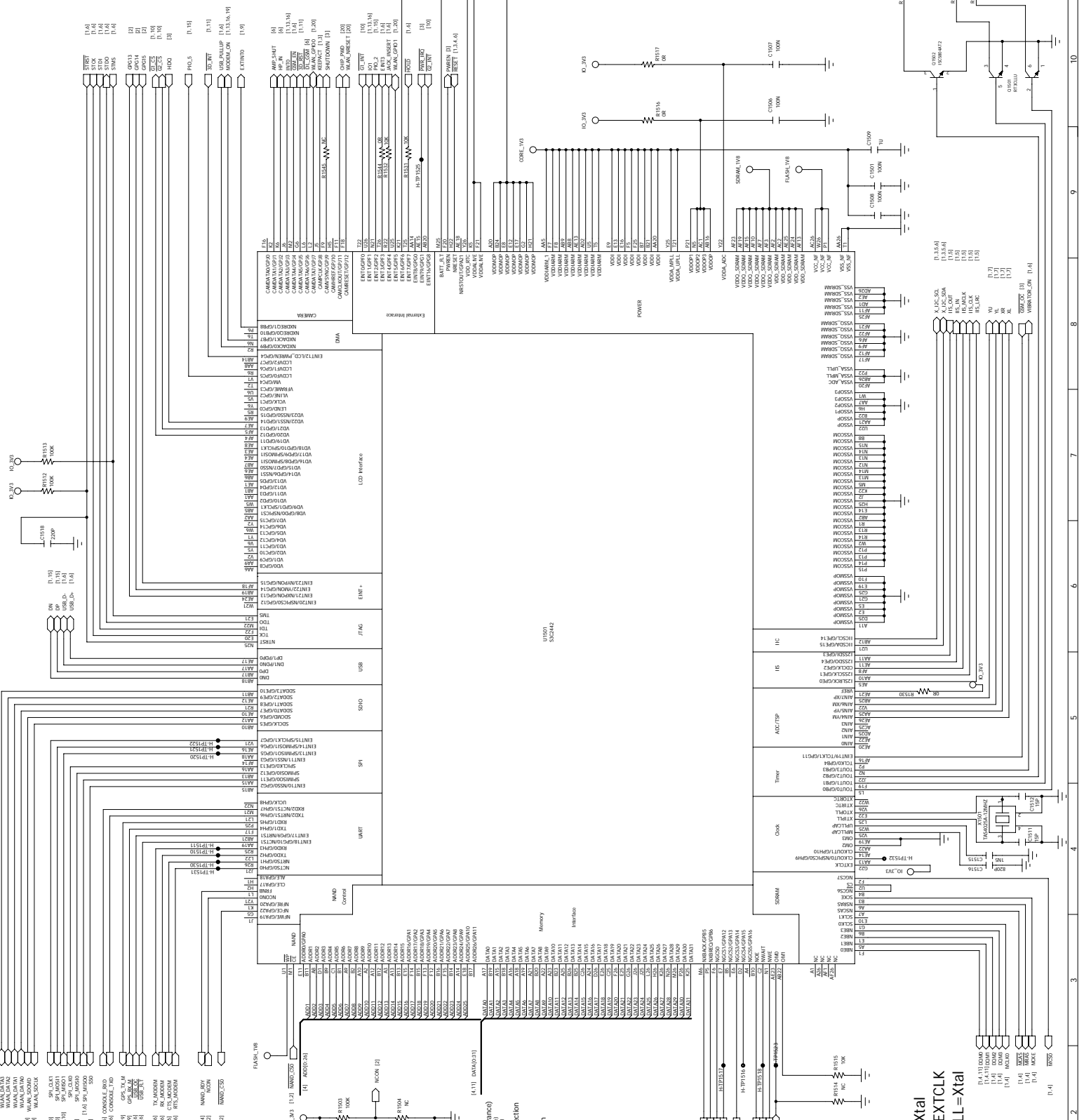
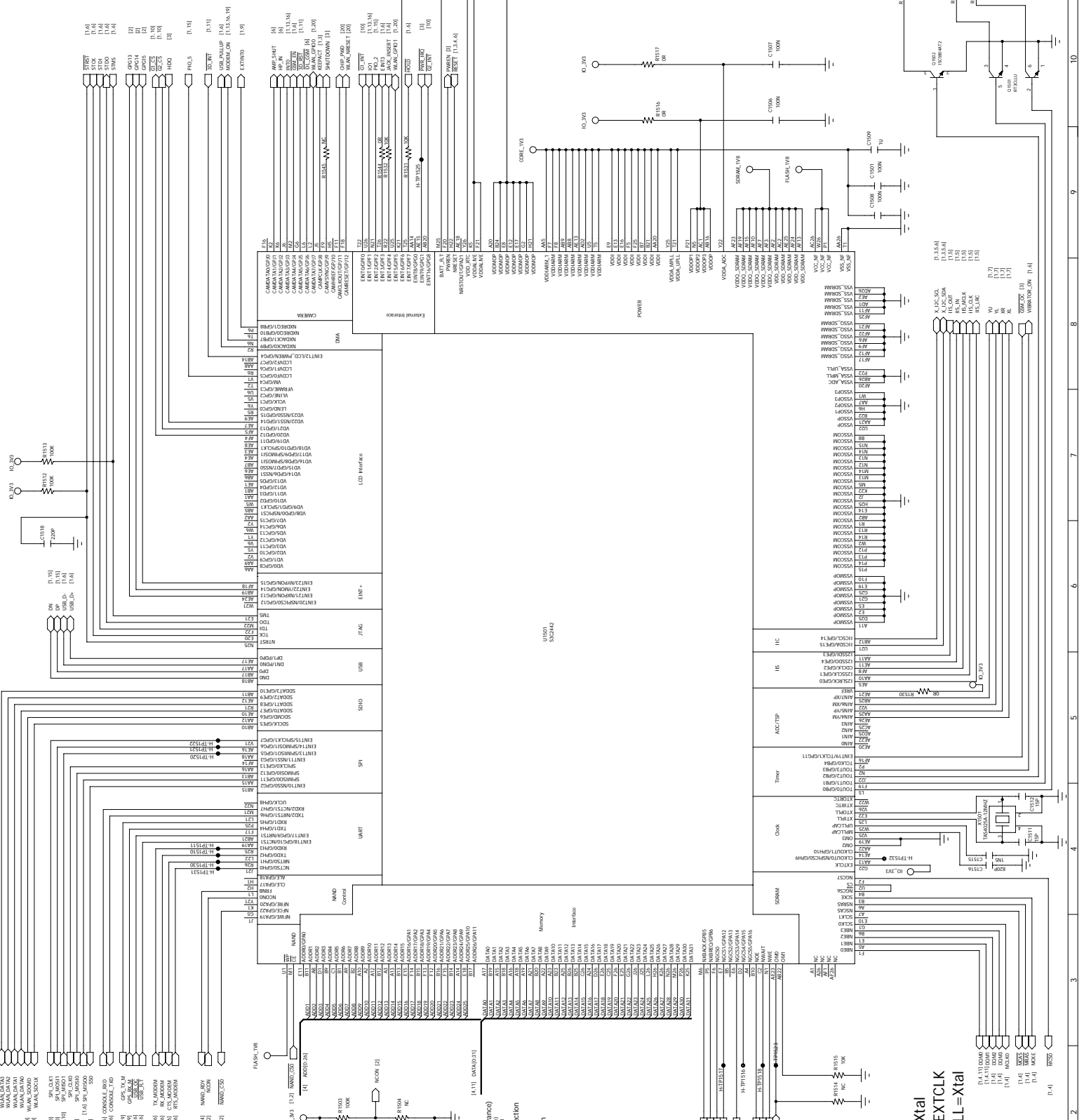
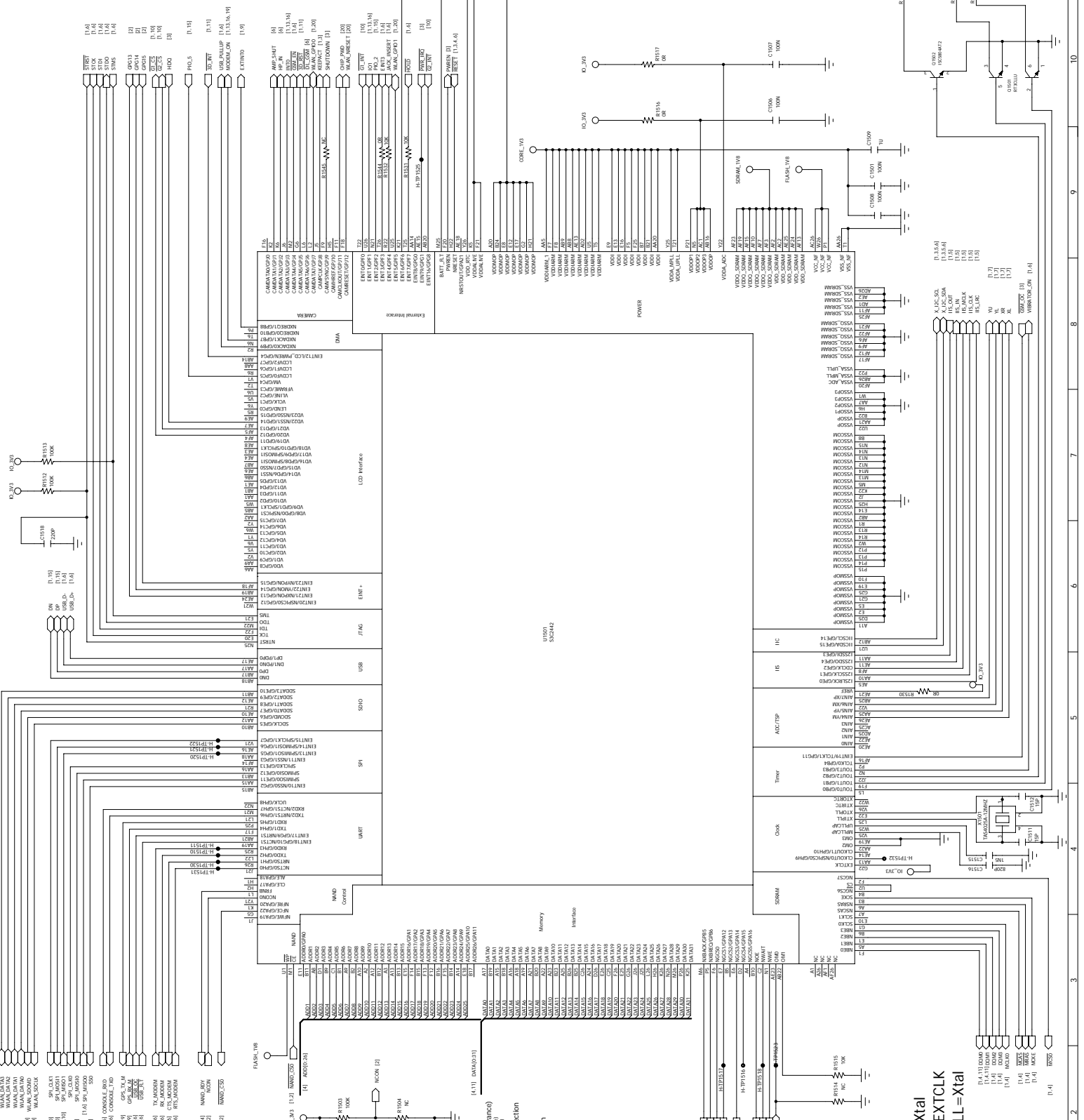
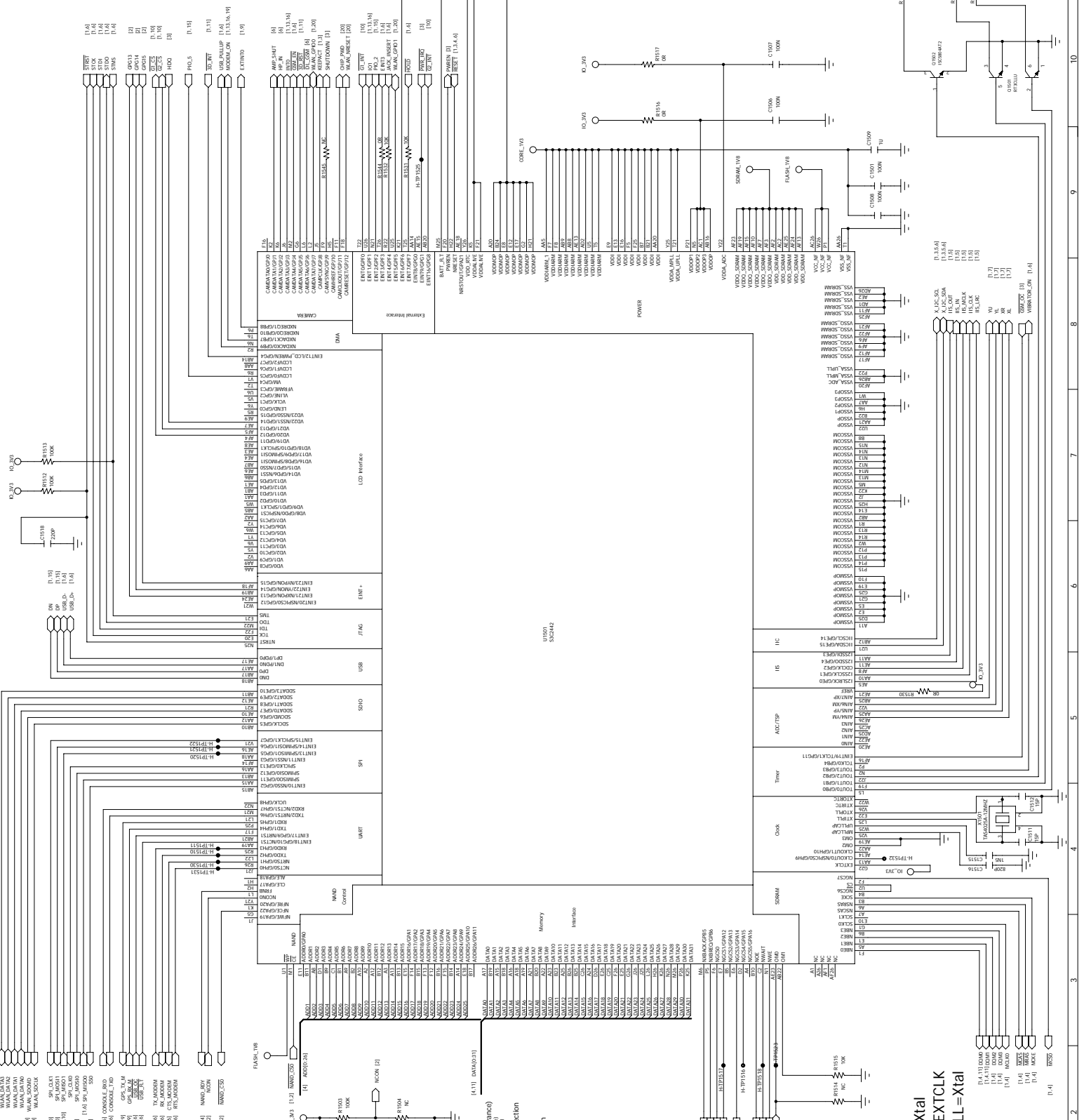
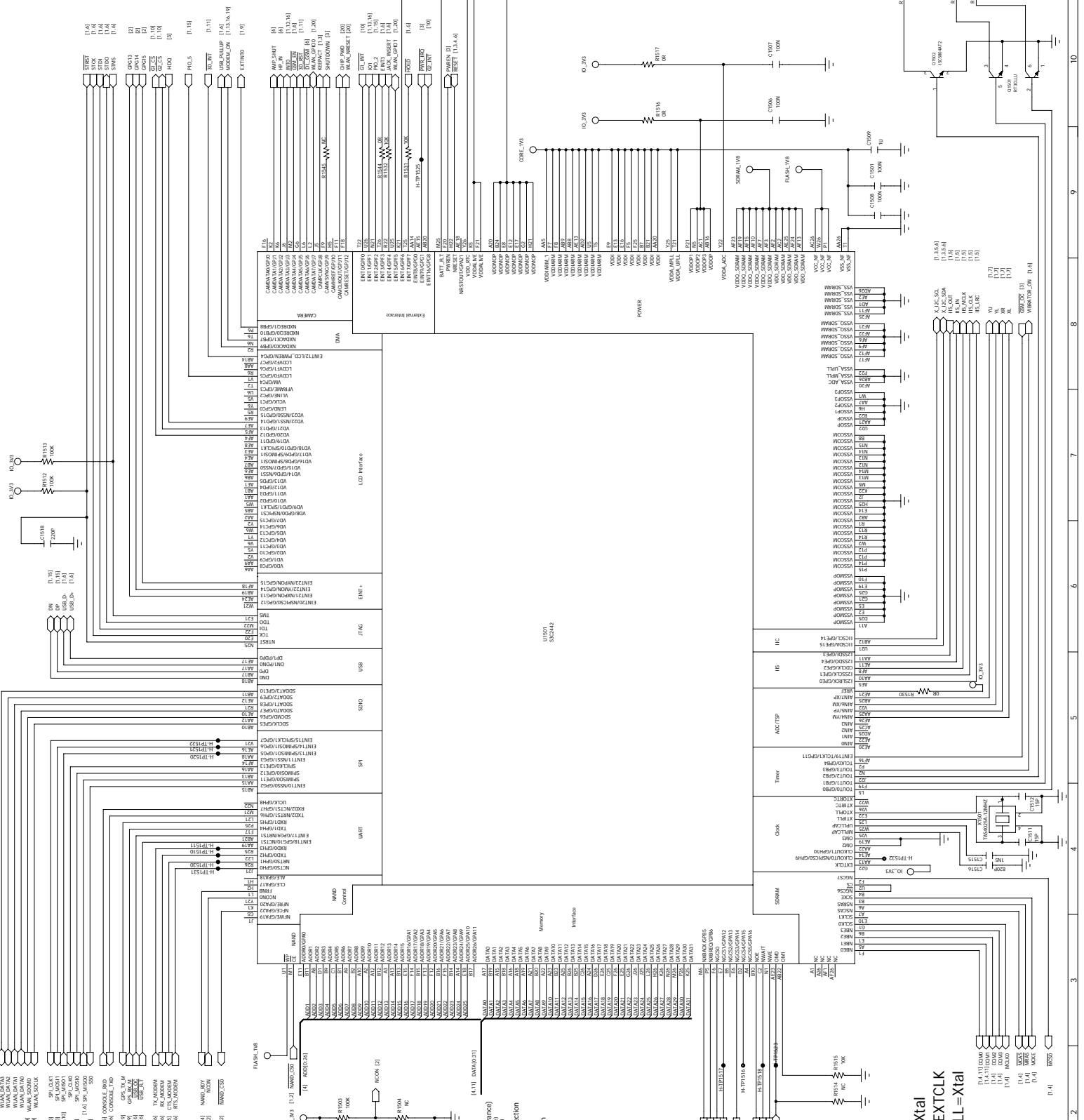
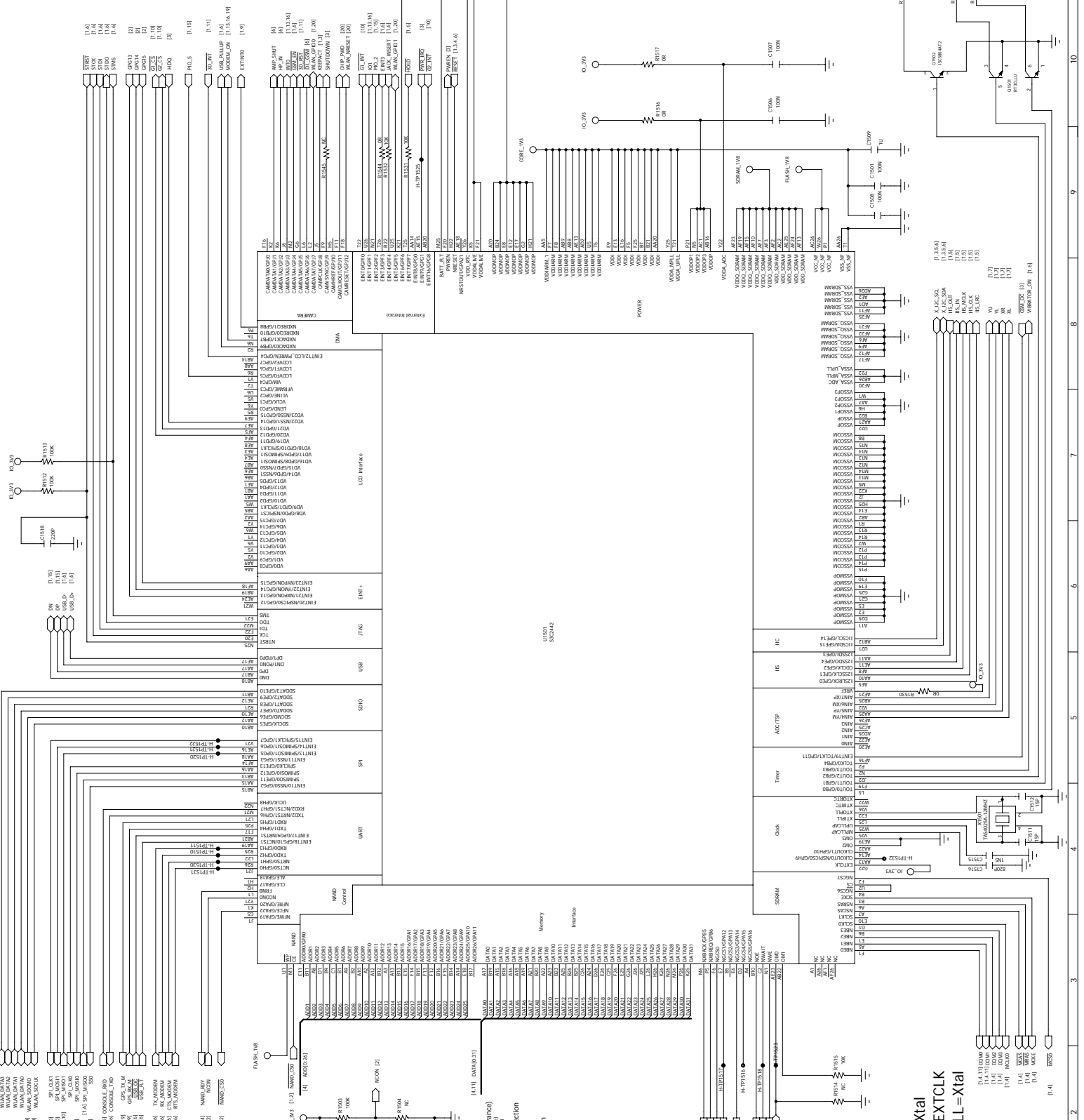
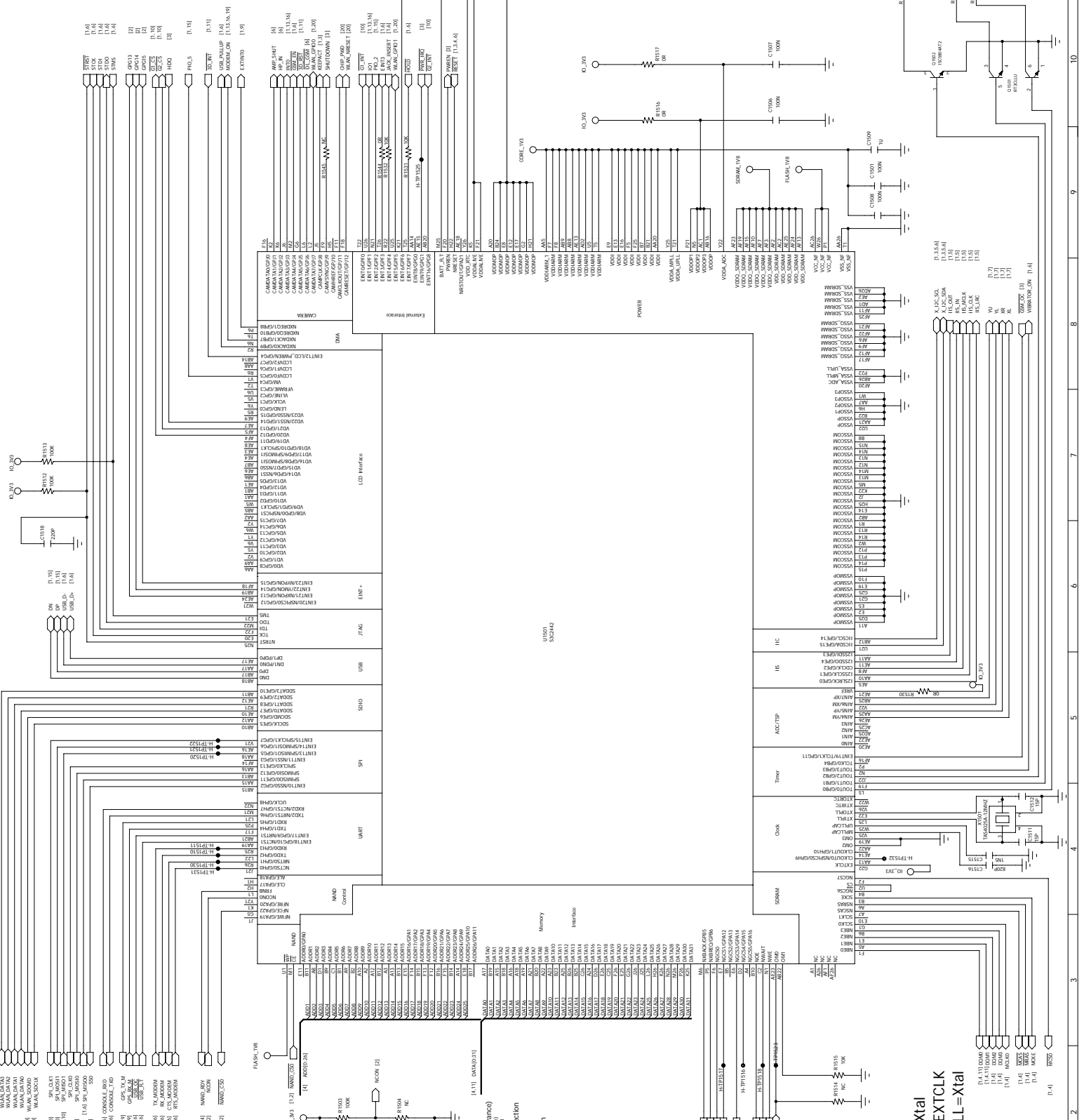
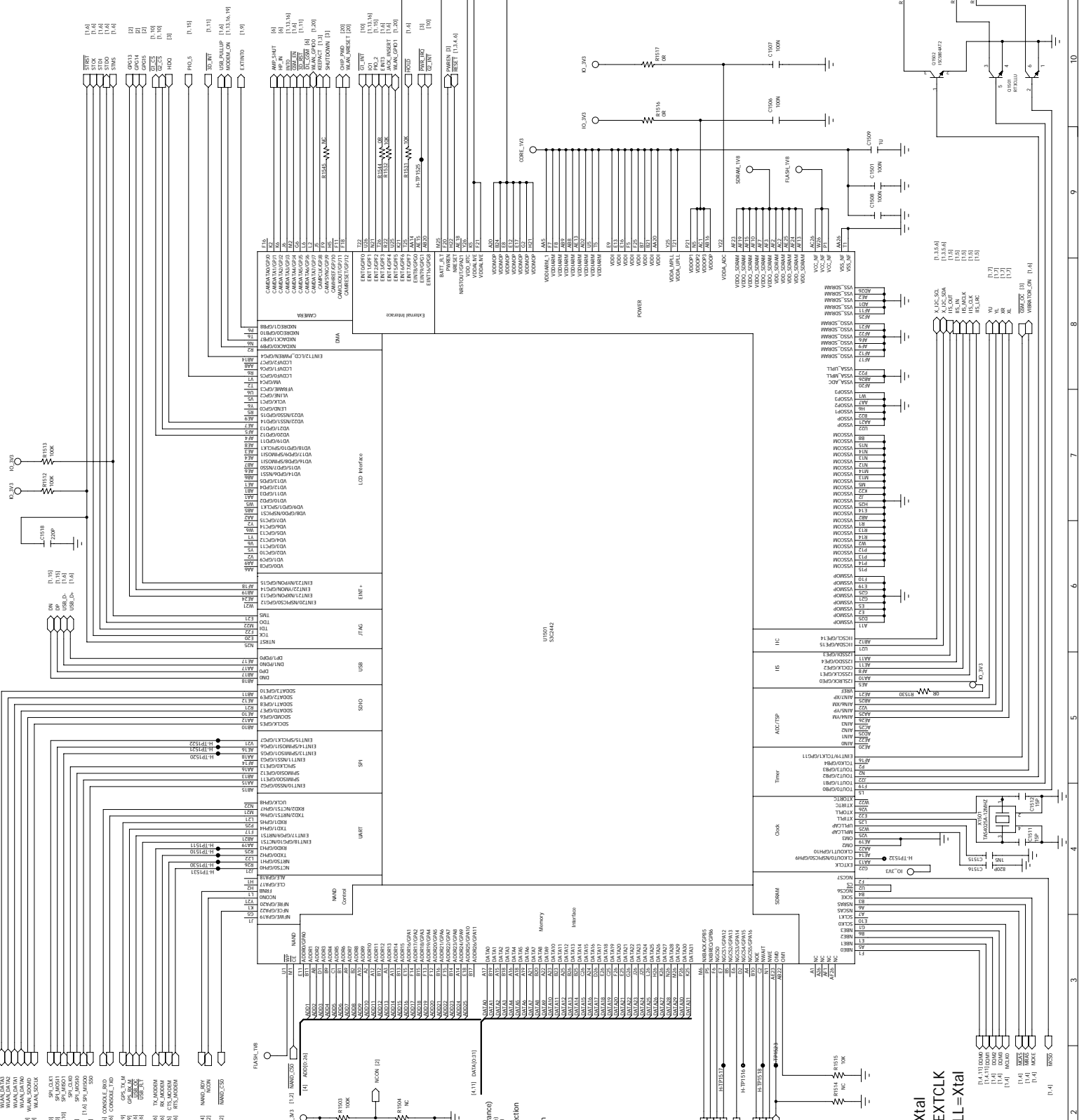
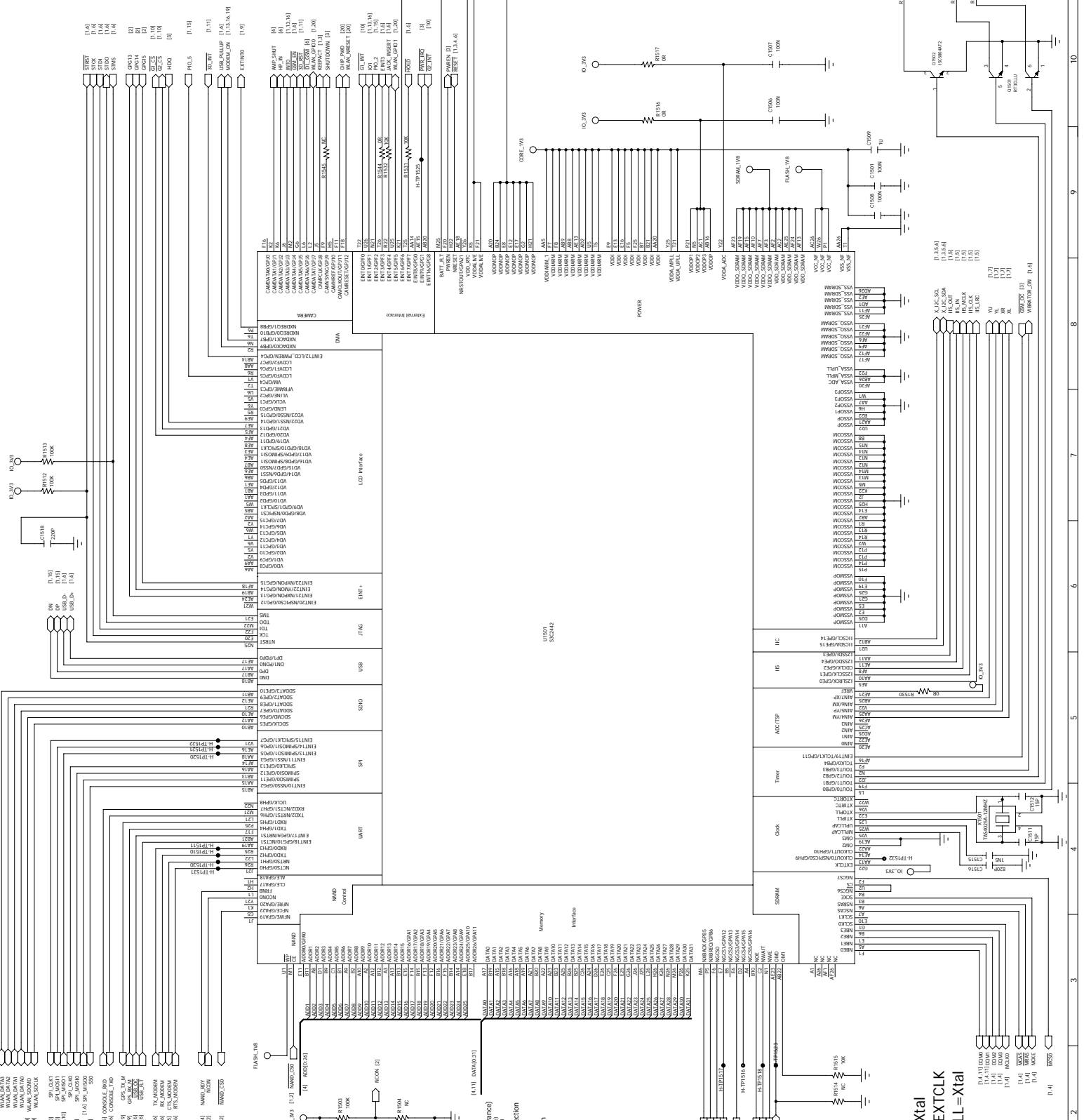
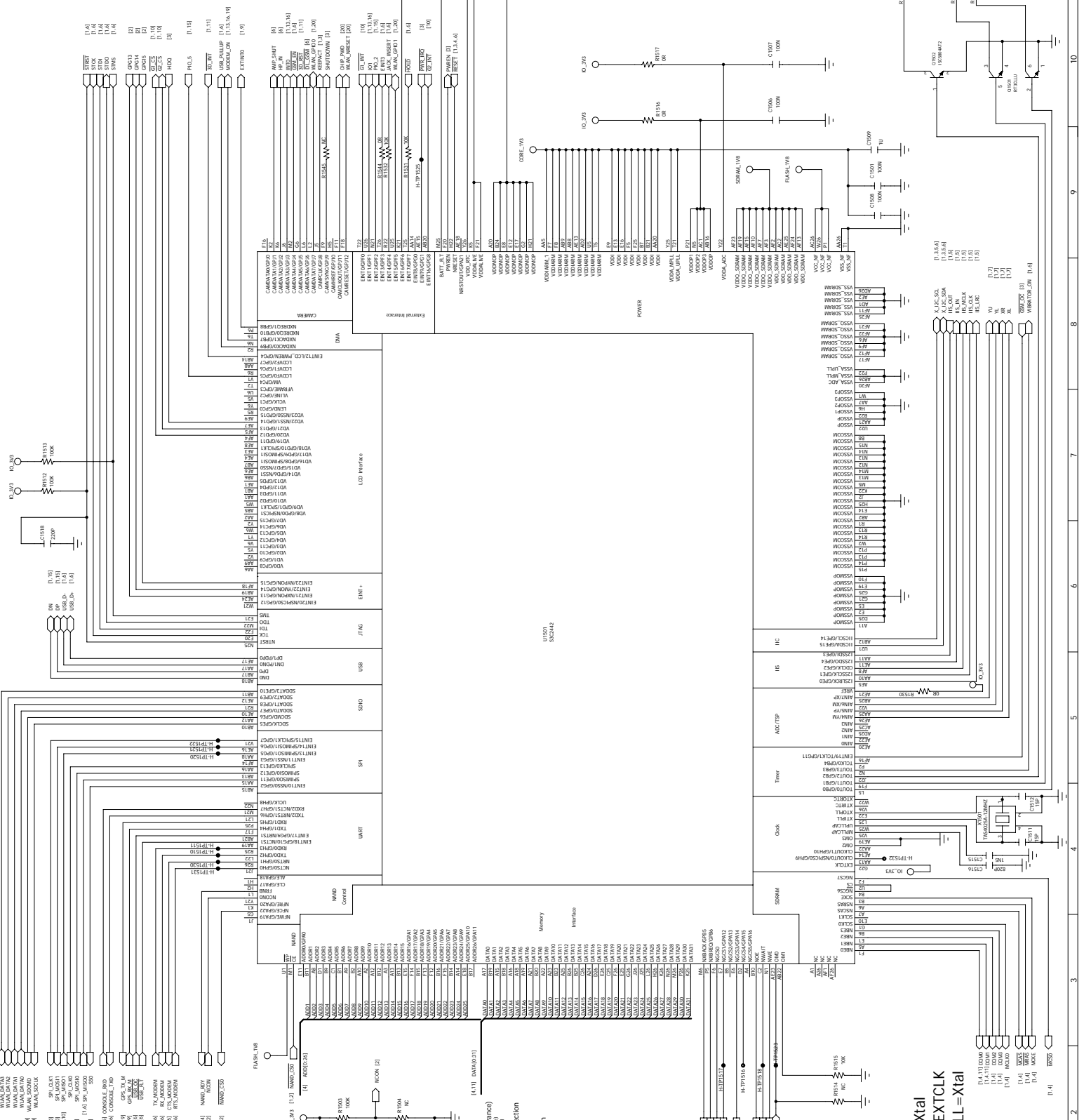
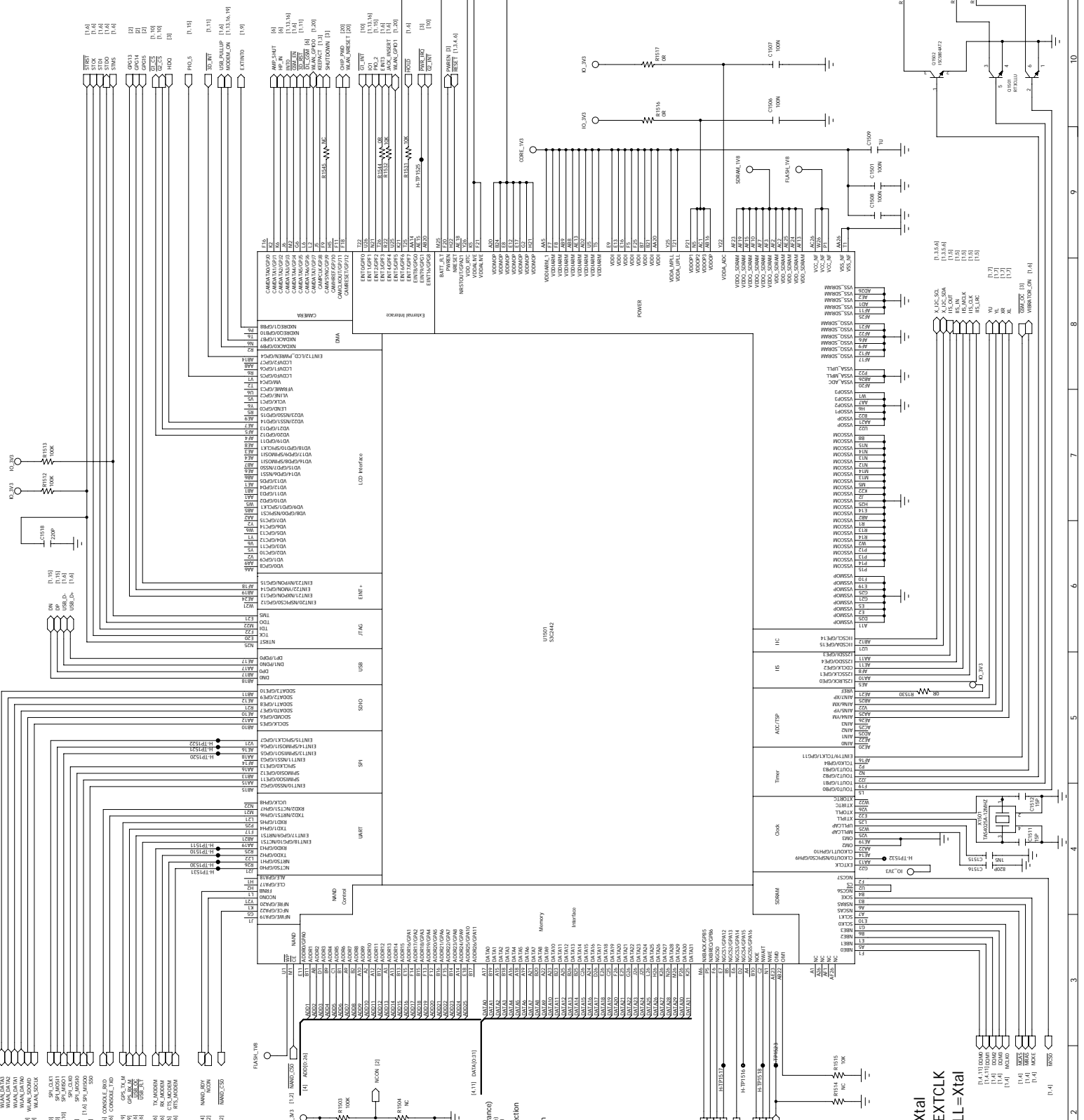
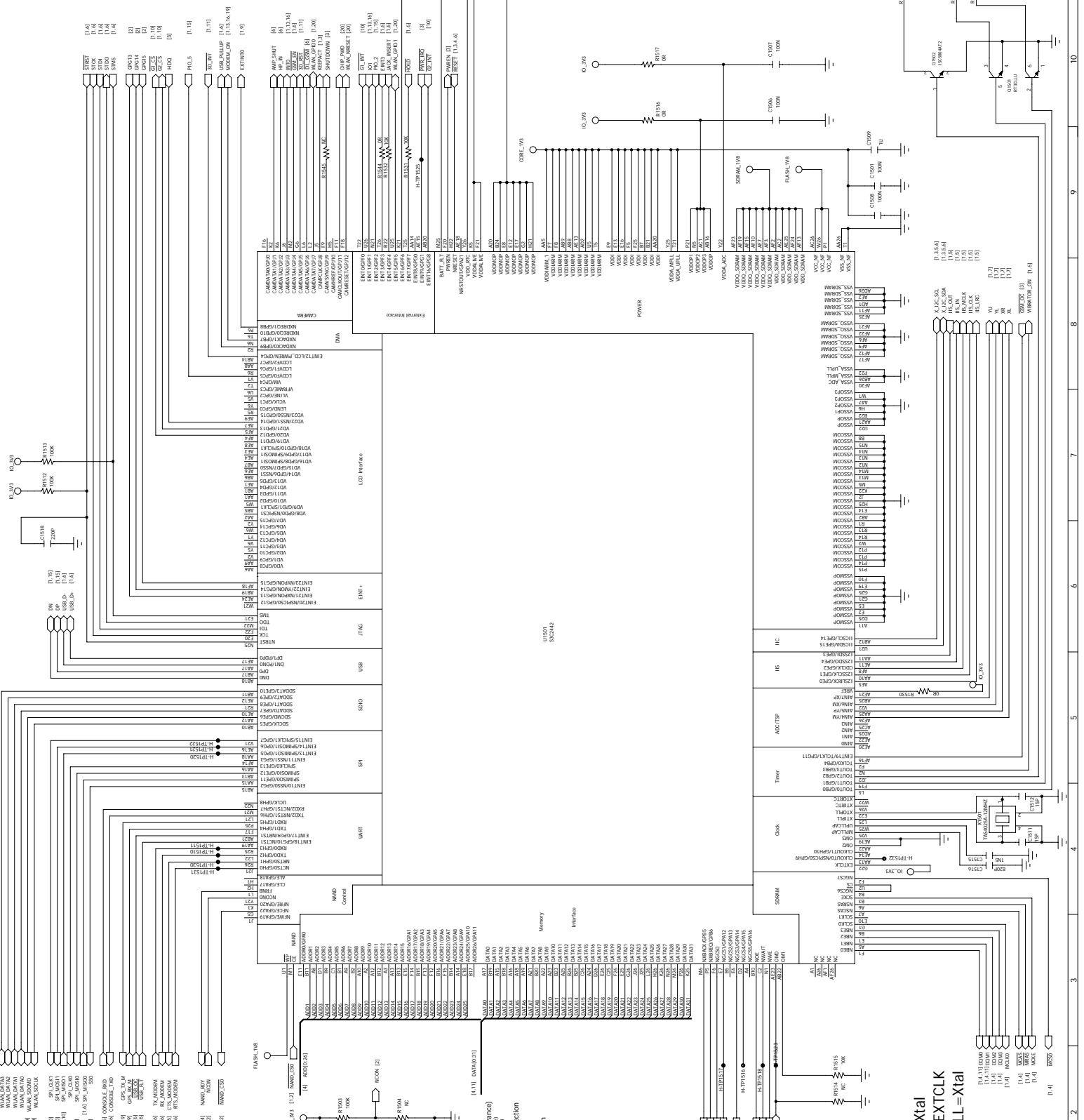
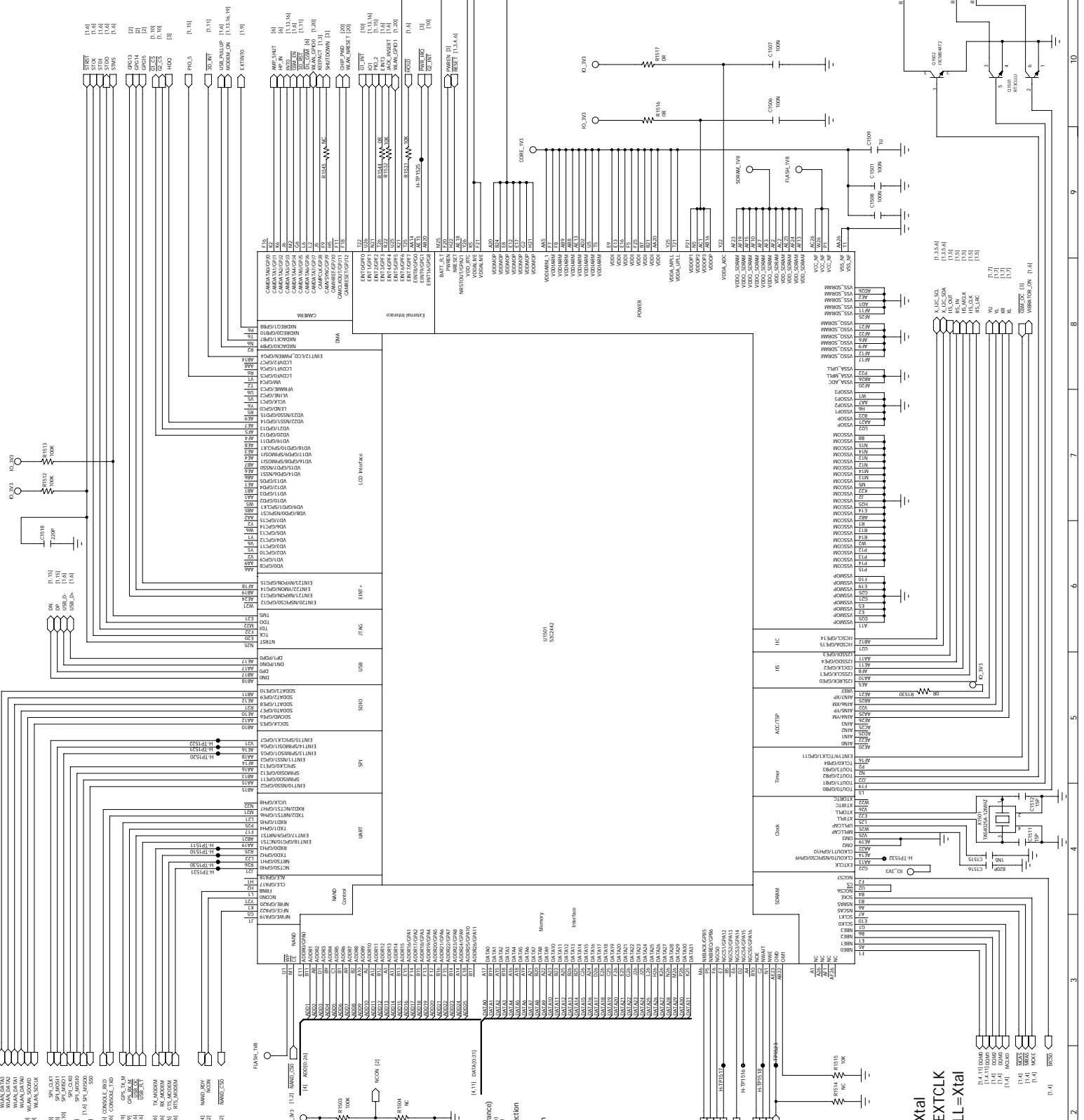
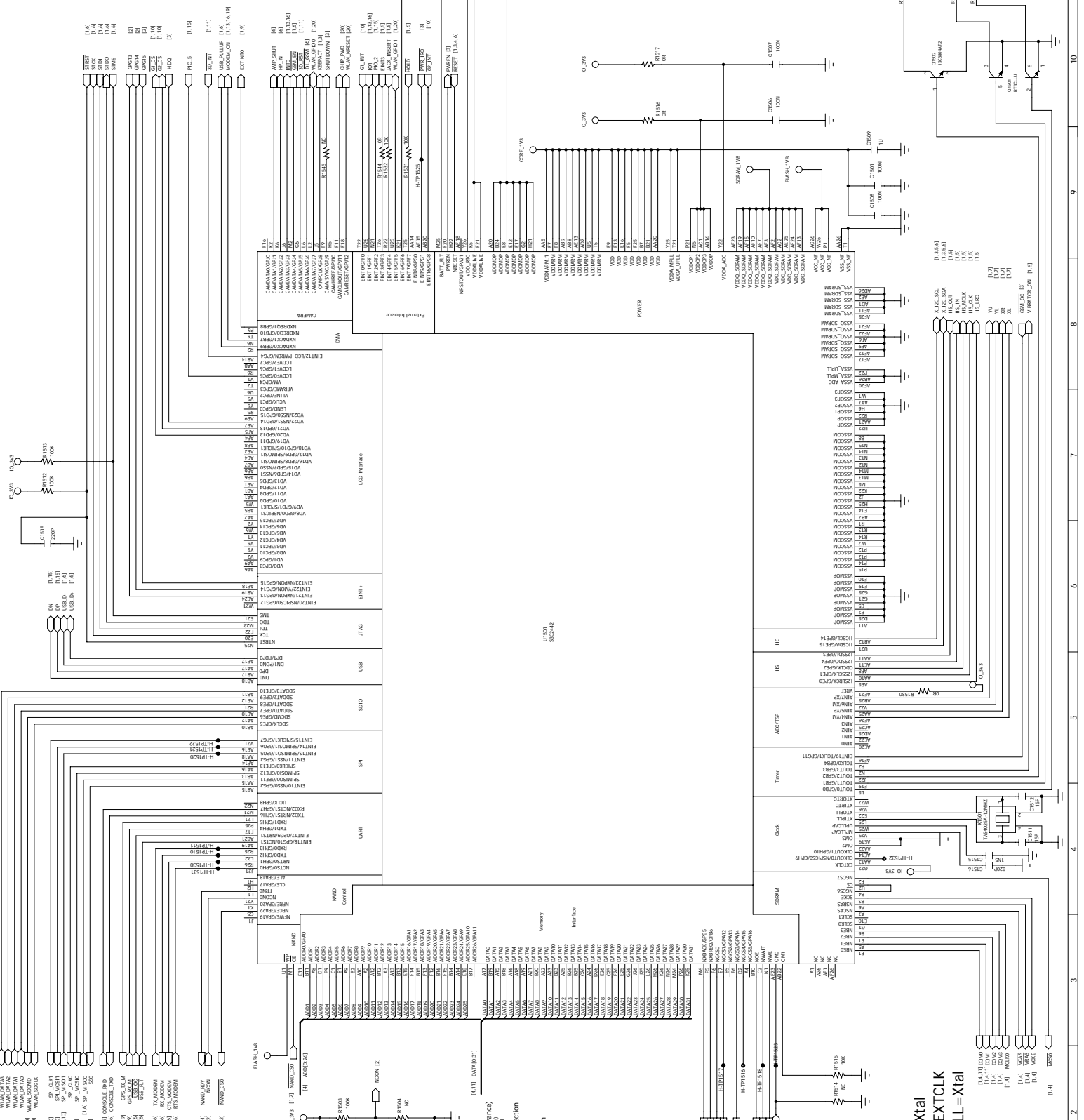
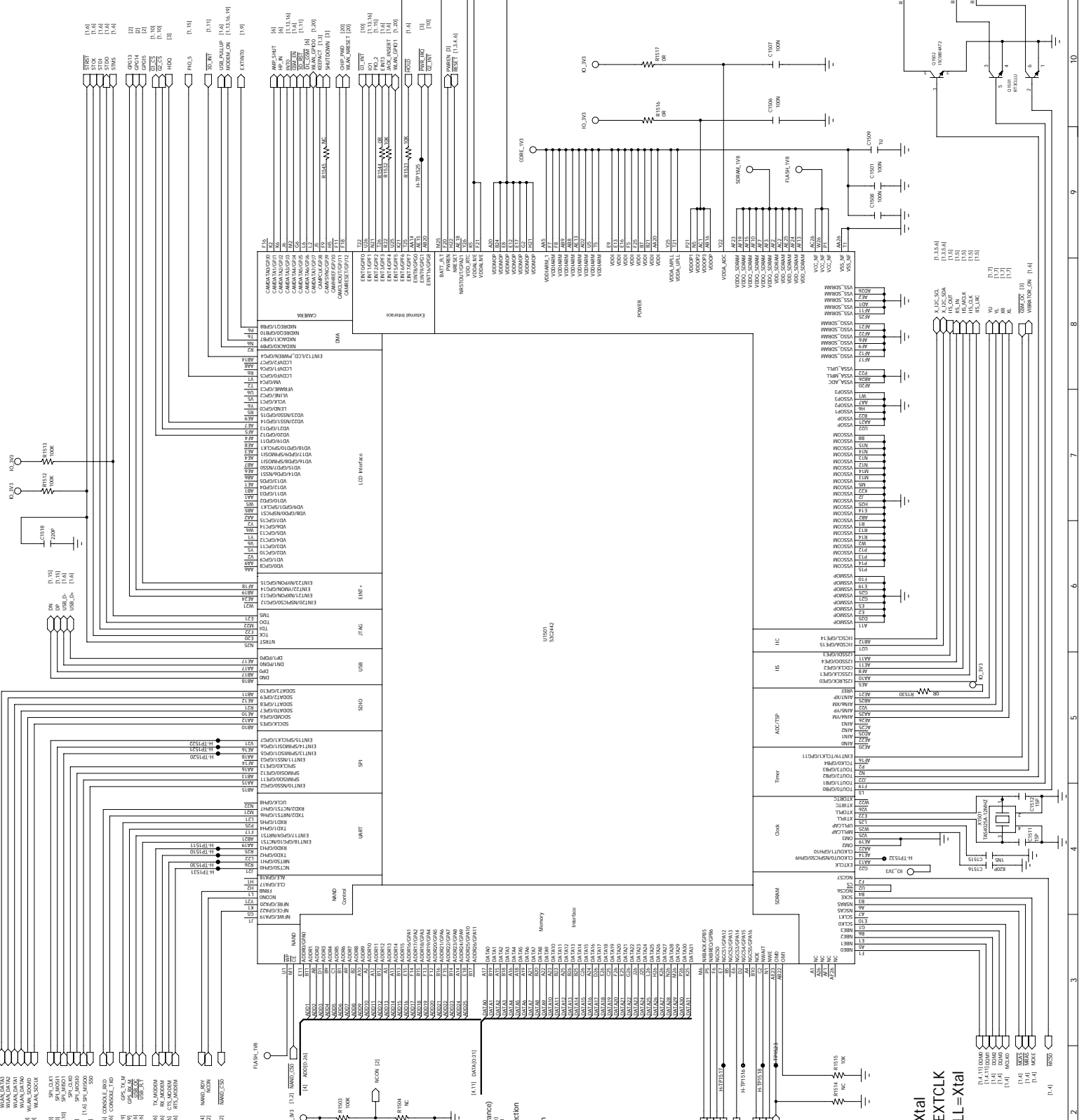
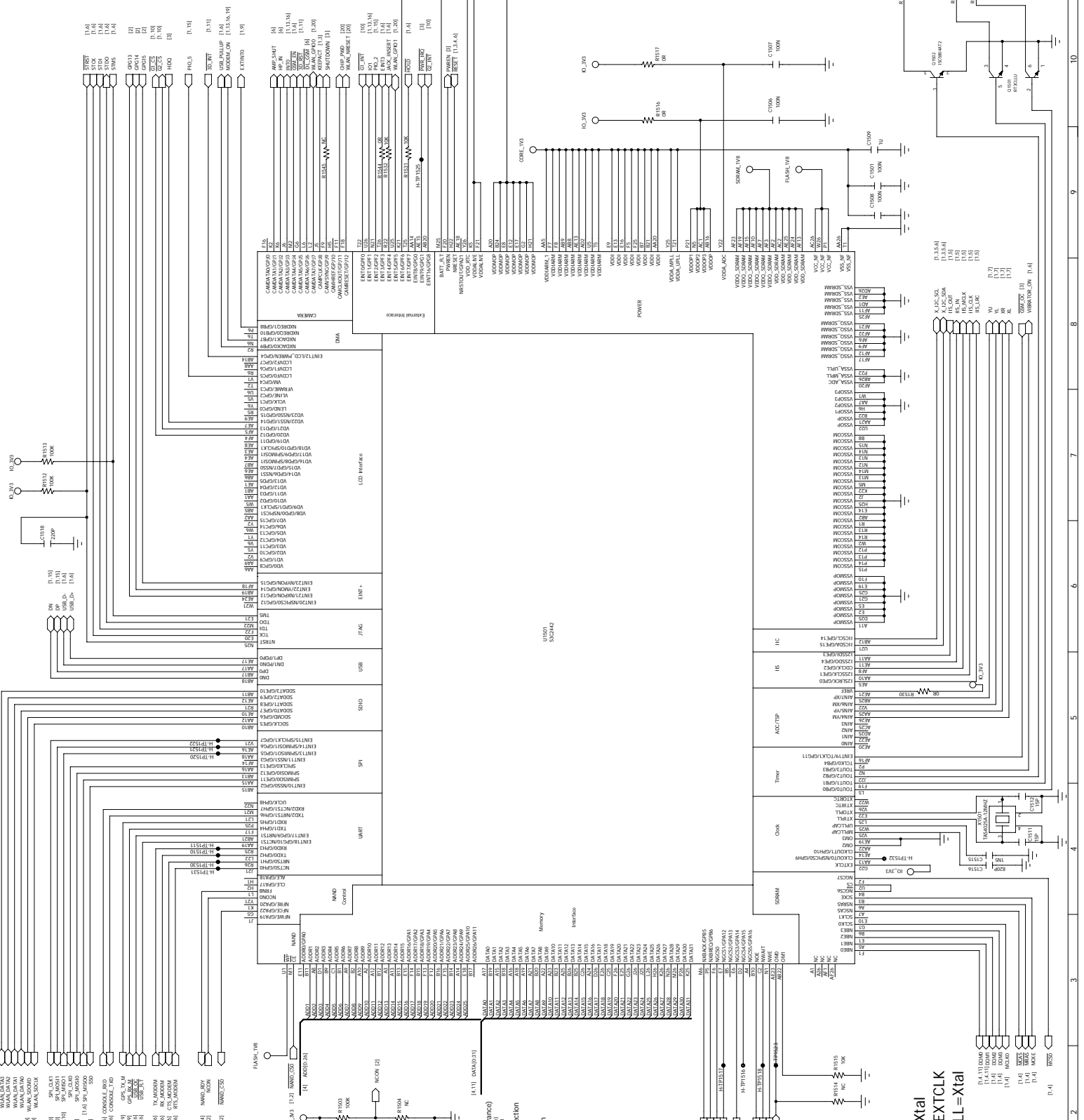
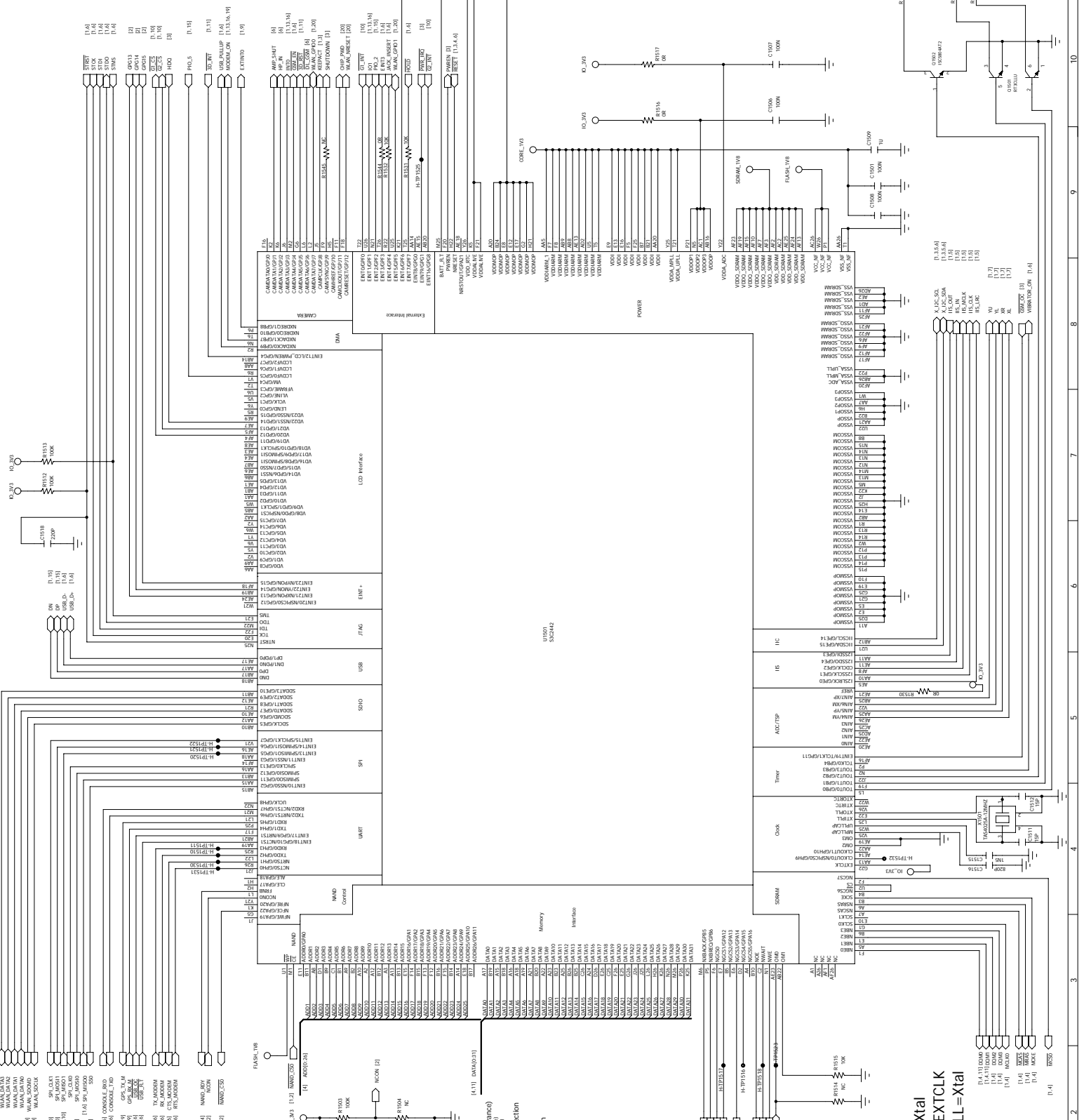
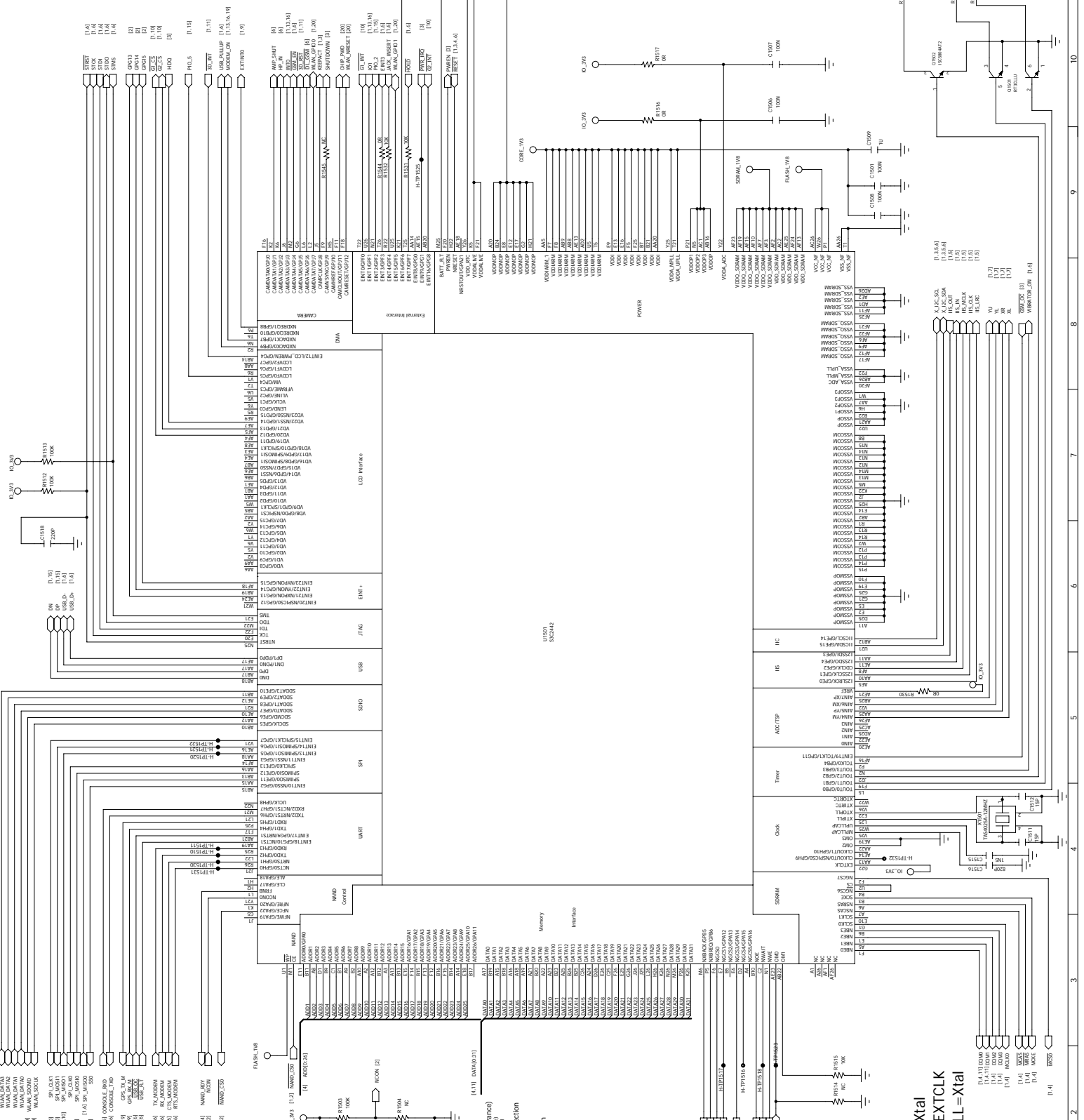
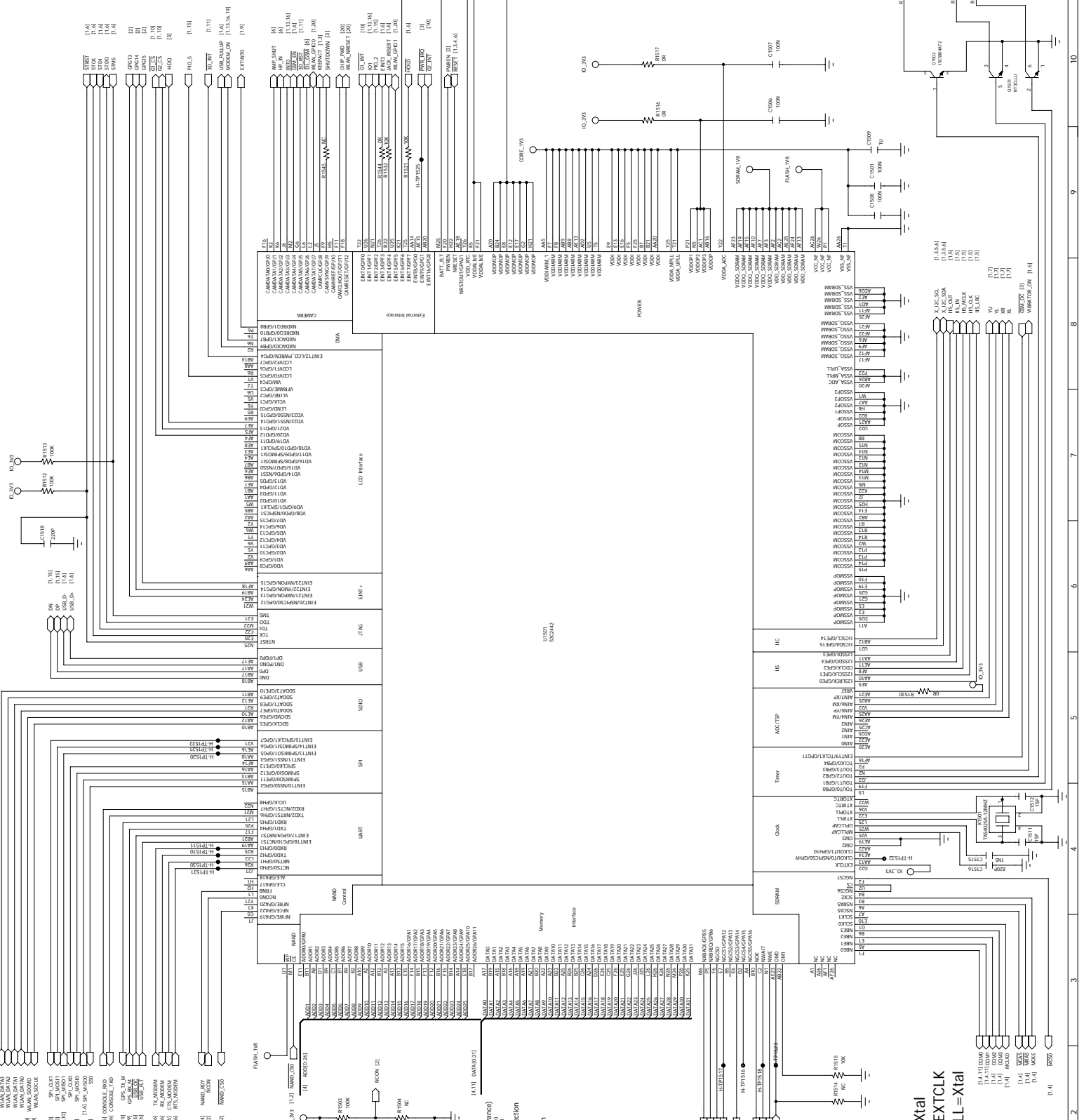
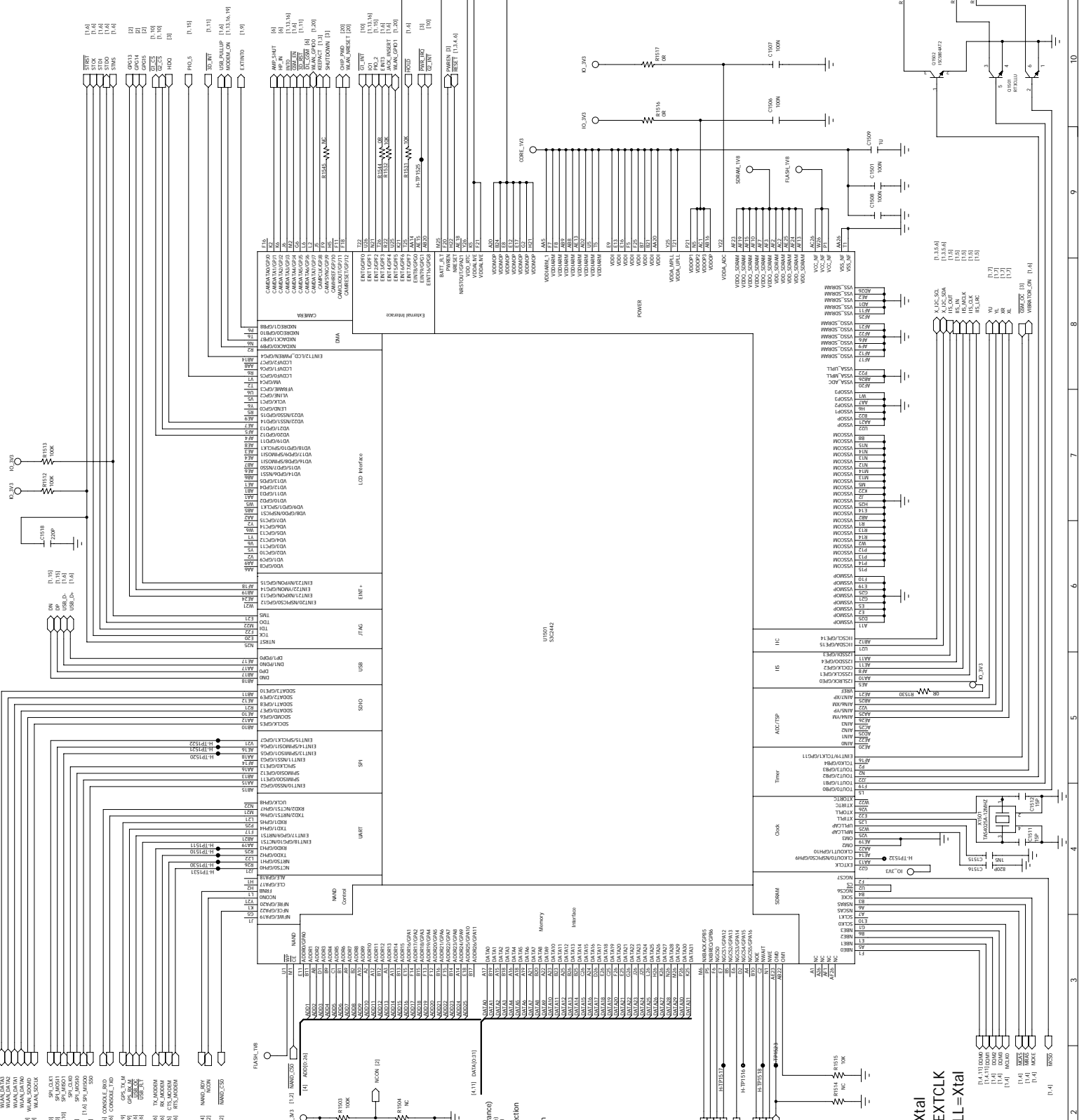
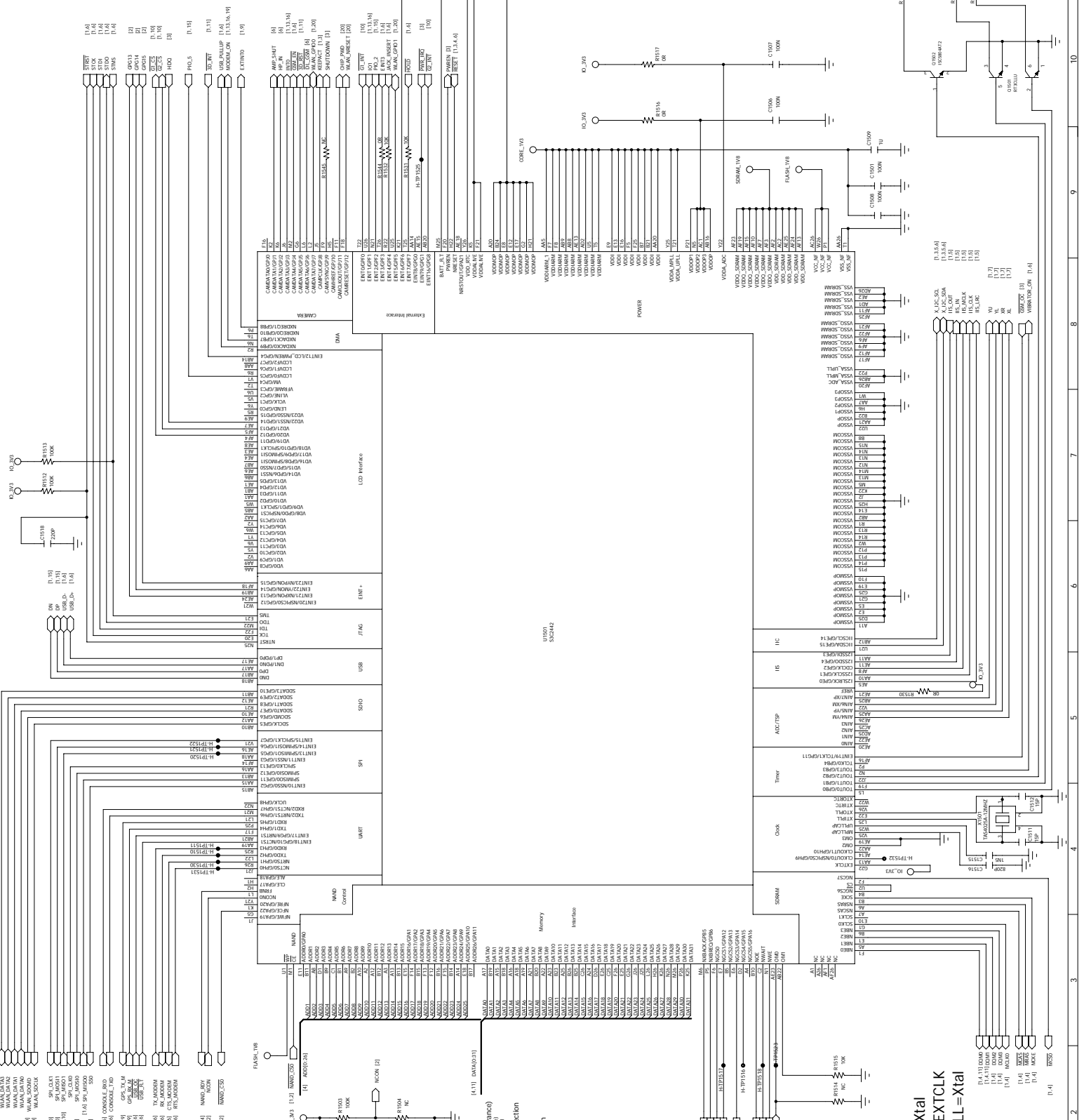
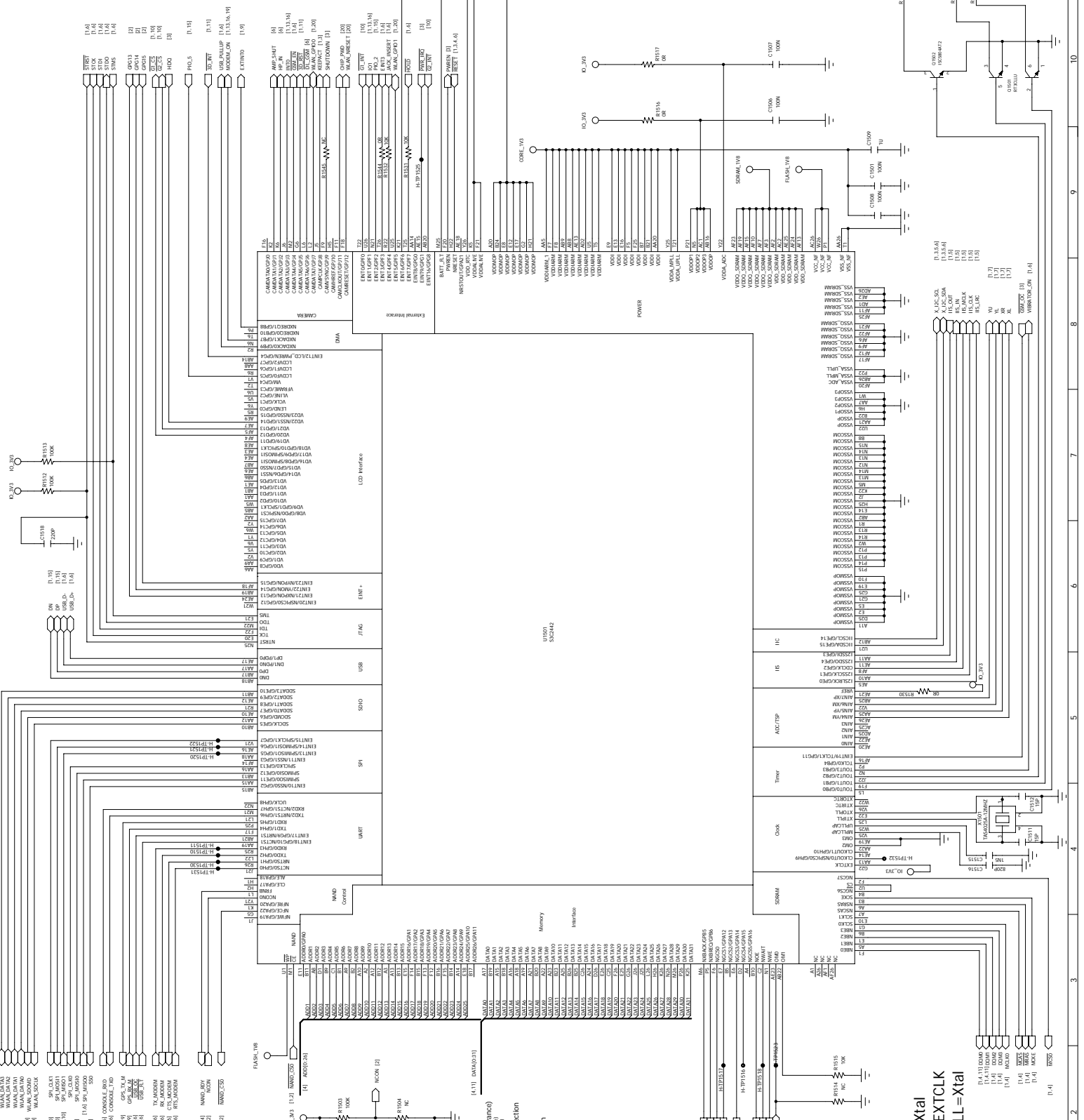
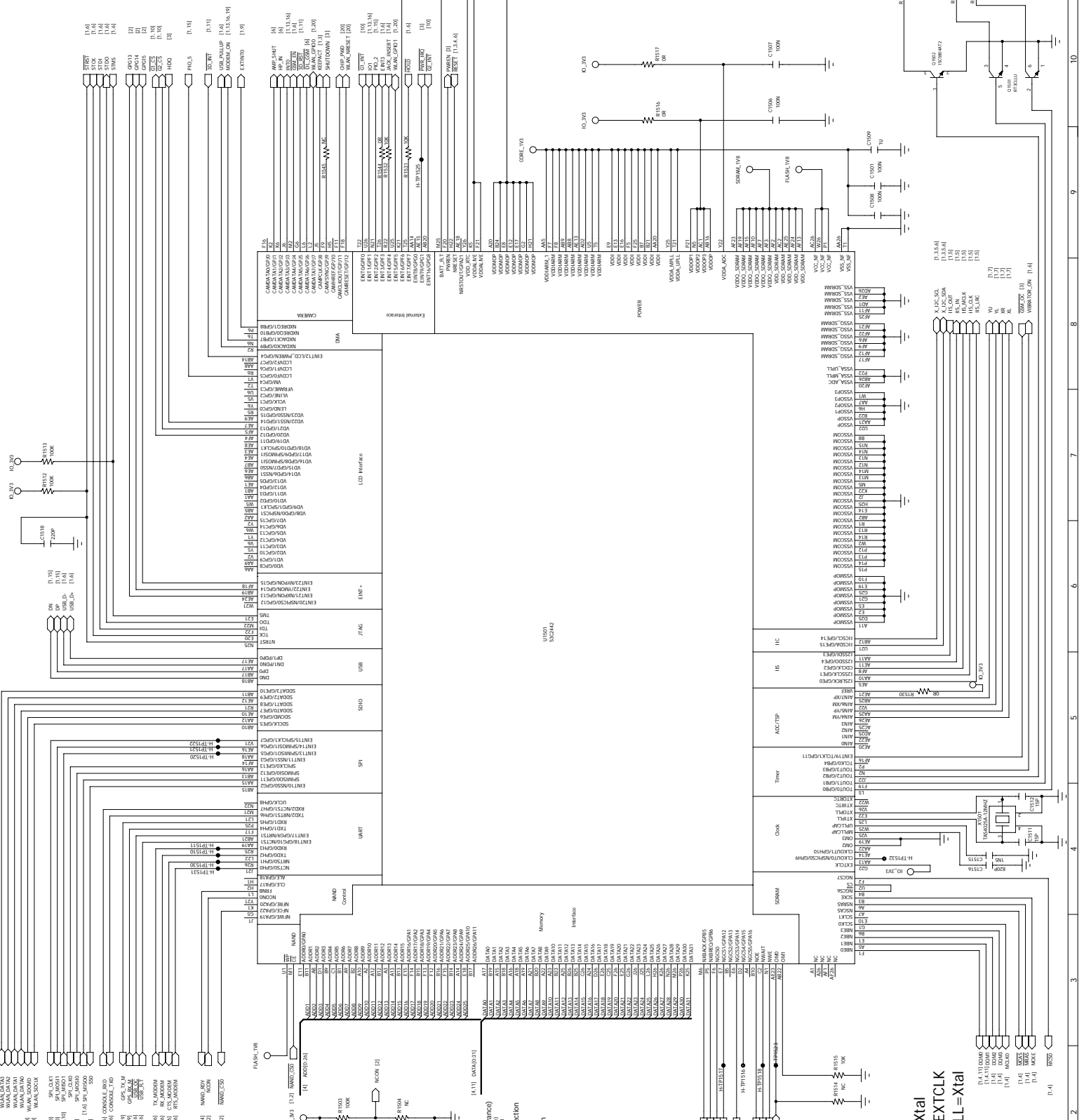
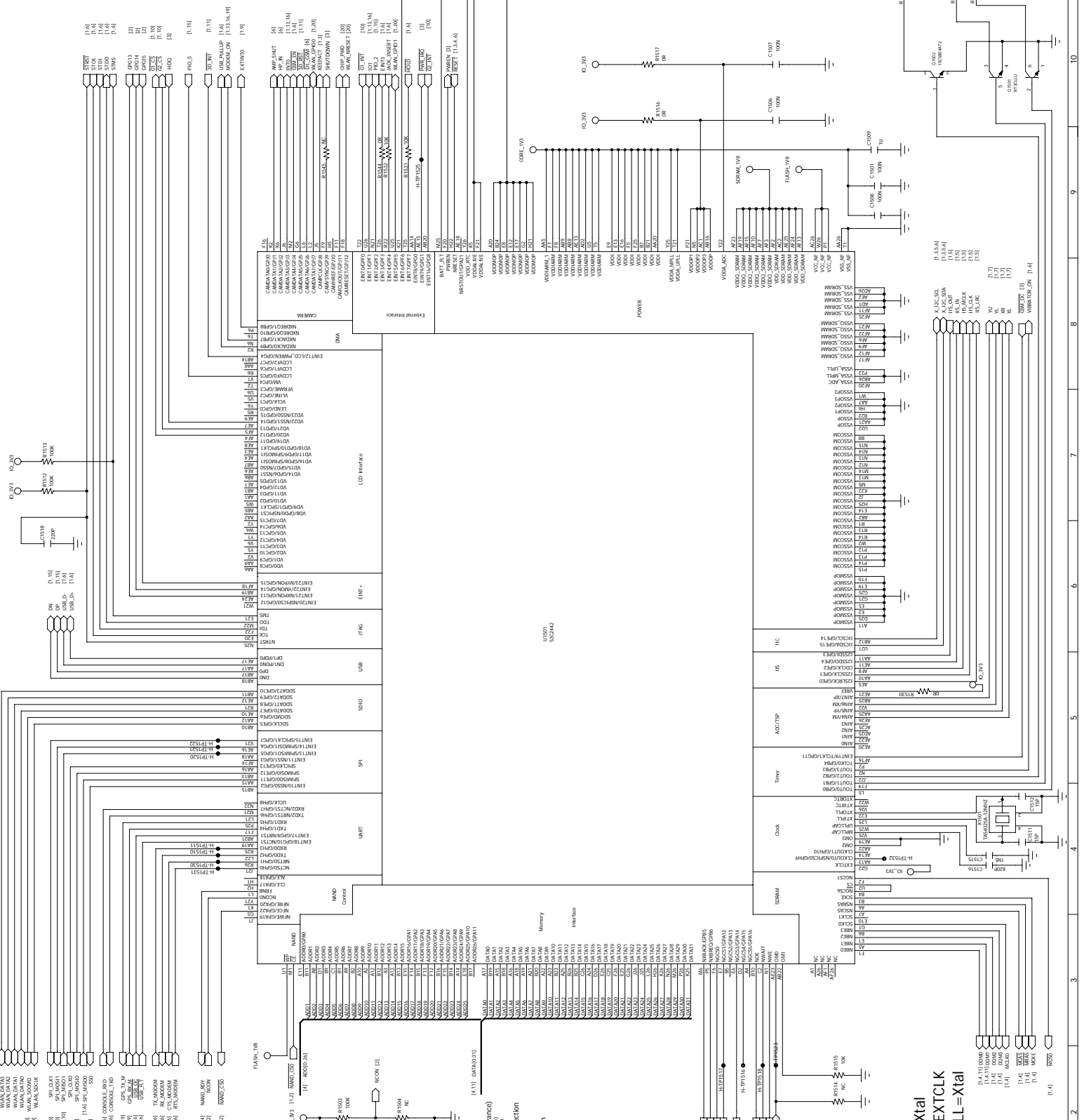
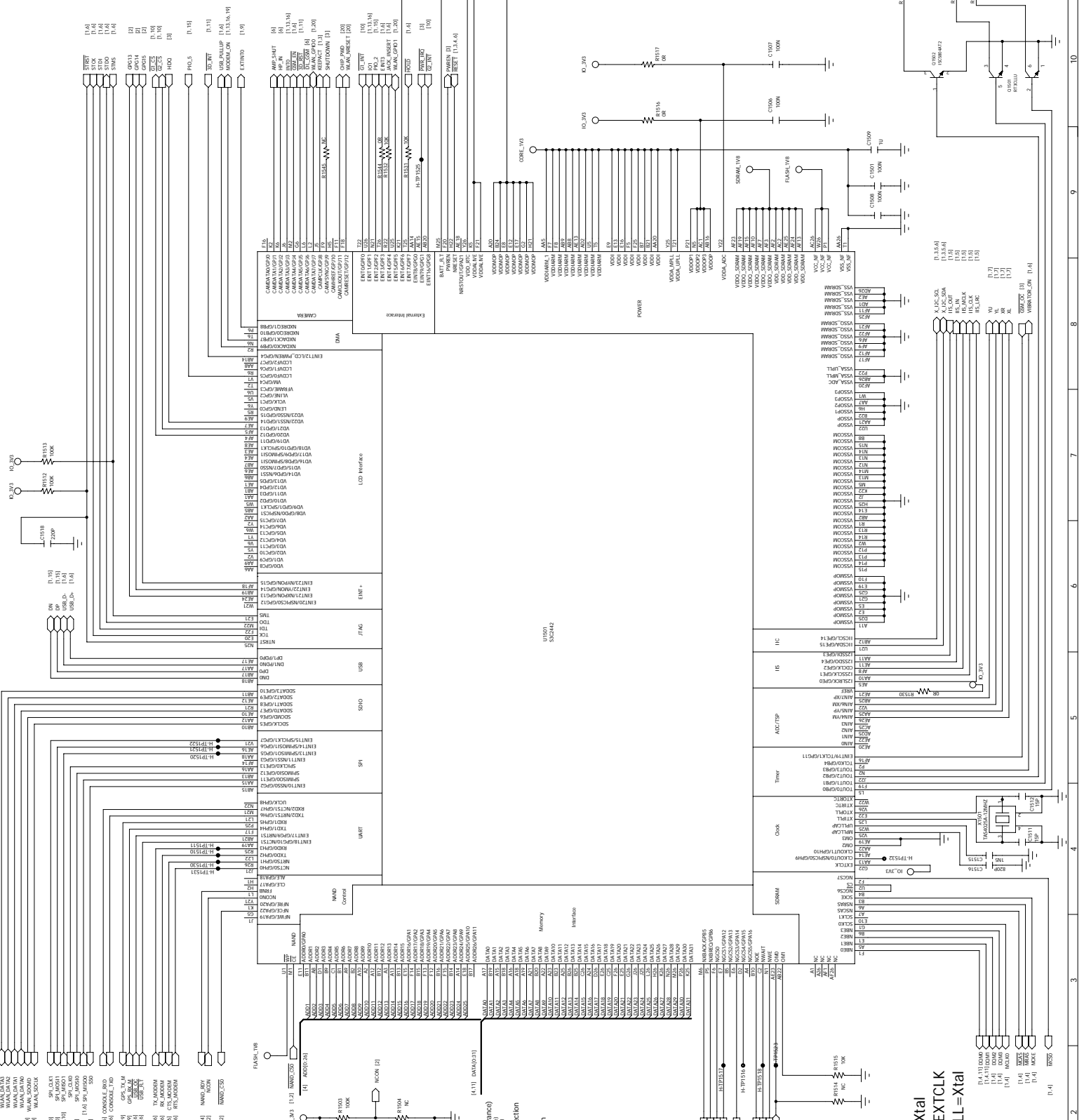
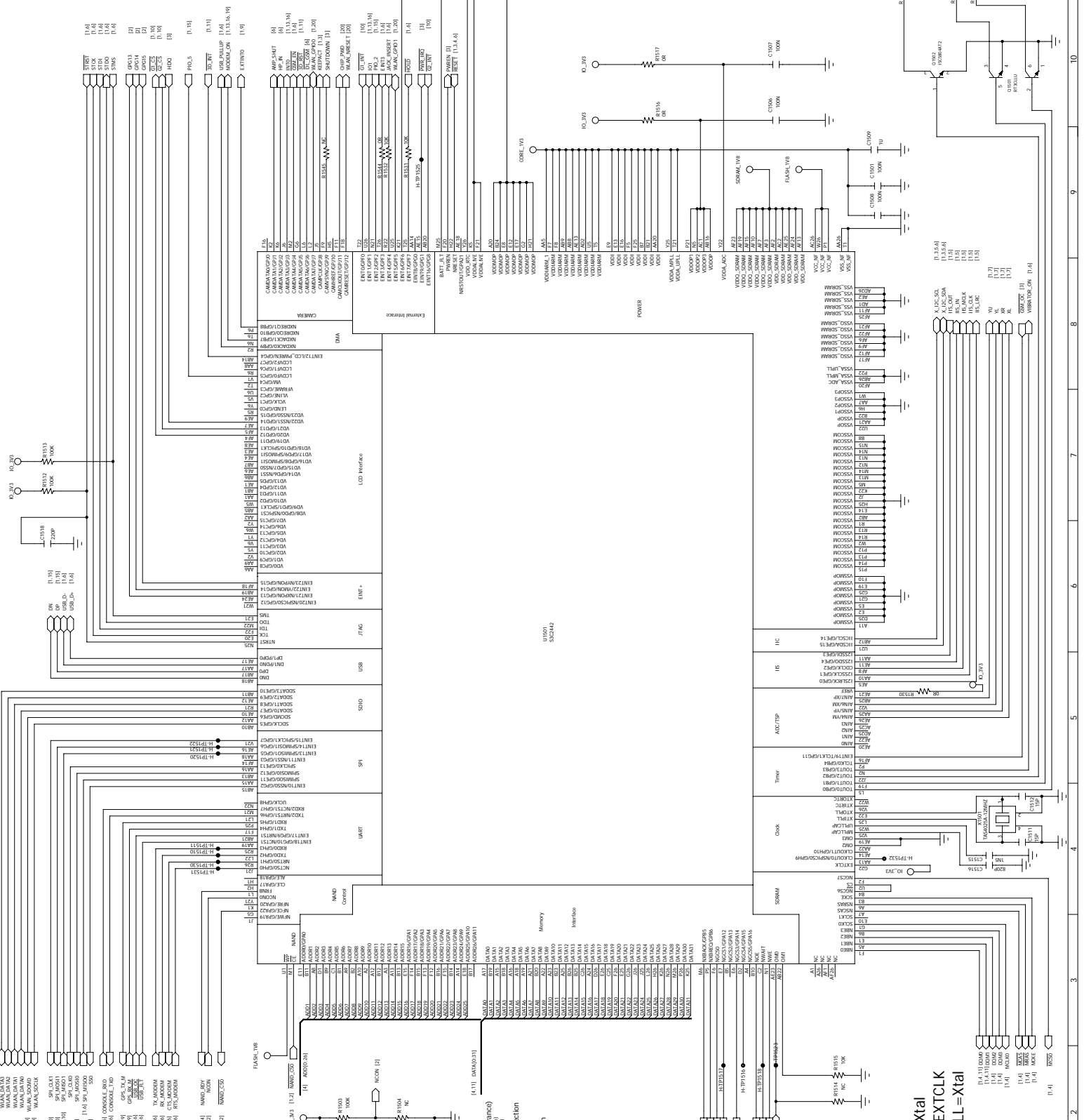
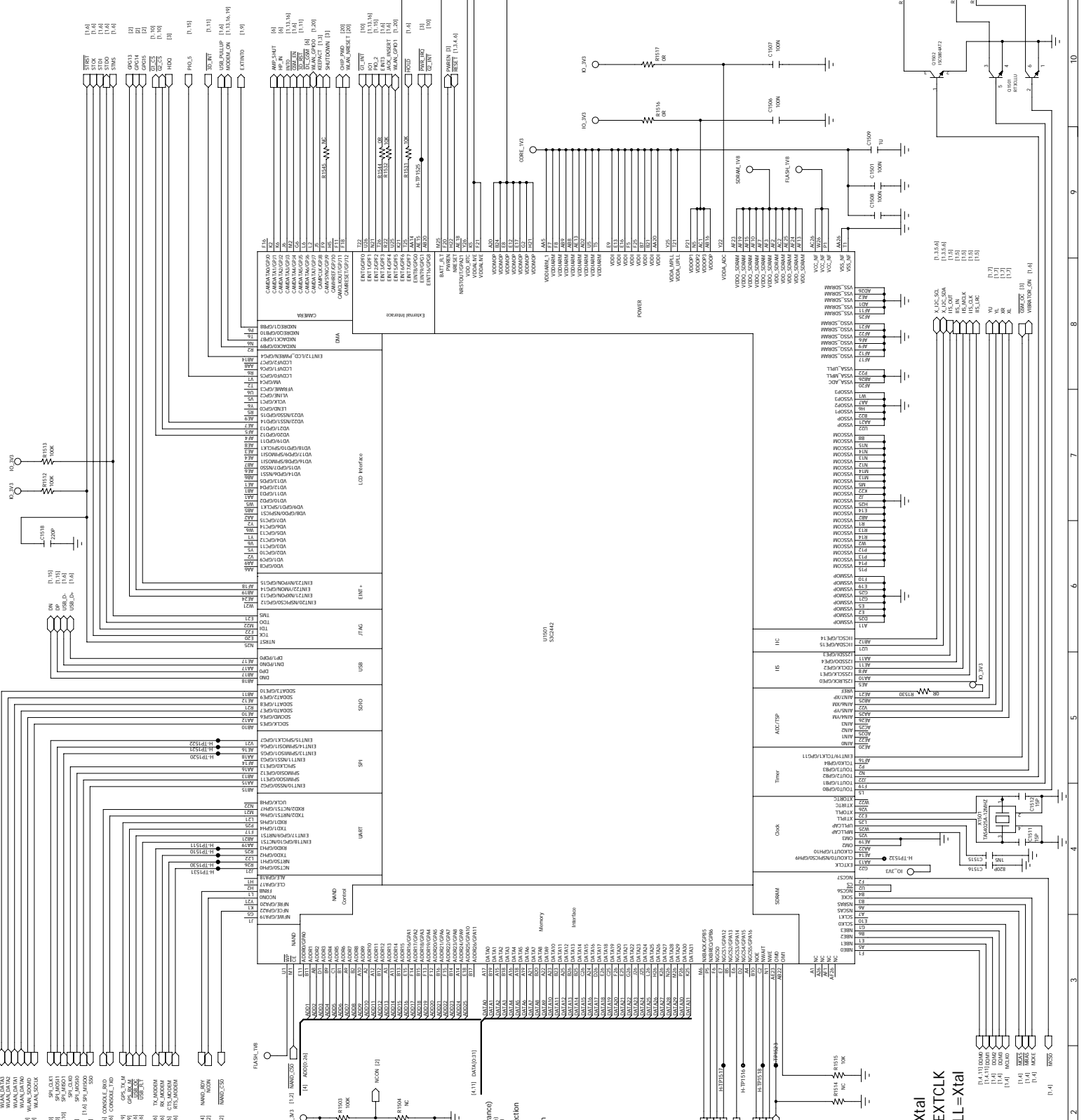
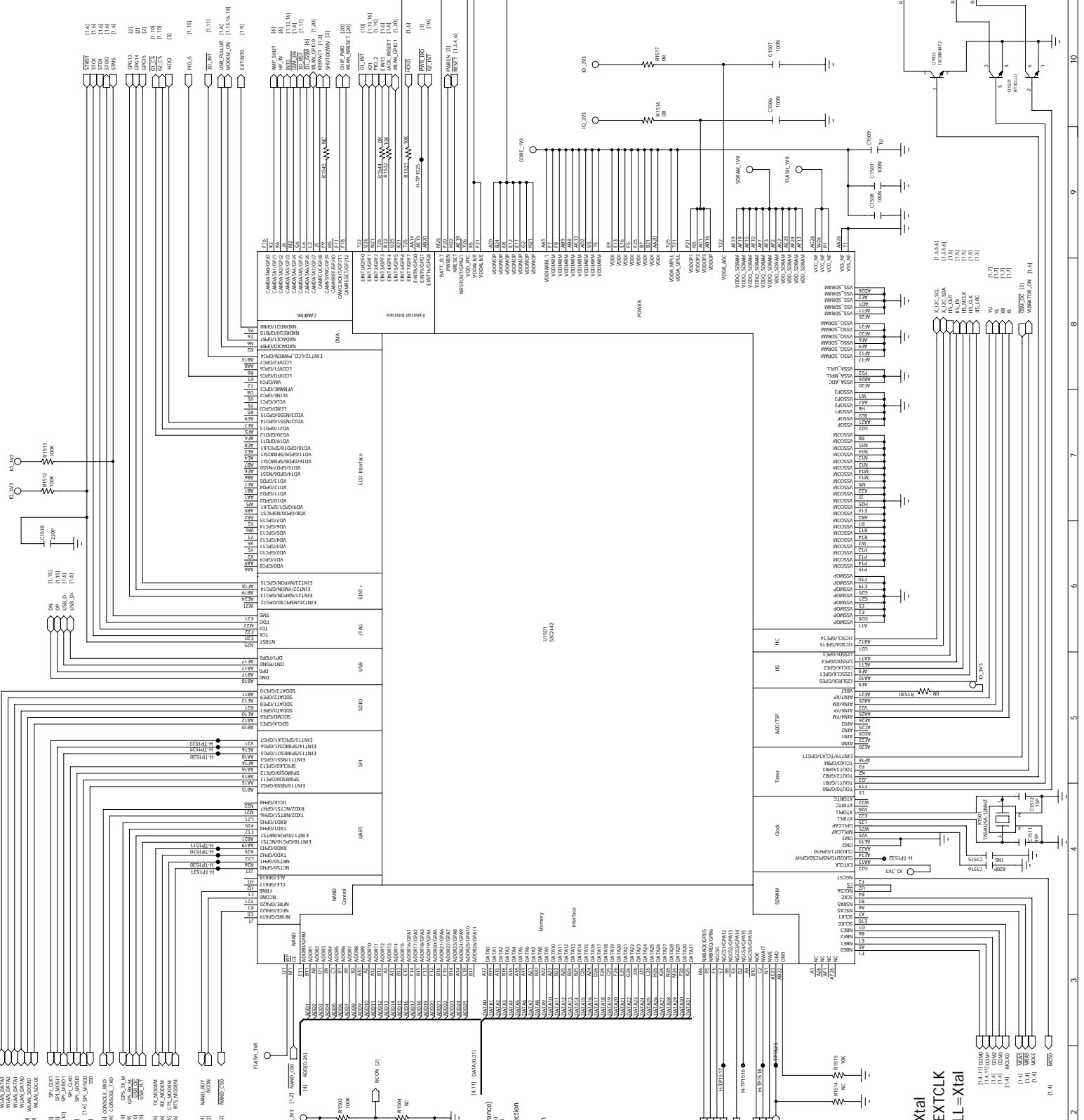
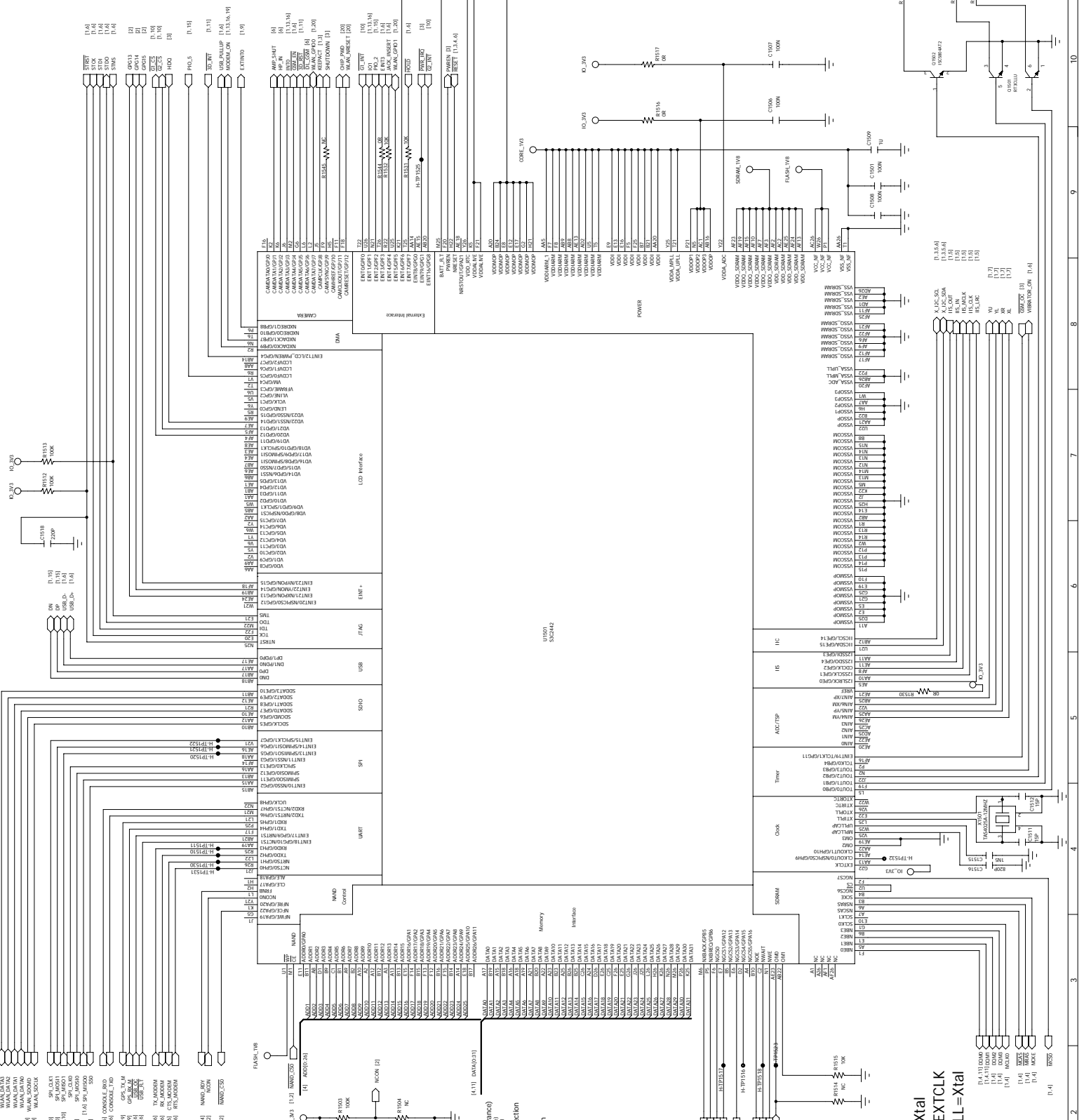
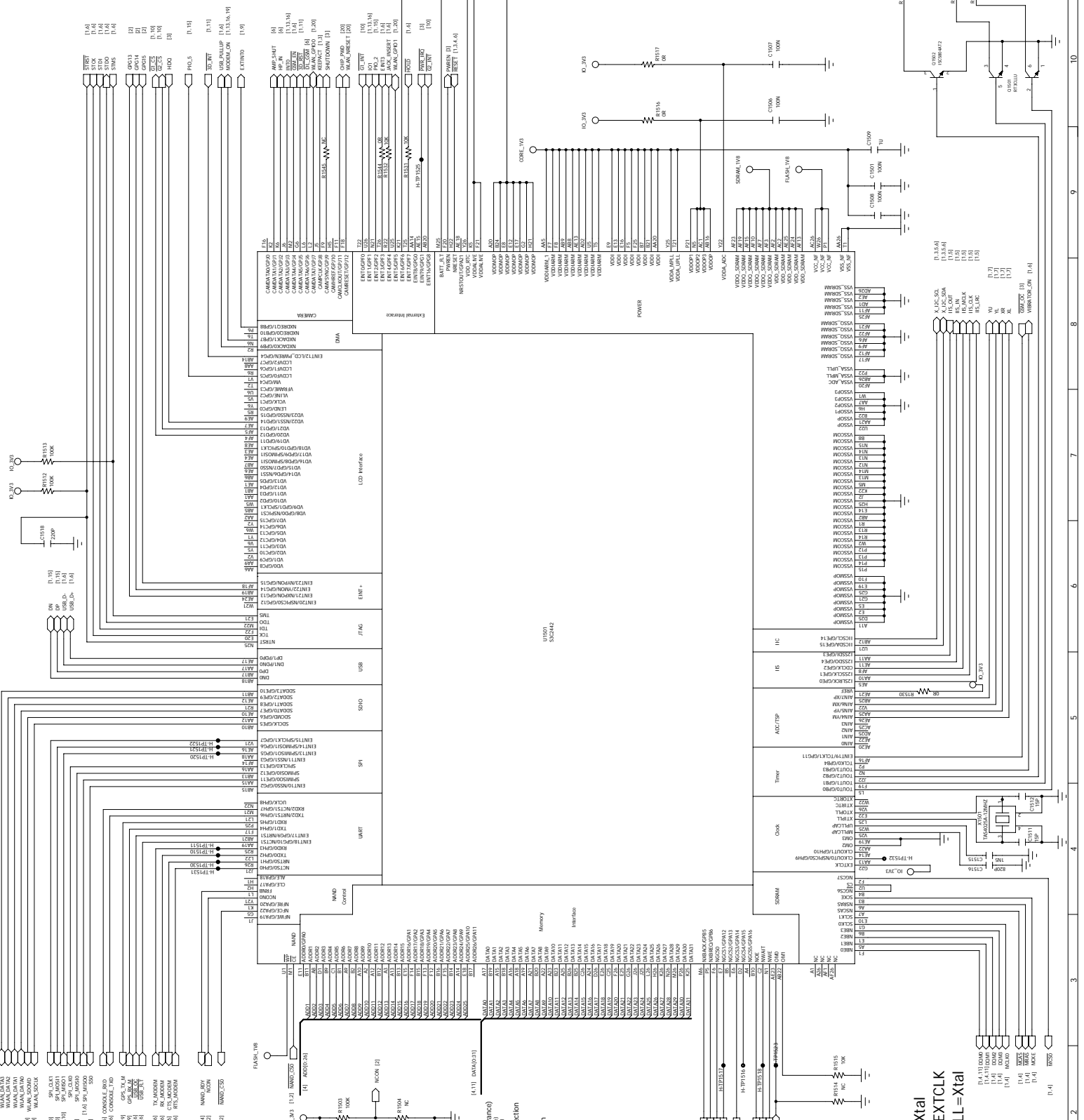
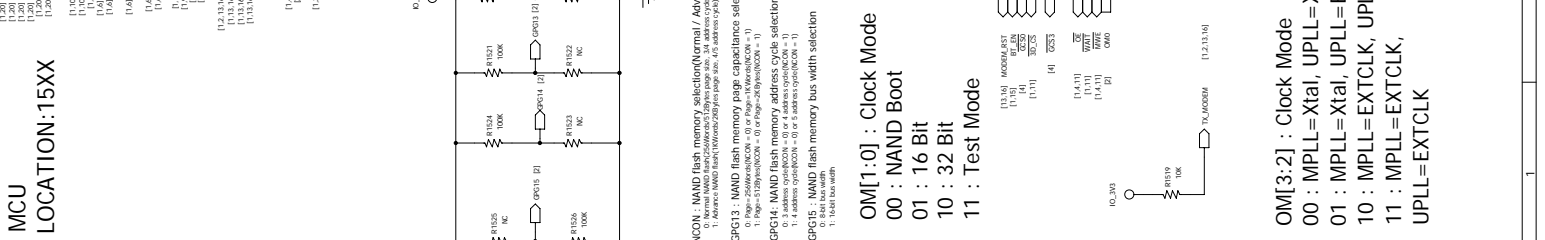
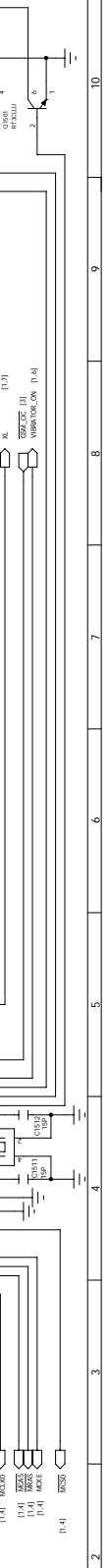
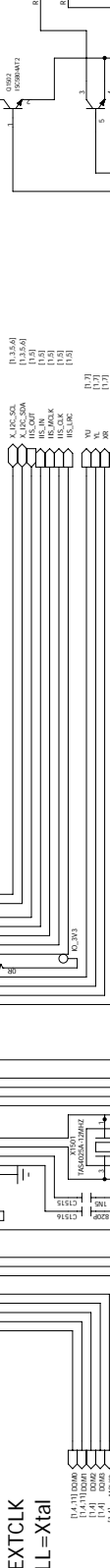
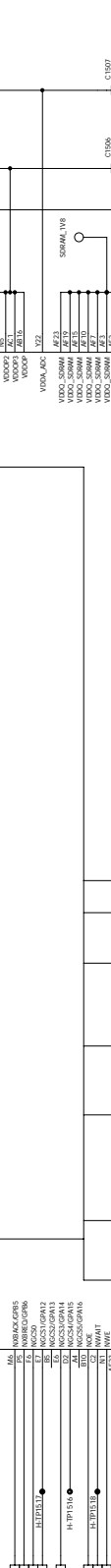
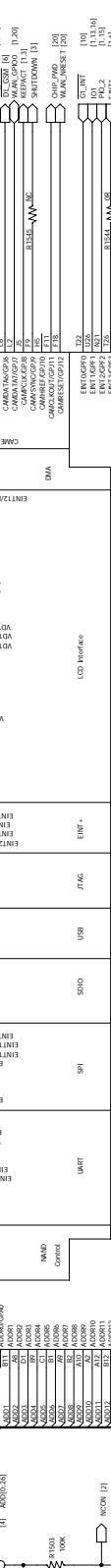
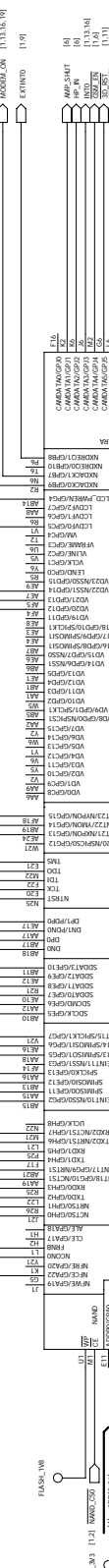
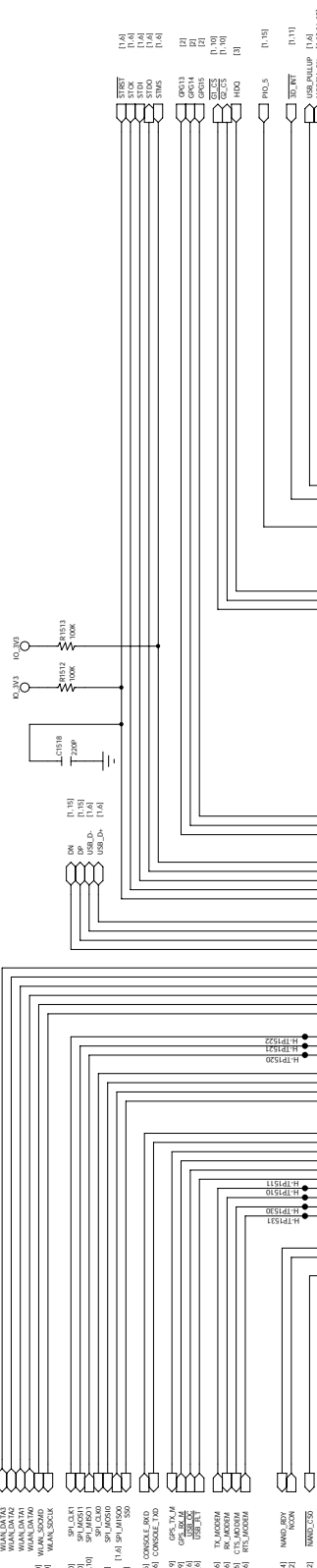
## 4. MEMORY

## 5. AUDIO CODEC

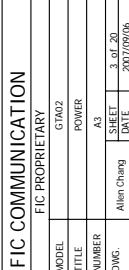
## 6. INTERFACE

FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GT402		
TITLE	STRUCTURE		
NUMBER	A3		
DWG.	Allen Chung	SHEET DATE	1 of 20 2007/09/04

MCU  
LOCATION:15XX

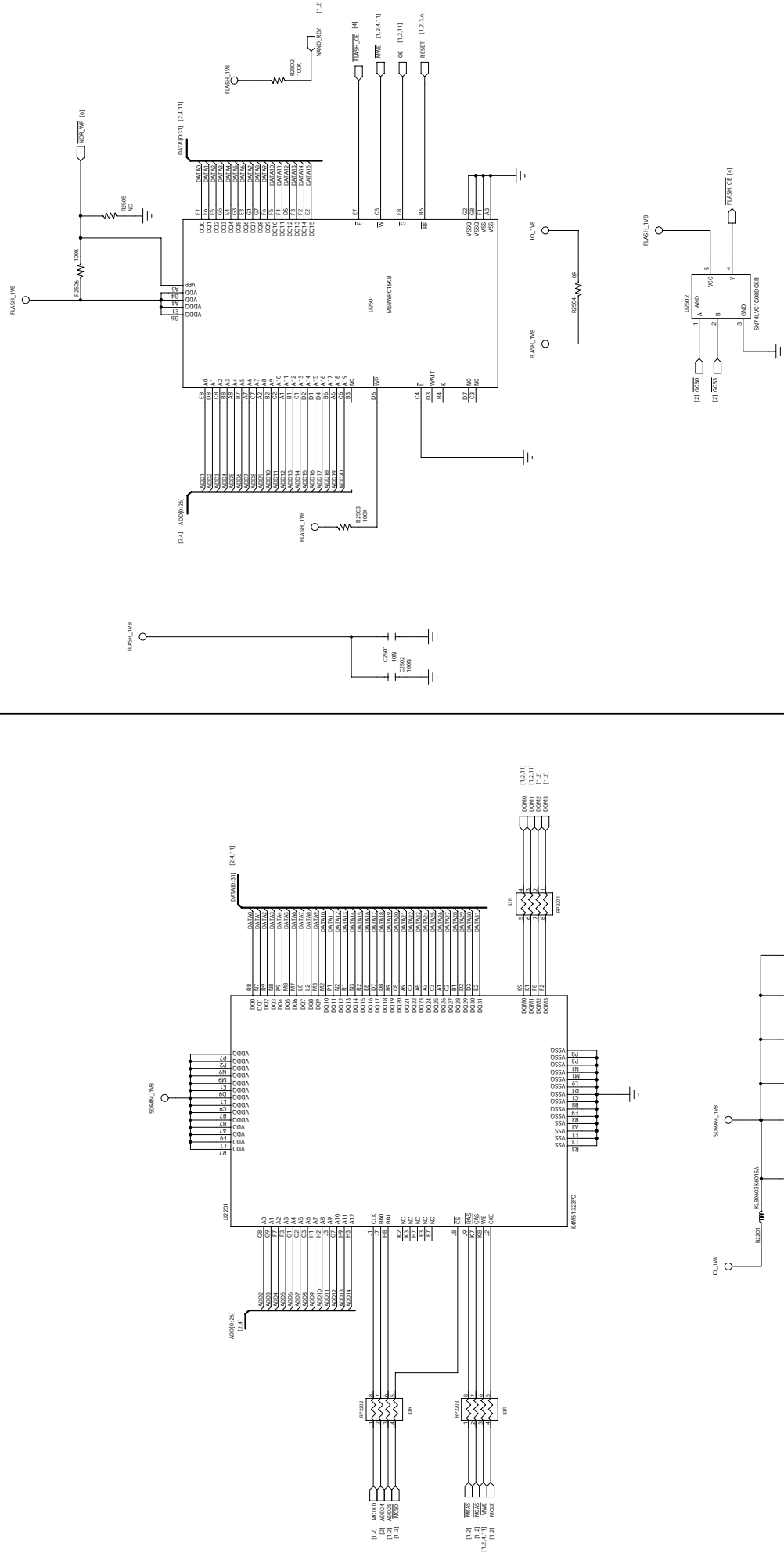


## Battery Connector



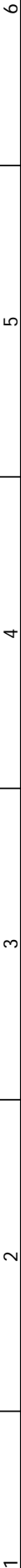
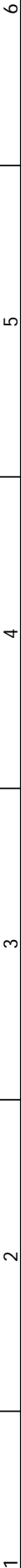
SDRAM  
LOCATION:22XX

NOR FLASH  
LOCATION:25XX



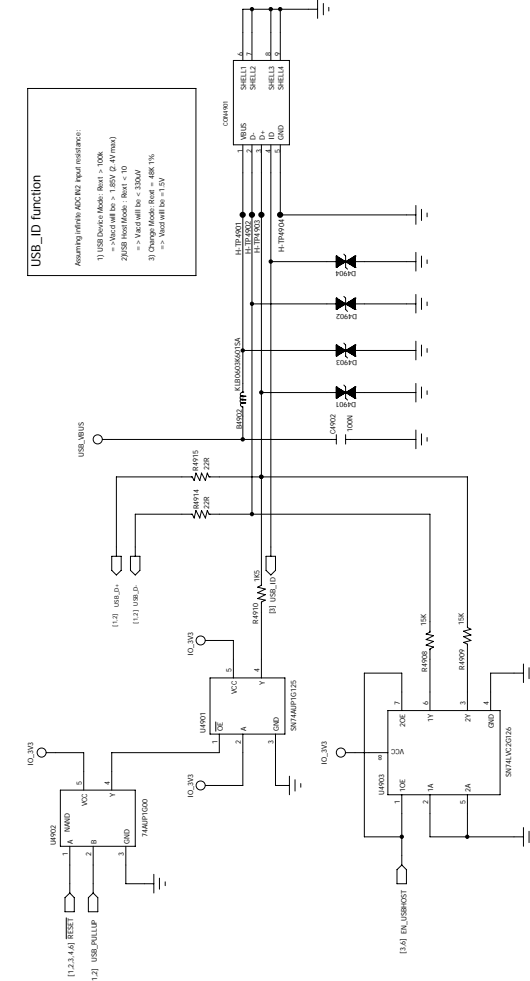
FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GT102		
TITLE	MEMORY		
NUMBER	A3		
DWG.	Allen Chung	SHEET DATE	4 of 20 2007/09/06

1	2	3	4	5	6
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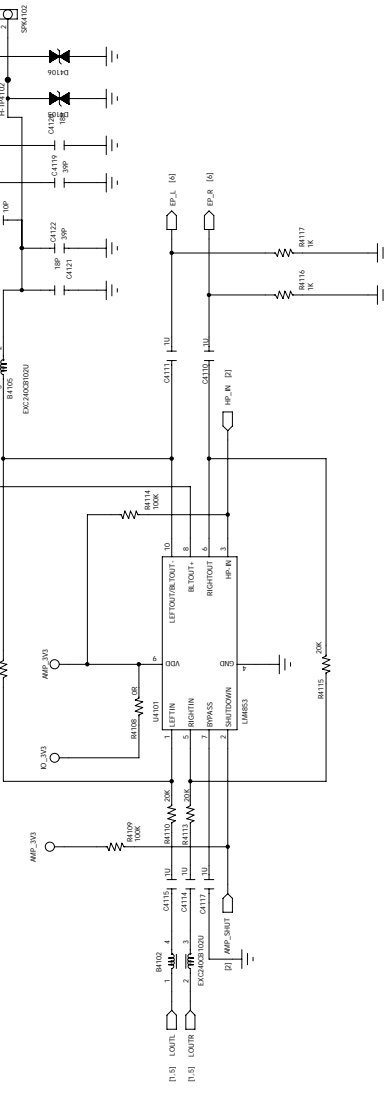


1	2	3	4	5	6
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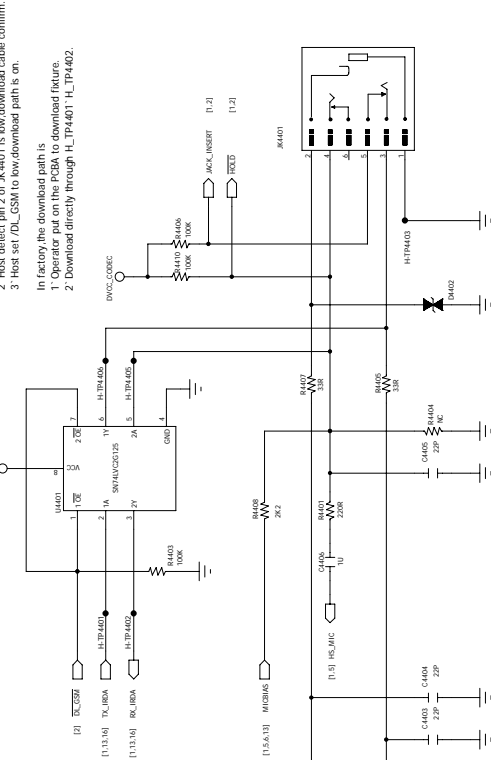
USB  
LOCATION:49XX



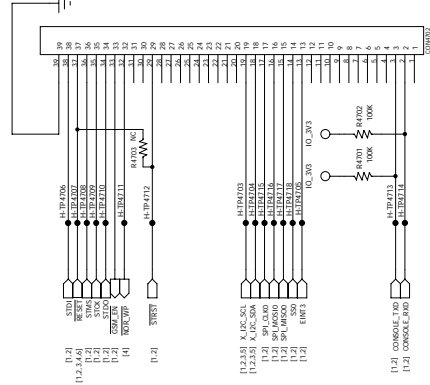
SPK  
LOCATION: 41XX



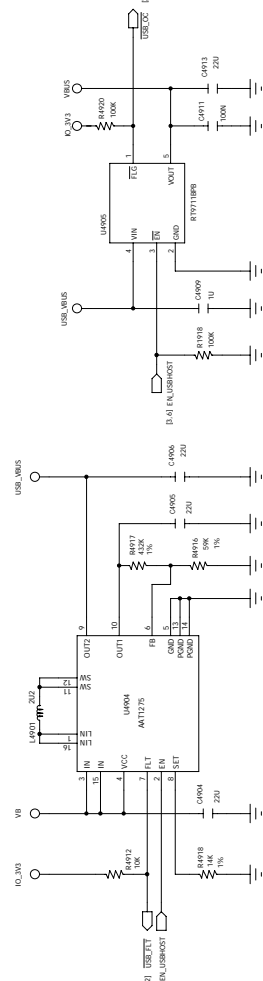
HEADSET  
LOCATION:44XX



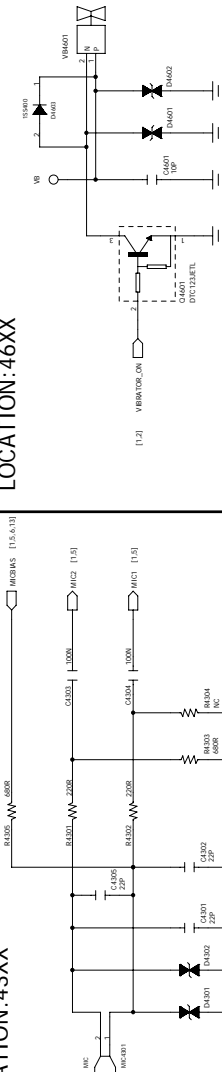
DEBUG  
LOCATION:47XX



MIC  
LOCATION:43XX

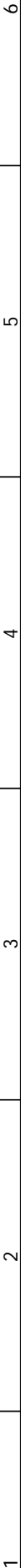


VIBRATOR  
LOCATION: 46XX



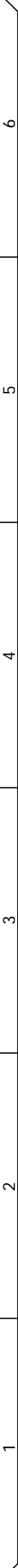
FIC COMMUNICATION	
FIC PROPRIETARY	
MODEL	GT402
TITLE	INTERFACE
NUMBER	A.3
DWG.	Allen Chang
	6. of 20
	2007/09/06

1	2	3	4	5	6
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1	2	3	4	5	6
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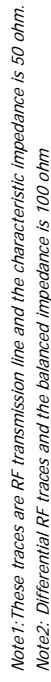
1	2	3	4	5	6
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1	2	3	4	5	6
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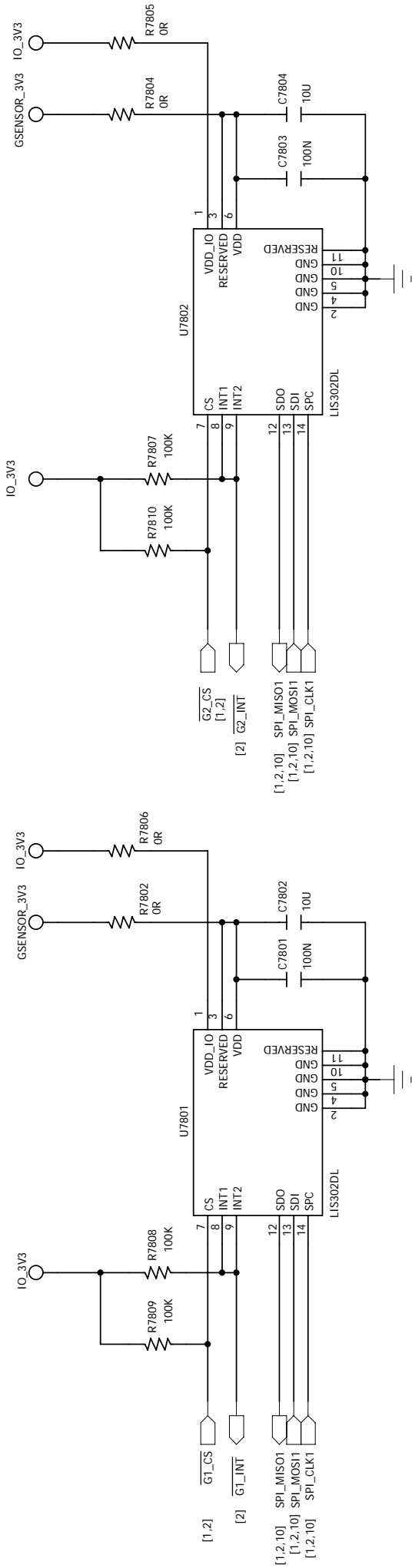


## LOCATION: 76XX



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	AGPS		
NUMBER	A3		
DWG.	Shawn Lin	SHEET DATE	9 of 20 2007/09/06

MOTION\_SENSOR  
LOCATION:78XX

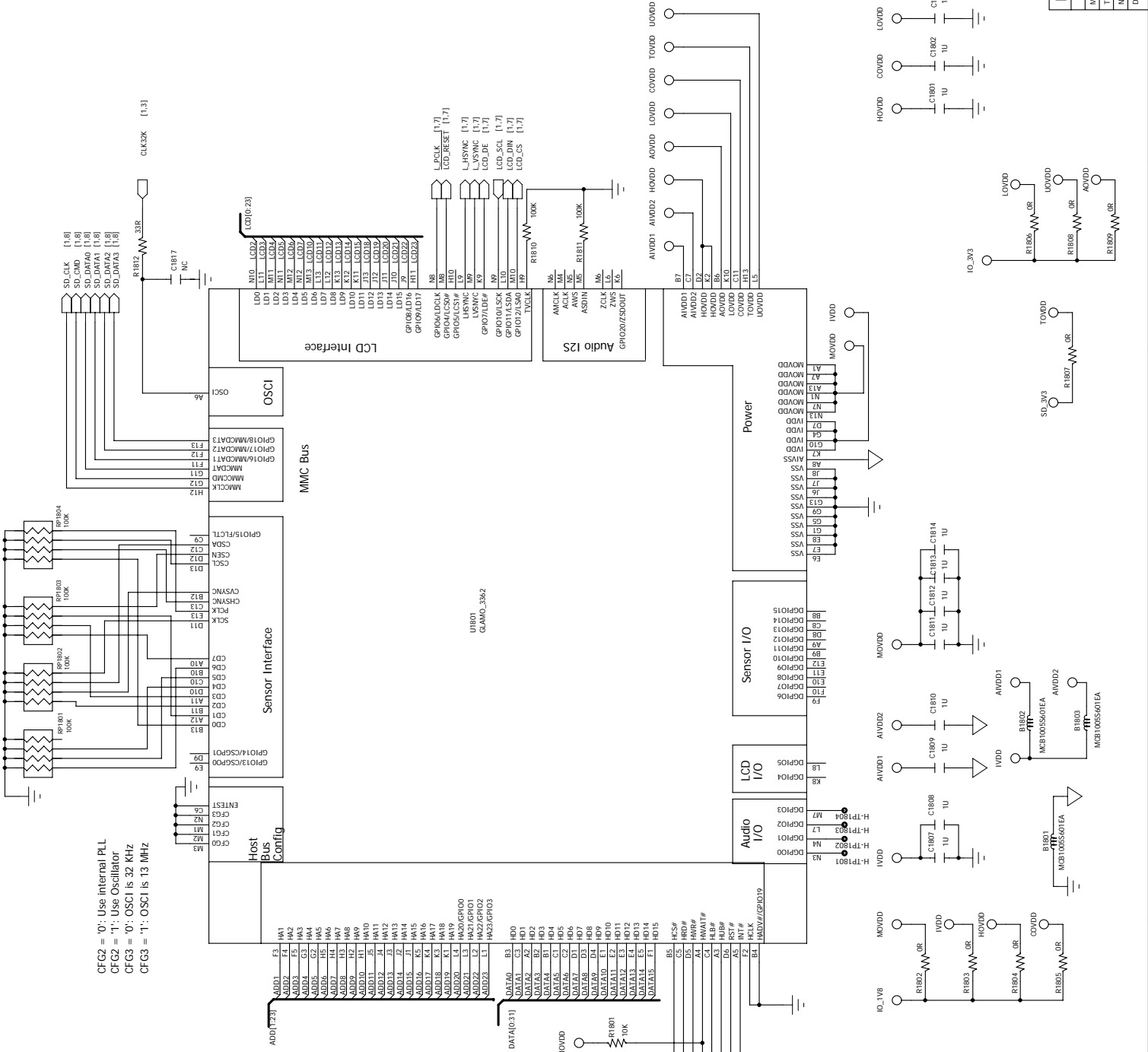


FIC COMMUNICATION					
FIC PROPRIETARY					
MODEL	GTA02				
TITLE	MOTION_SENSOR				
NUMBER	A3				
DWG.	Allen Chang			SHEET	10 of 20
	DATE			2007/09/06	

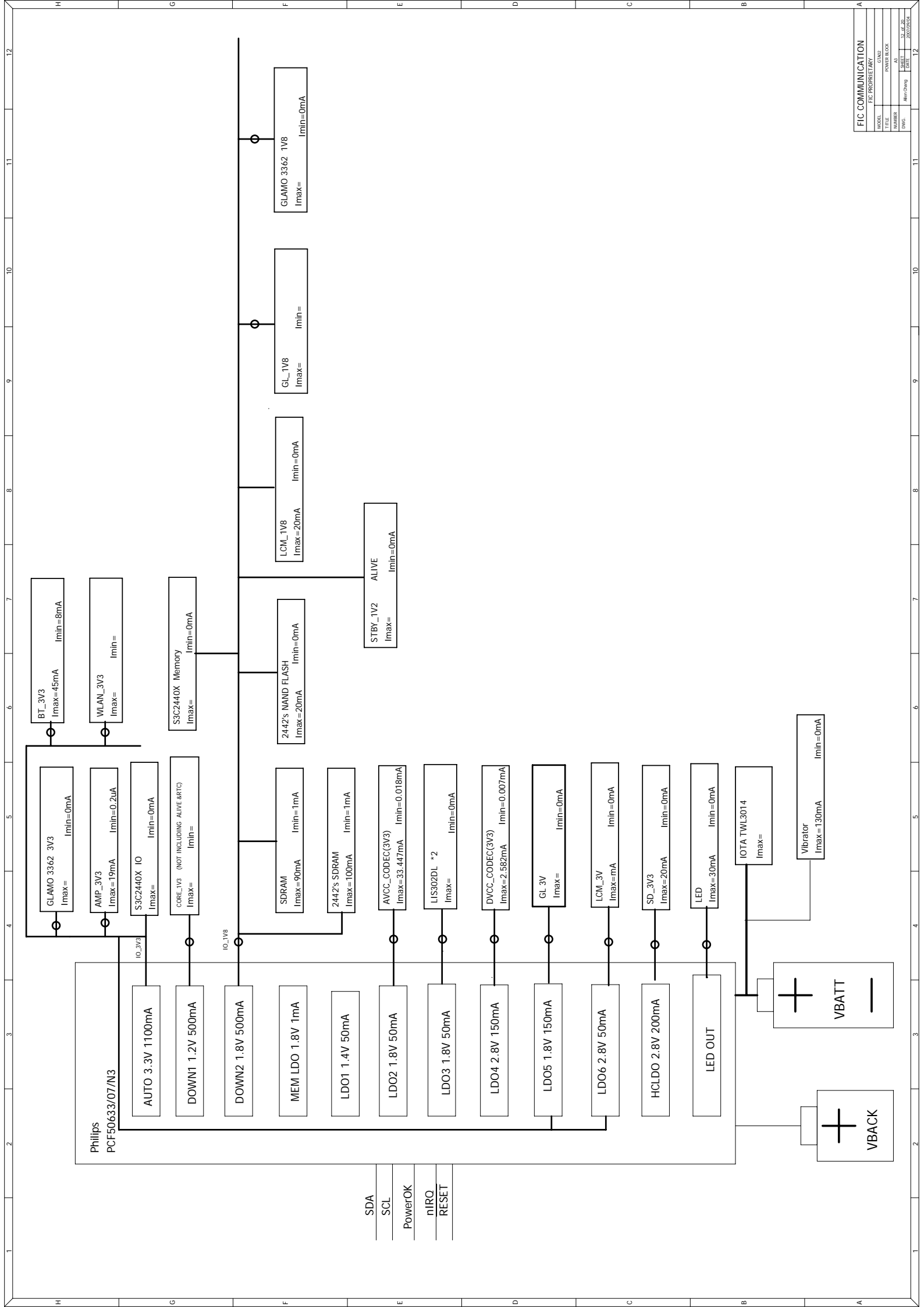
MULTIMEDIA\_CO-PROCESSOR  
LOCATION:18XX

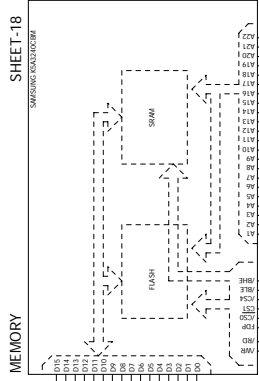
CFG0	CFG1	HCLK	HADV/N	HA10	Host Bus Type	Description
0	0	0	0	0	A10	Direct addressing 16-bit 80 type 1
0	0	0	0	1	A10	Direct addressing 16-bit 80 type 2
0	0	1	N/A	A10	Direct addressing 16-bit 68 type	
0	1	0	ADV	A10	Indirect addressing 16-bit 80 type	
0	1	1	ADV	A10	Indirect addressing 16-bit 68 type	
0	1	0	ADV	1	Indirect addressing 8-bit 80 type	
0	1	1	ADV	1	Indirect addressing 8-bit 68 type	
1	1	ITF	CLK	ADV/N	NA	Synchronous Burst type

CFG2 = '0': Use Internal PLL  
CFG2 = '1': Use Oscillator  
CFG3 = '0': OSCI is 32 KHz  
CFG3 = '1': OSCI is 13 MHz

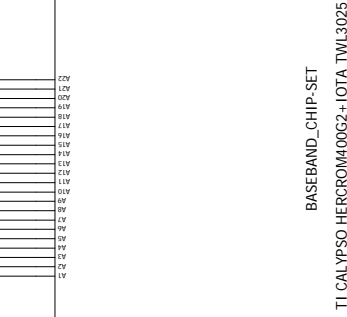


FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GT402		
TITLE	MULTIMEDIA CO-PROCESSOR		
NUMBER	A3		
DATE	11.07.00		
DMC.	Shawn Lin		

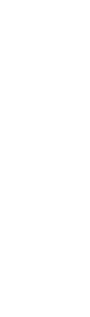
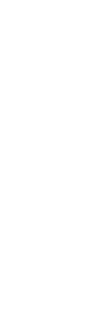
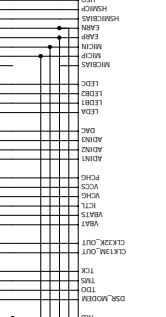
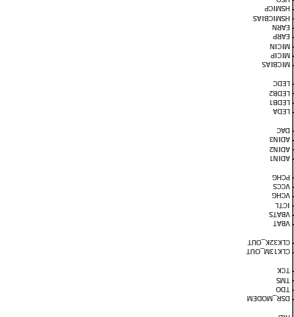


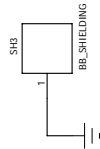
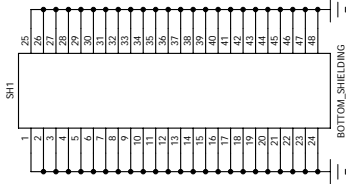
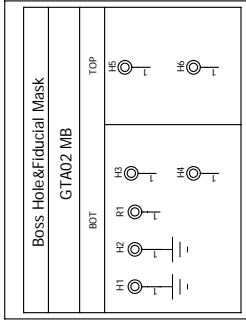
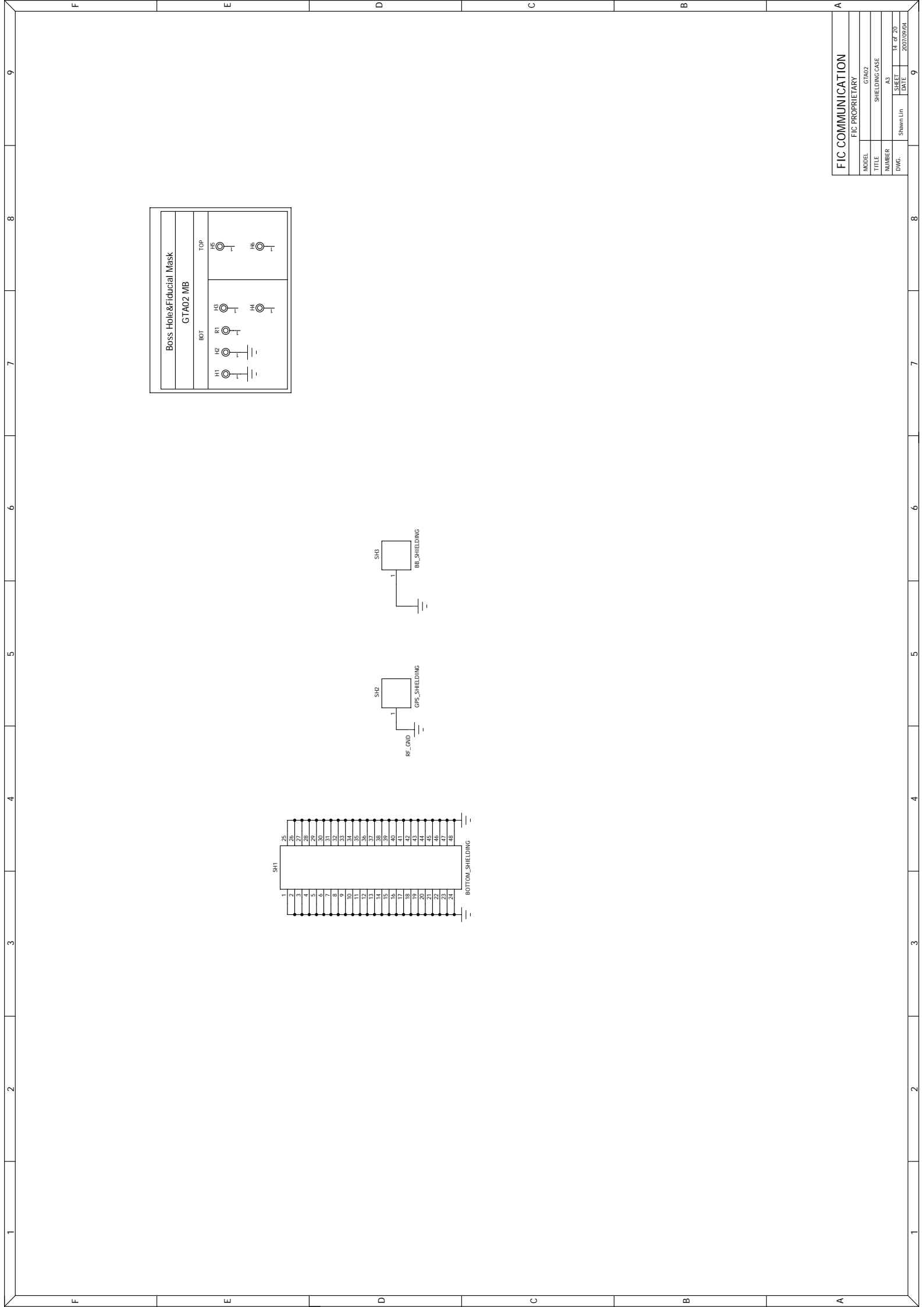


SHEET-19  
MODEM\_INTERFACE



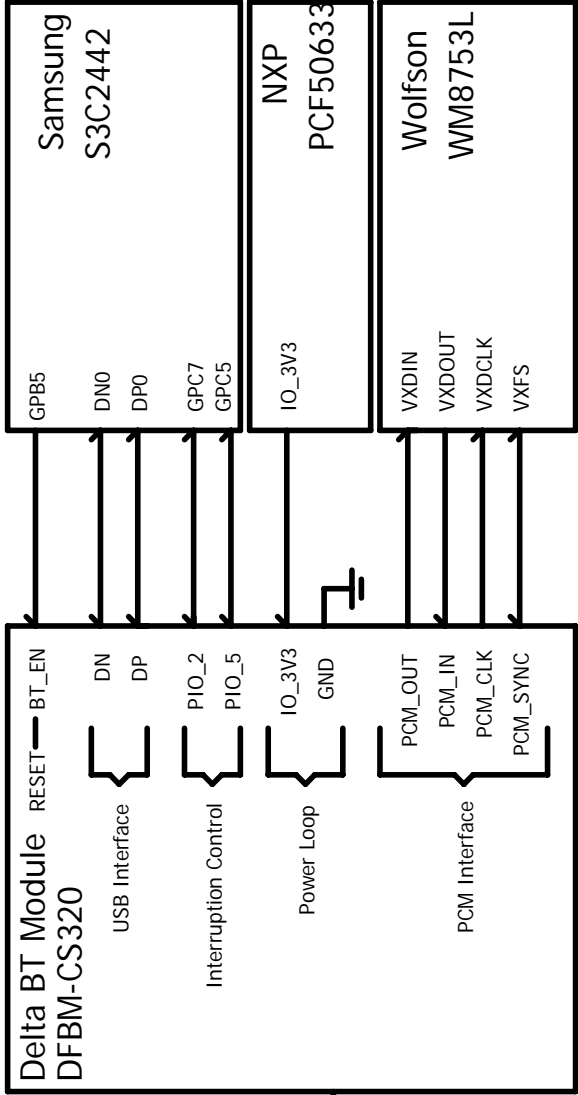
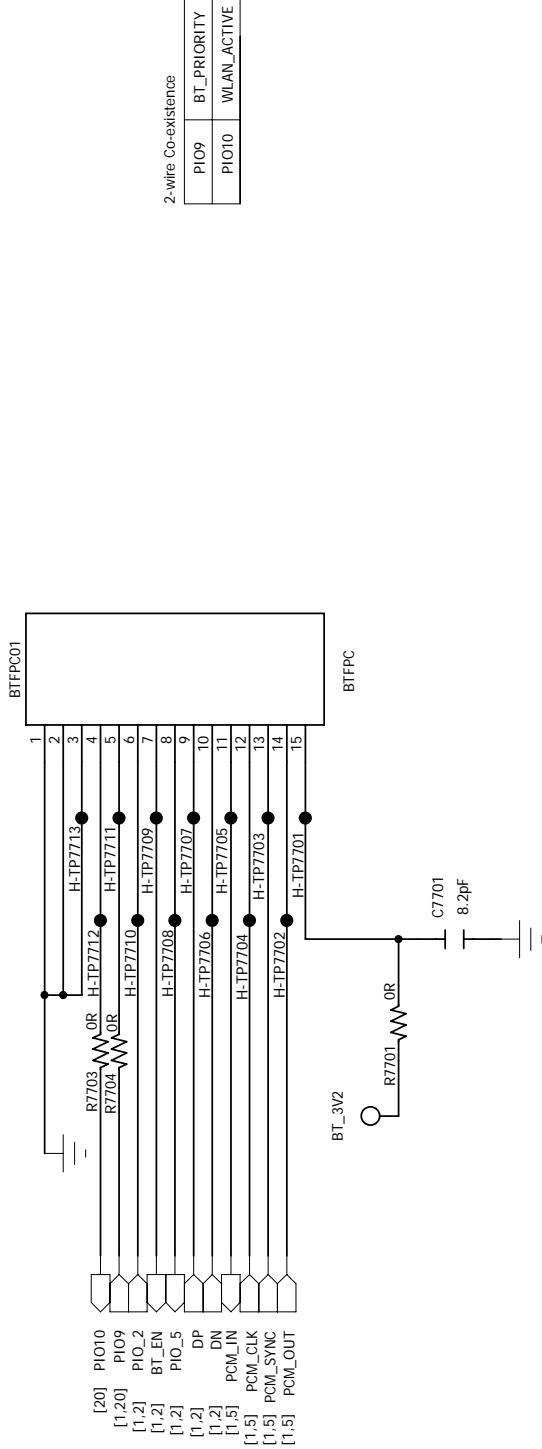
SHEET-16



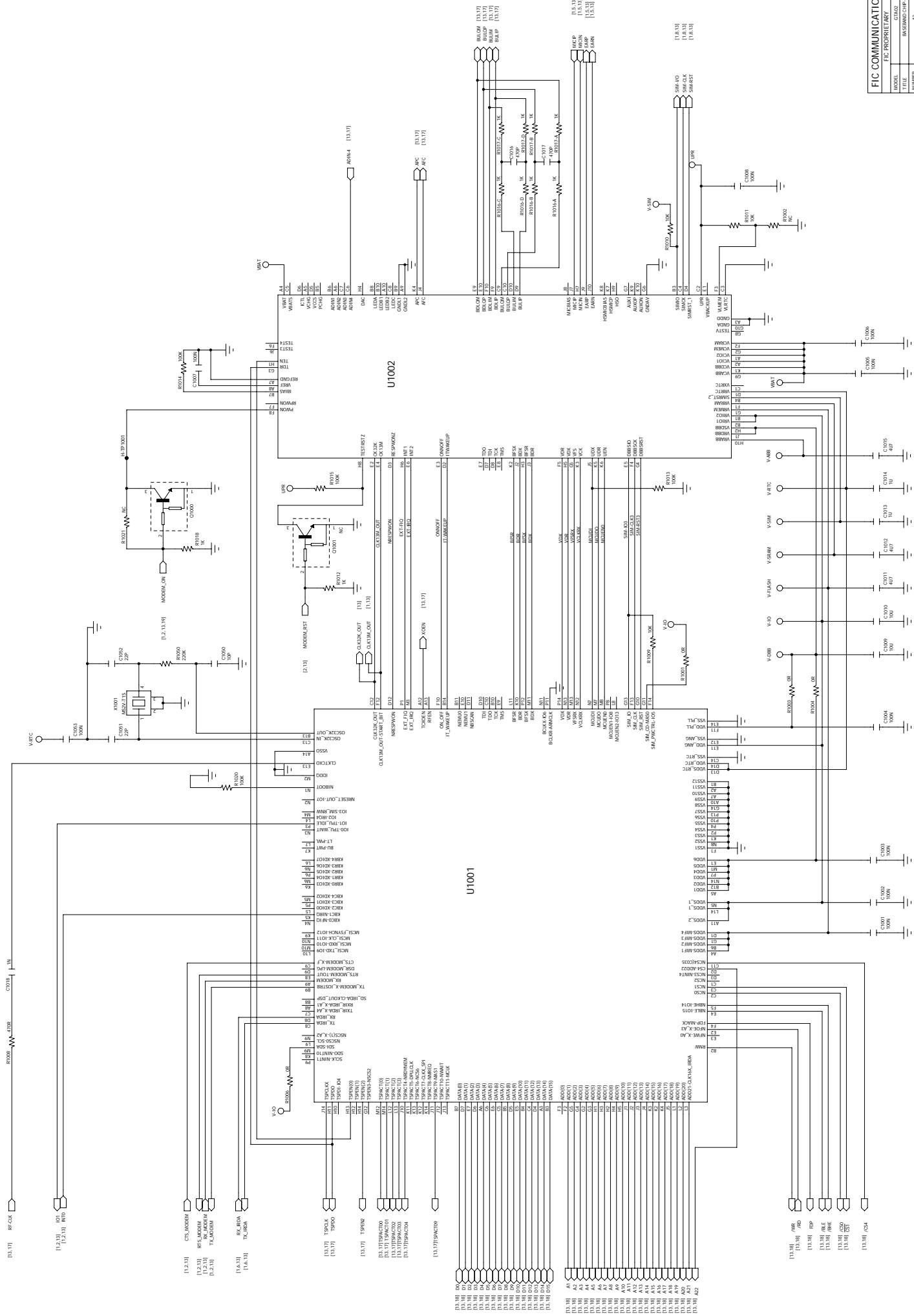


FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	SHIELDING CASE		
NUMBER	A3		
DATE	DATE	DATE	DATE
Shawn Lin			

Bluetooth  
LOCATION:77XX



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	Bluetooth		
NUMBER	A3		
DWG.	Shawn Lin	SHEET	15 of 20
		DATE	2007/09/06

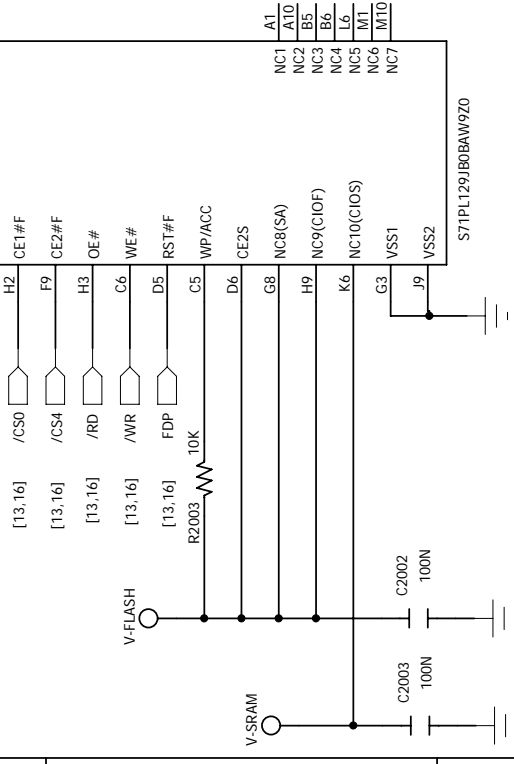
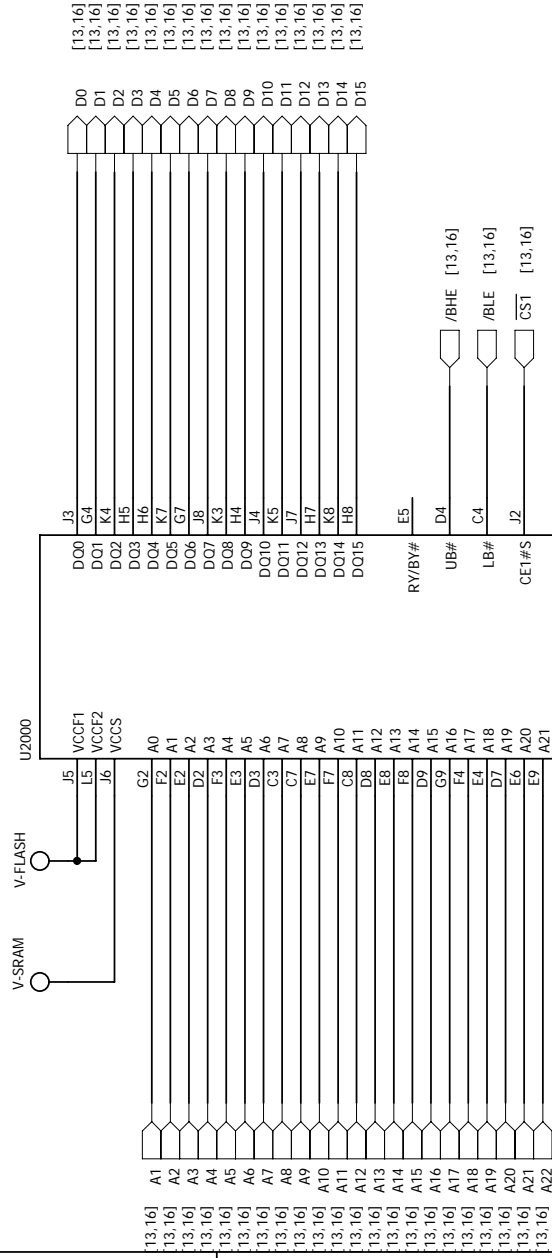


FIC COMMUNICATION				FIC PROPRIETARY	
MODEL		G7A02			
TITLE		BASEBAND CHIP			
NUMBER		A2			
DWG.		Shawn Lin		SHEET	
				DATE	





MEMORY  
LOCATION: 2XXX



Supplier	MCP	Vcc-Range	Memory			Dimension(L*W*H)	Different Pin define and mapping										PIN
			FLASH 16bits	SRAM 16bits	NAND 98bits		NC	NC	NC	NC	NC	NC	NC	NC	NC		
SPANSION	S71PLO32I80	2.7~3.1	32M	8M		9*7*1.09	NC	NC	NC	NC	NC	NC	NC	NC	56		
	S71PLO64I80	2.7~3.1	64M	8M		9*9*1.19	A21	NC	NC	NC	NC	NC	NC	NC	56		
	S71PLO64IA0	2.7~3.1	64M	16M		9*7*1.09	A21	NC	NC	NC	NC	NC	NC	NC	56		
	S71PL129I80	2.7~3.1	128M	32M		11.6*8*1.19	A21 E9	CE2#f F9	NC	NC	NC	NC	H9 G8	NC	64		
SANSUNG	K5A3240CT(8M	2.7~3.6	32M	4M		11*8*1.2	NC	NC	BYTES	SA					69		
	K5A3281CT(8M	2.7~3.6	32M	8M		11*8*1.2	NC	NC	BYTES	NC					69		
	K5A6317CT(8M	2.7~3.6	64M	16M		11.6*8*1.4	A21	NC	BYTES	NC					69		
	K4Dxx0700D	2.7~3.6	128M	32M		11.6*8*1.4	A21	CE2F	BYTES	NC					69		

NOR <=> Flash : N <=> F  
SRAM <=> UIRAM : S <=> U  
SA : Address input for SRAM  
BYTES : BYTES control for SRAM  
BYTES : H =>WORD / L =>BYTE

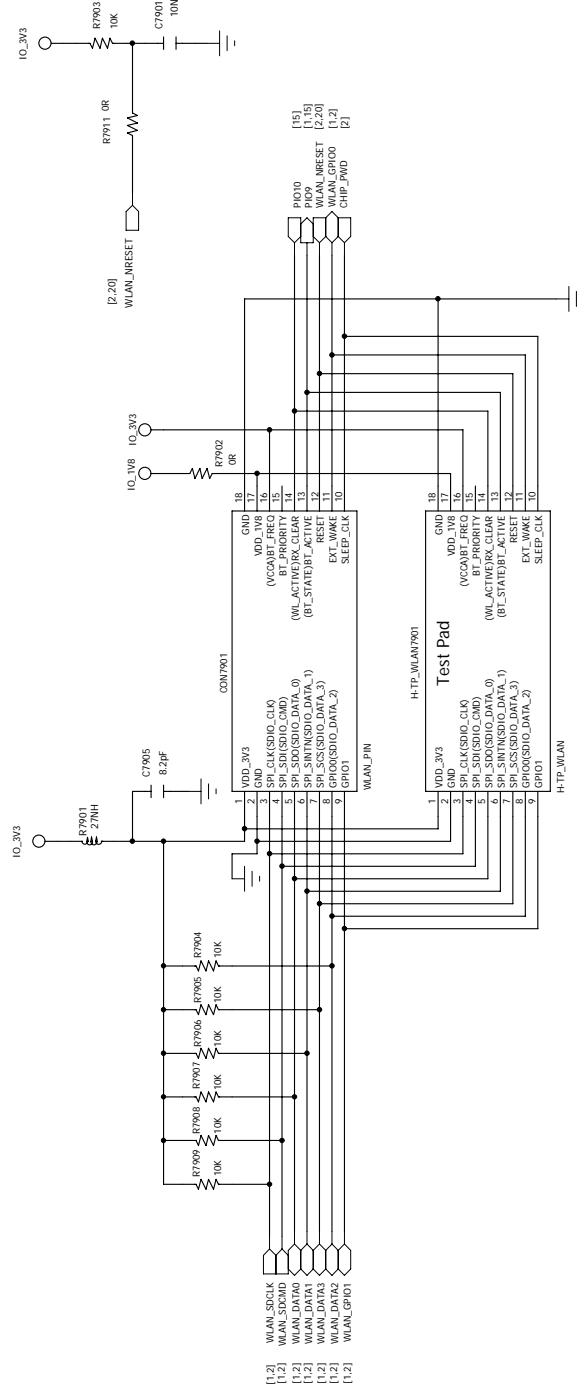
\* The component package is SPANSION type (S71PL129JB0)

## FIC COMMUNICATION

FIC PROPRIETARY			
MODEL	GTA02		
TITLE	MEMORY		
NUMBER	A3		
DWG.	Shawn Lin	SHEET DATE	18 of 20 2007/09/06



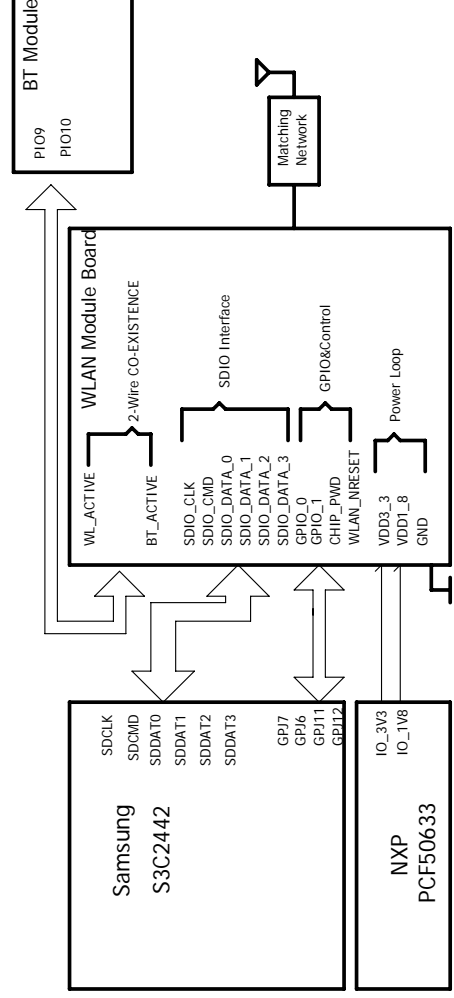
WLAN  
LOCATION:79XX



Module	DFWM-MHG21		DFWM-AHG11	SWL-2480	SWL-2700S
	DELTA			SAMSUNG	
Pin	Marvell 88W8686		Atheros AR6001GZ	Marvell 88W8686	Atheros AR6001GZ
1	⊕	⊕	⊕	⊕	⊕
2	⊕	⊕	⊕	⊕	⊕
3	⊕	⊕	⊕	⊕	⊕
4	⊕	⊕	⊕	⊕	⊕
5	⊕	⊕	⊕	⊕	⊕
6	⊕	⊕	⊕	⊕	⊕
7	⊕	⊕	⊕	⊕	⊕
8	⊕	⊕	⊕	⊕	⊕
9			⊕	⊕	⊕
10	⊕			⊕	
11	⊕			⊕	
12				⊕	
13	⊕	⊕	⊕	⊕	⊕
14	⊕	⊕	⊕	⊕	⊕
15	⊕	⊕	⊕	⊕	⊕
16			⊕		⊕
17			⊕	⊕	⊕
18	⊕	⊕	⊕	⊕	⊕

\* 3-wire co-existence supported only use CS324 BT module

- ⊖ : Power Loop
- ⊙ : SPI Interface
- △ : SDIO Interface
- : Interruption Control
- ⊗ : 3-wire Co-existence if BT supports
- ⊙ : 4-wire Co-existence if BT supports



FIC COMMUNICATION		
FIC PROPRIETARY		
MODEL	GTA02	
TITLE	WLAN	
NUMBER	A3	
DWG.	Shawn Lin	SHEET DATE 20 of 20 2007/09/06