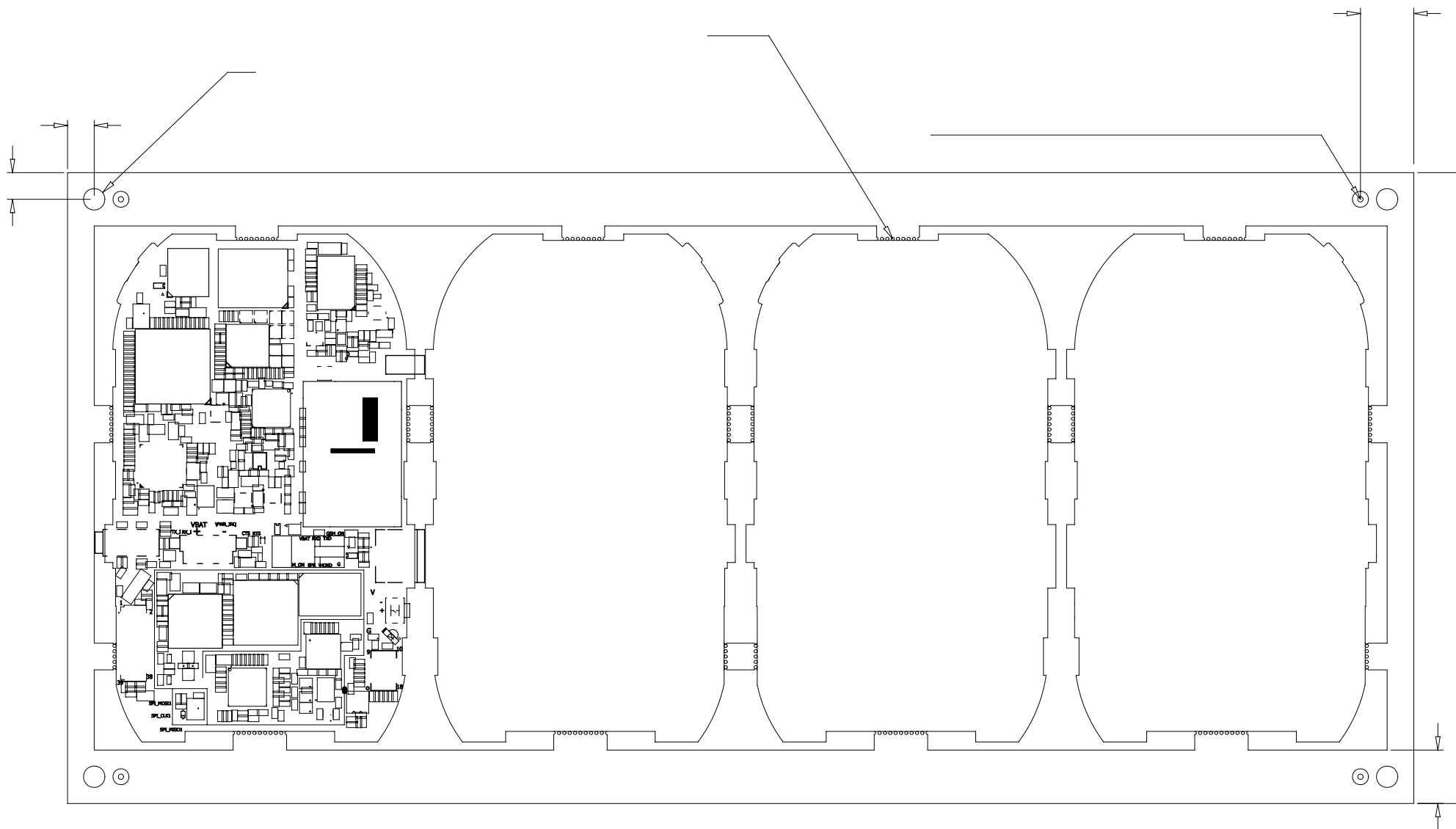
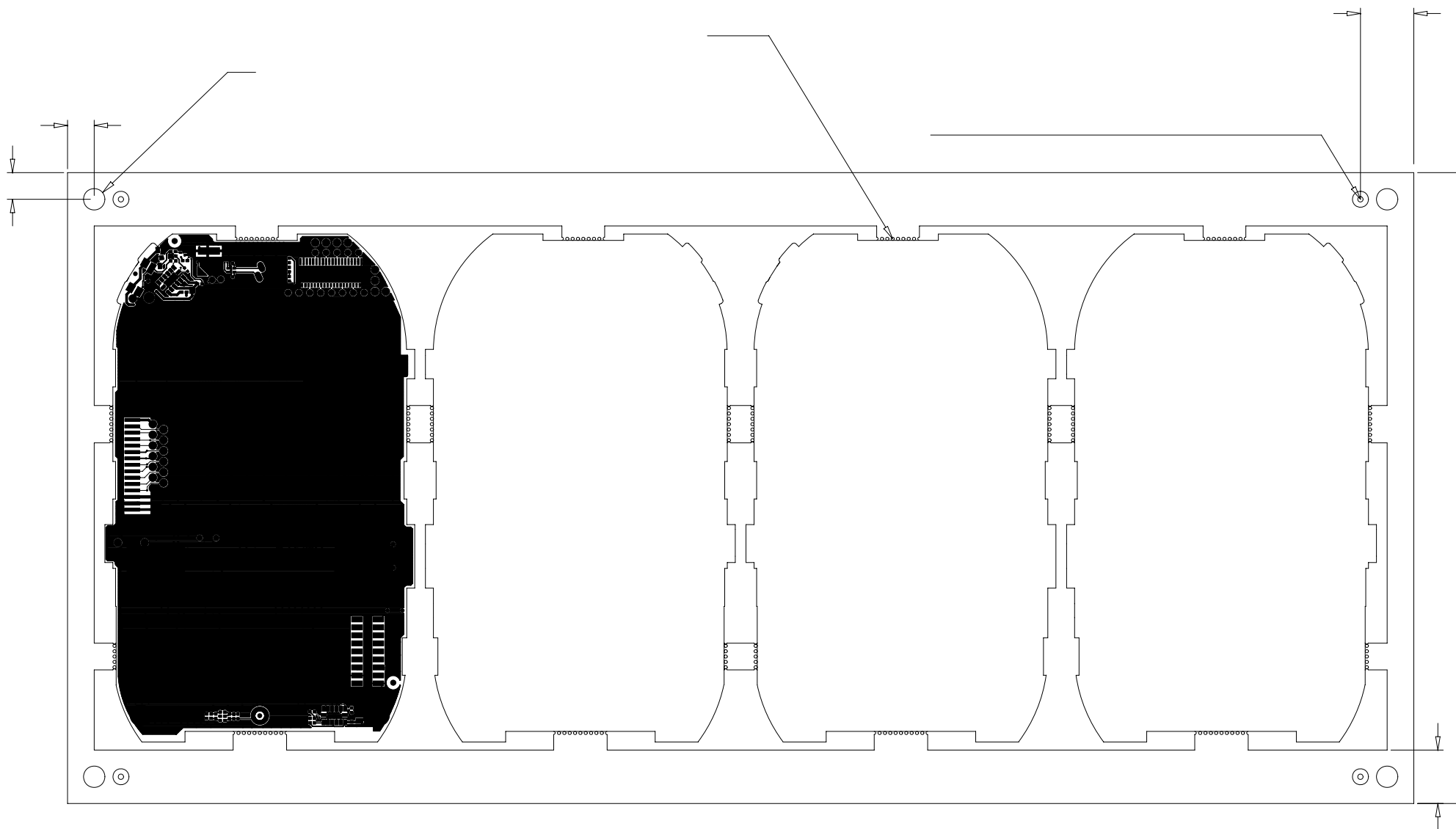


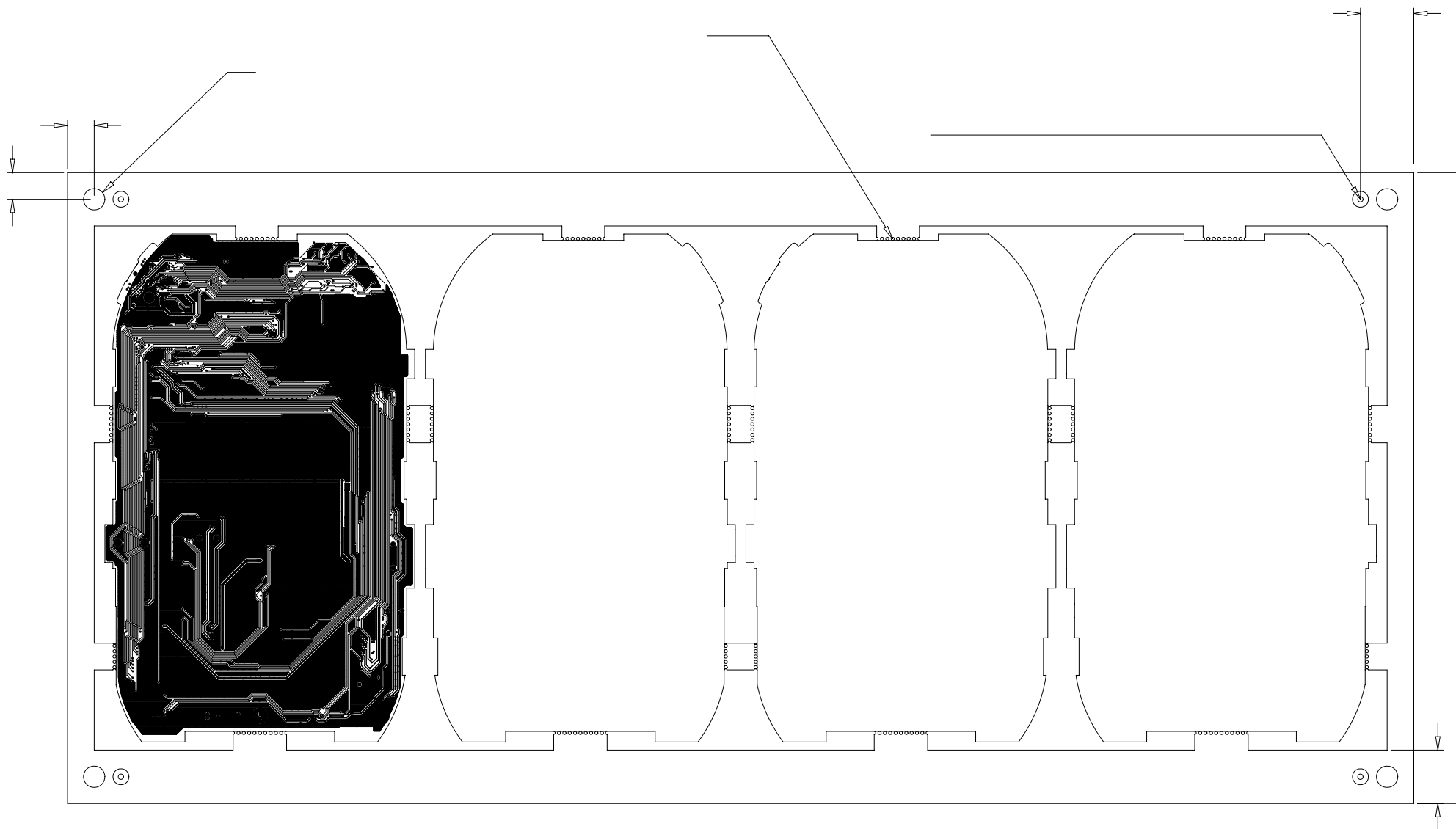
ST	GTA02	8 LAYER
	FIC011	
LAYER:	SILKSCREEN (TOP)	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0



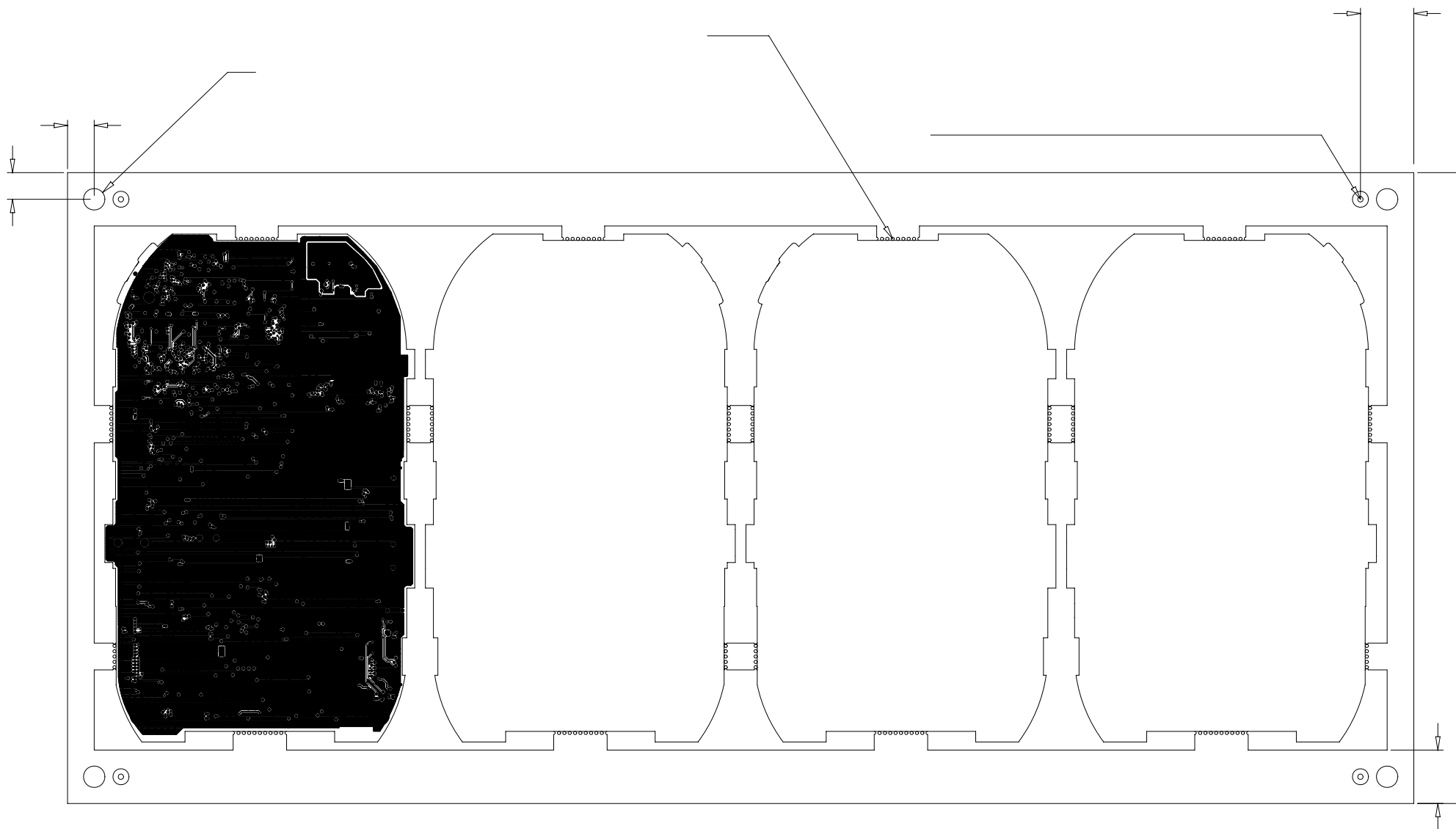
ST	GTA02	8 LAYER
	FIC011	
LAYER:	(TOP) ИЛКСКРИН	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0



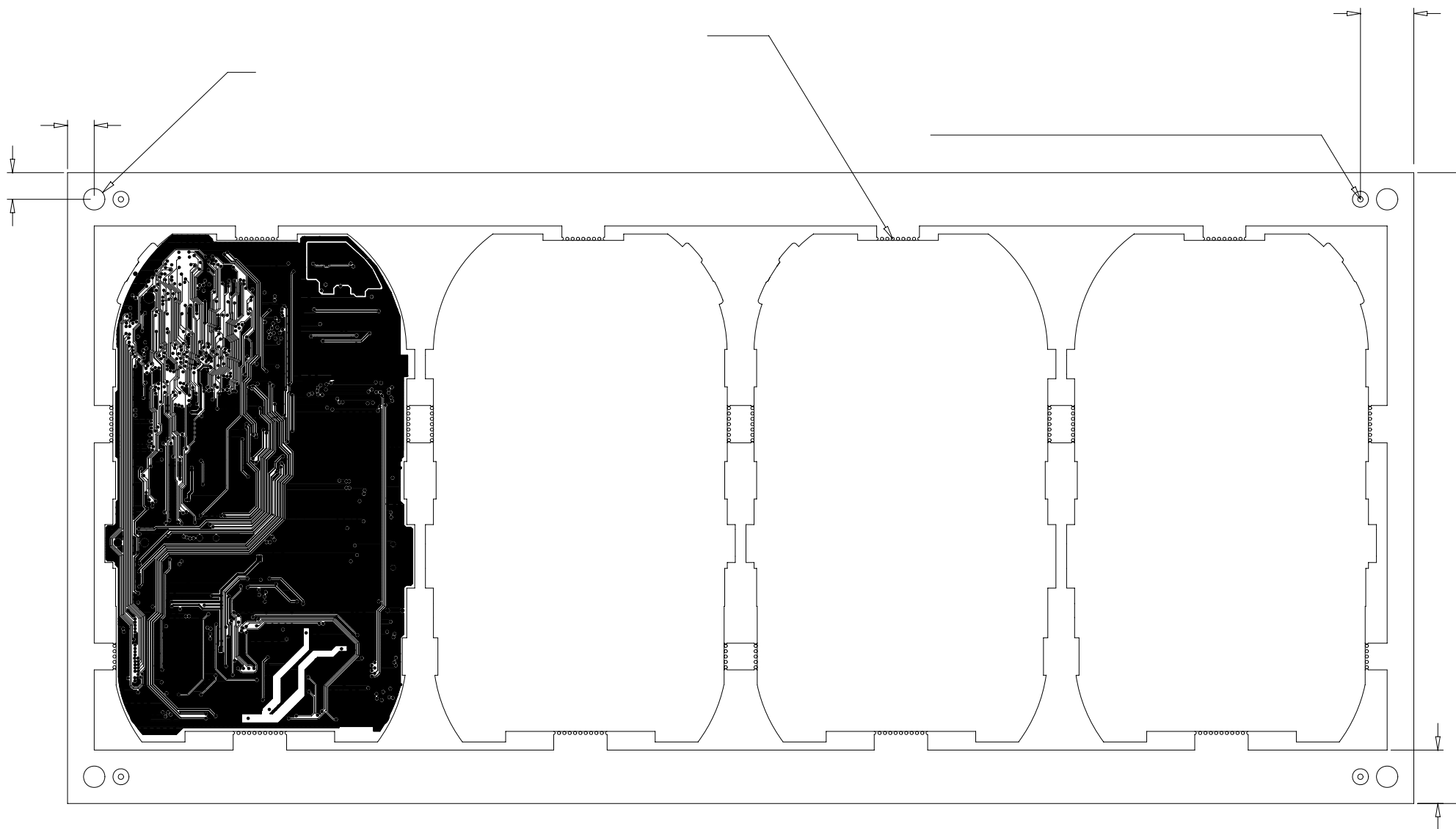
ST	GTA02	8 LAYER
	FIC011	
LAYER: L1 TOP SIDE		
DATE: 2007-12-20 12:00		
Clearance error		8
Connectivity error		0



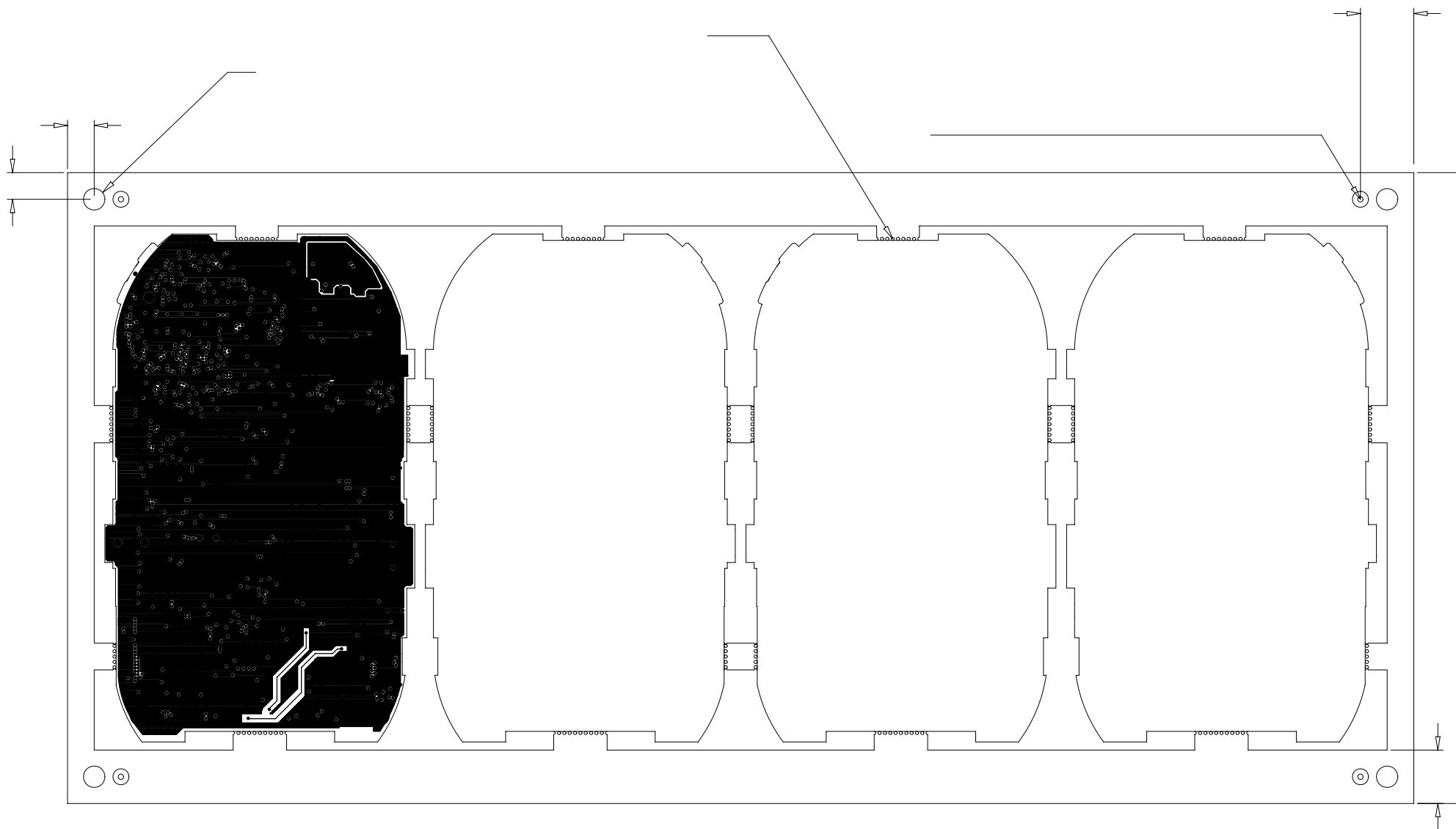
ST	GTA02	8 LAYER
	FIC011	
LAYER: L2 GND(TRK)		
DATE: 2007-12-20 12:00		
Clearance error		8
Connectivity error		0



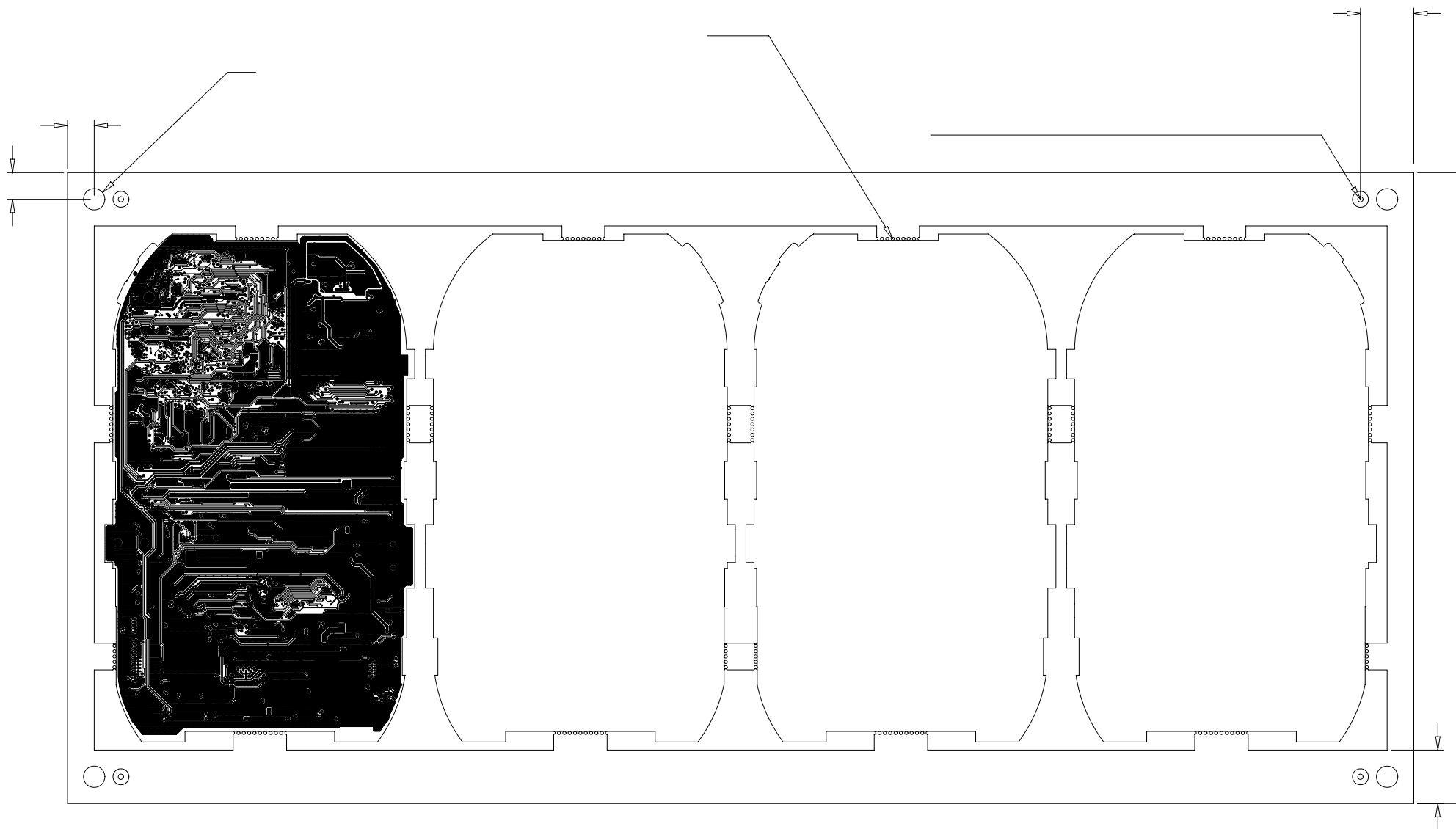
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L3 GND(TRK)	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0



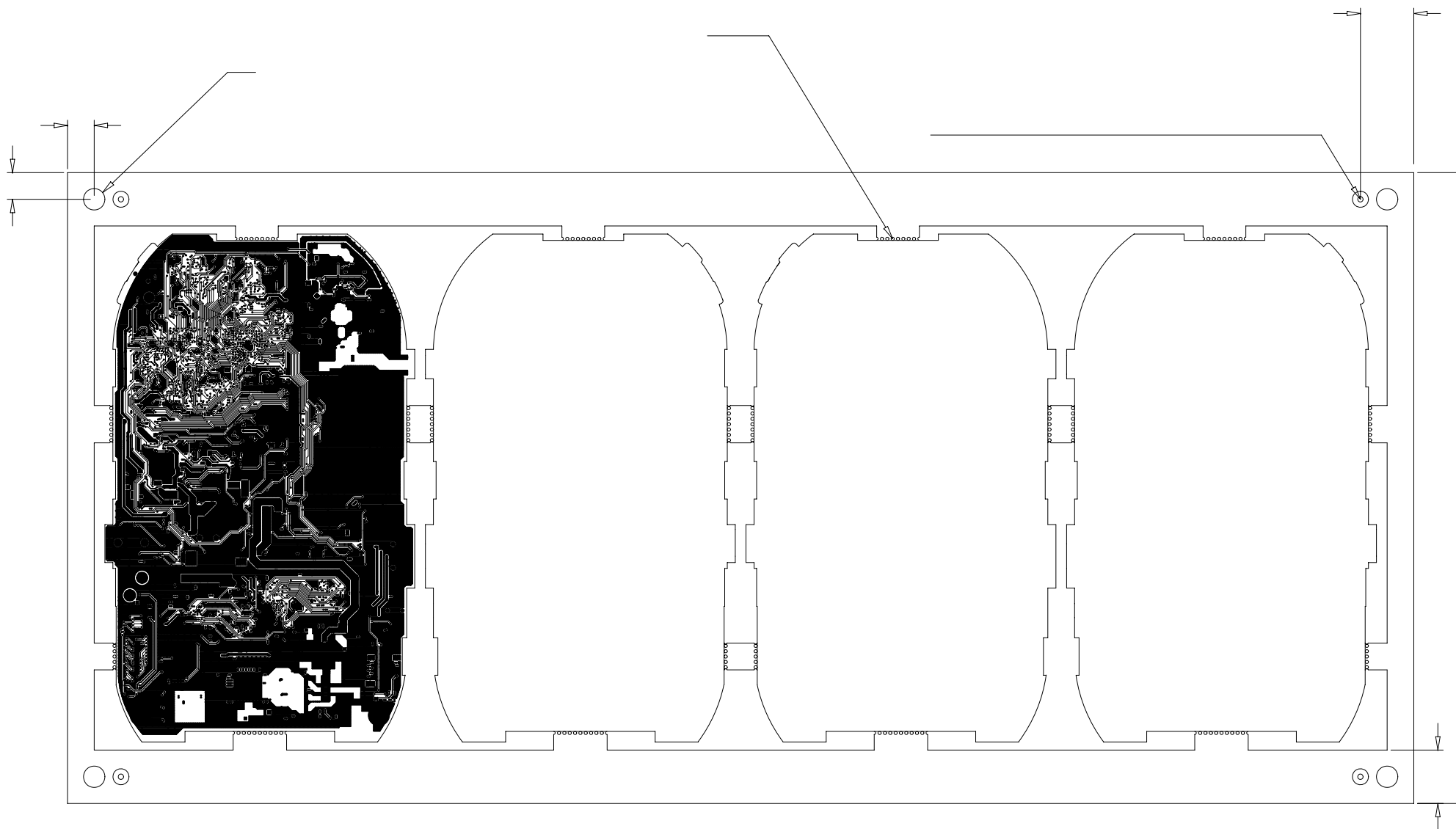
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L4 GND1	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0



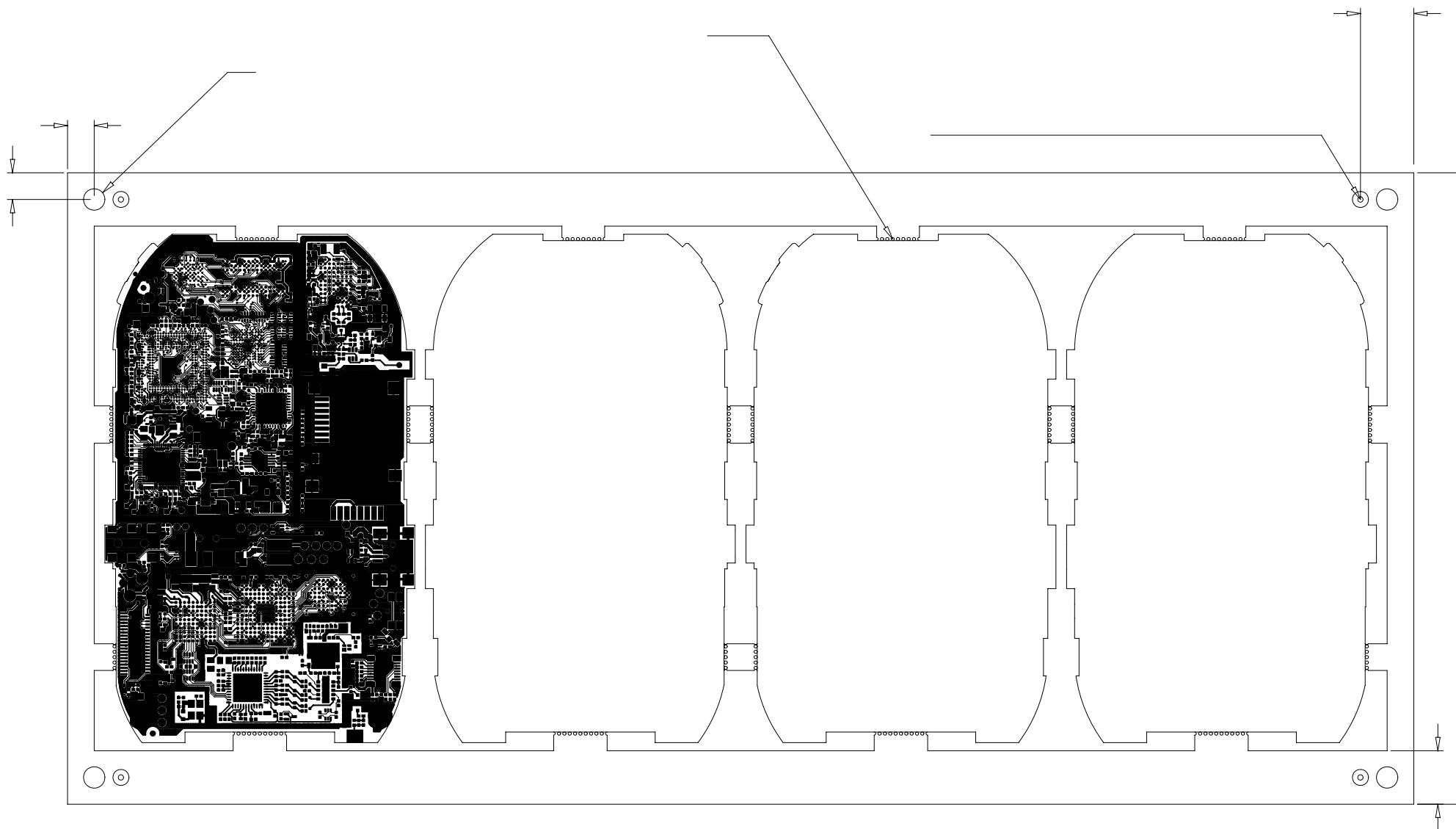
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L5 VCC	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0



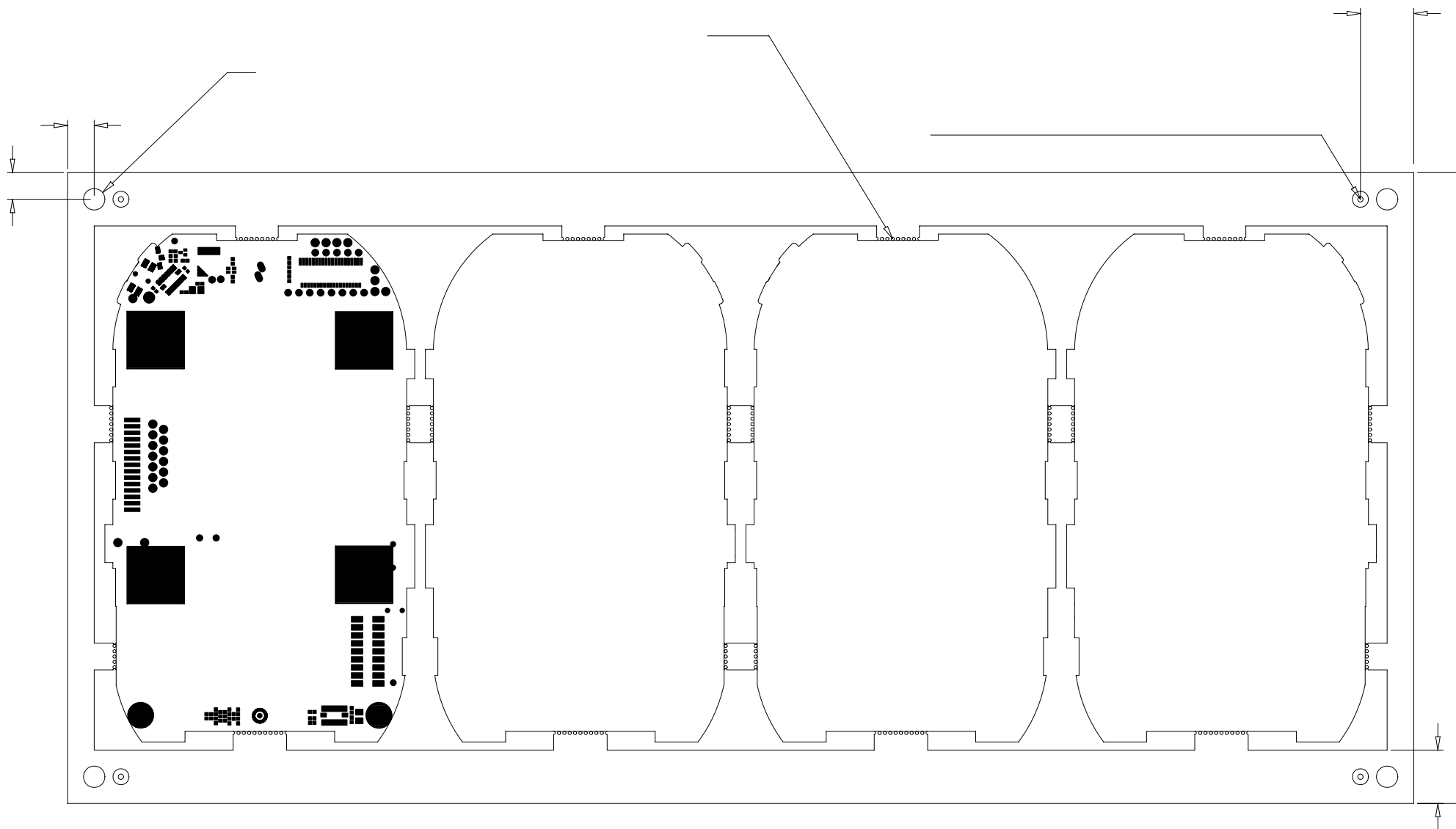
ST	GTA02	8 LAYER
	FIC011	
LAYER: L6 GND2		
DATE: 2007-12-20 12:00		
Clearance error		8
Connectivity error		0



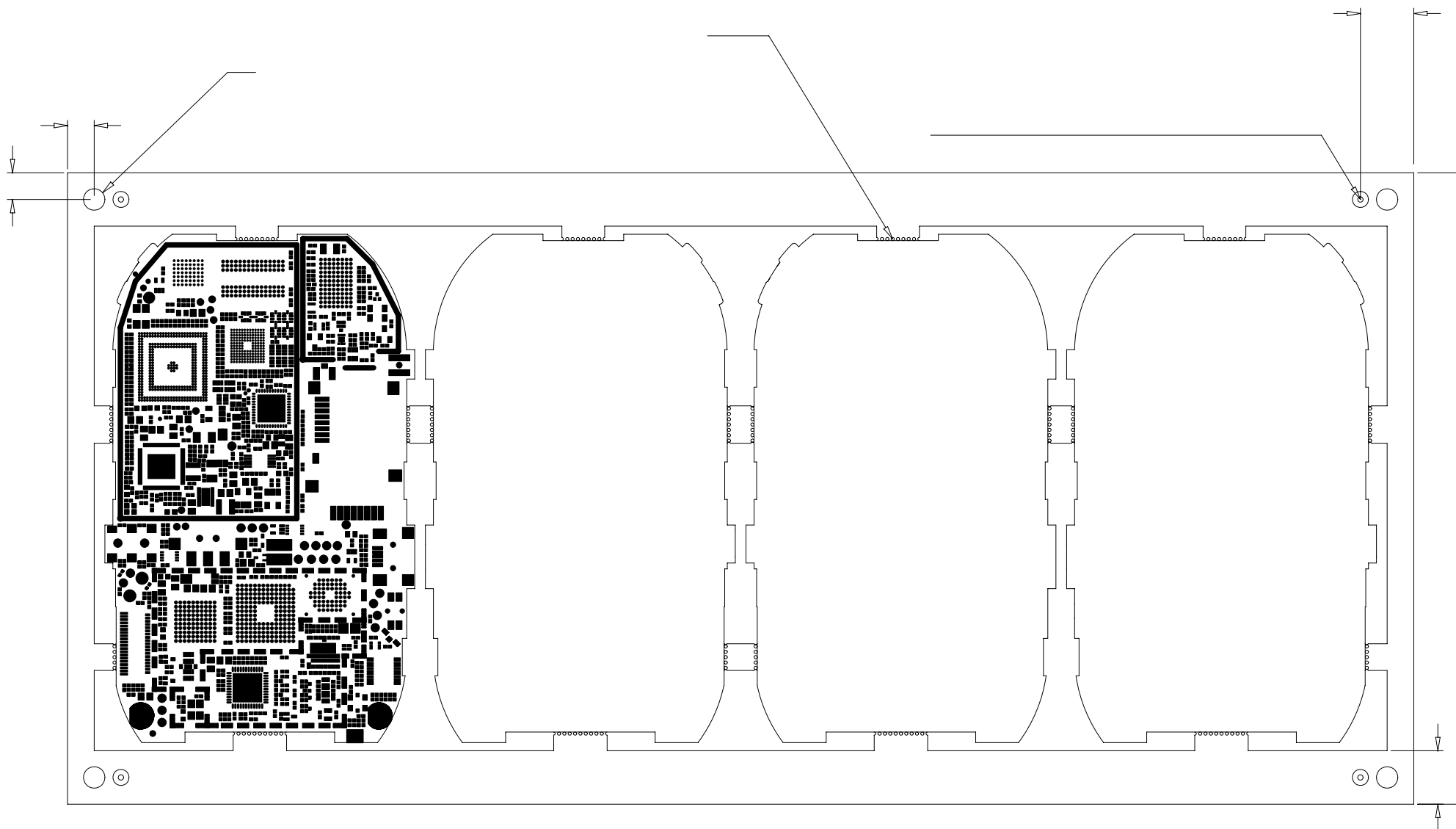
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L7 GND(TRK)	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0



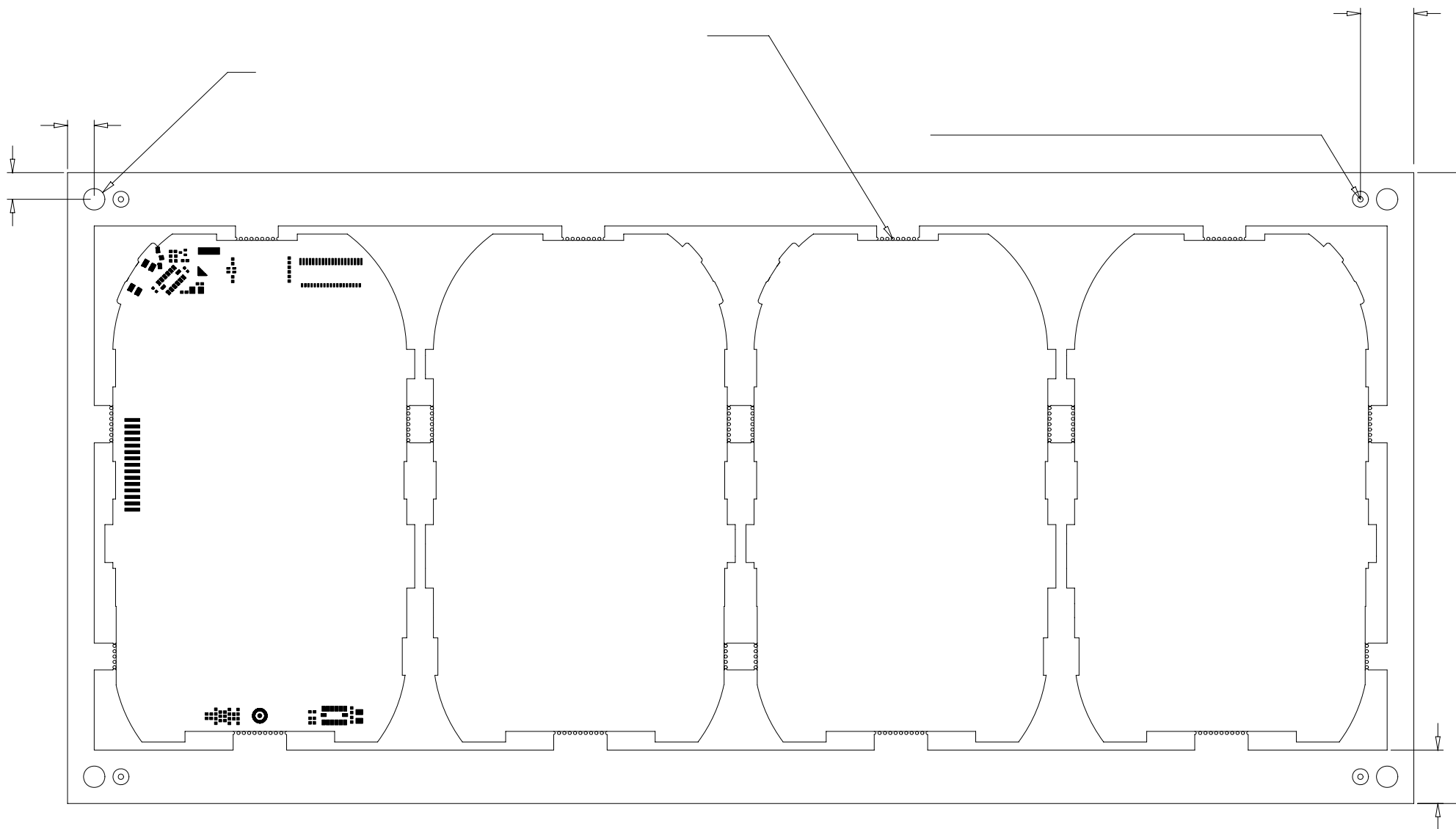
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L8 SOLDER SIDE	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0



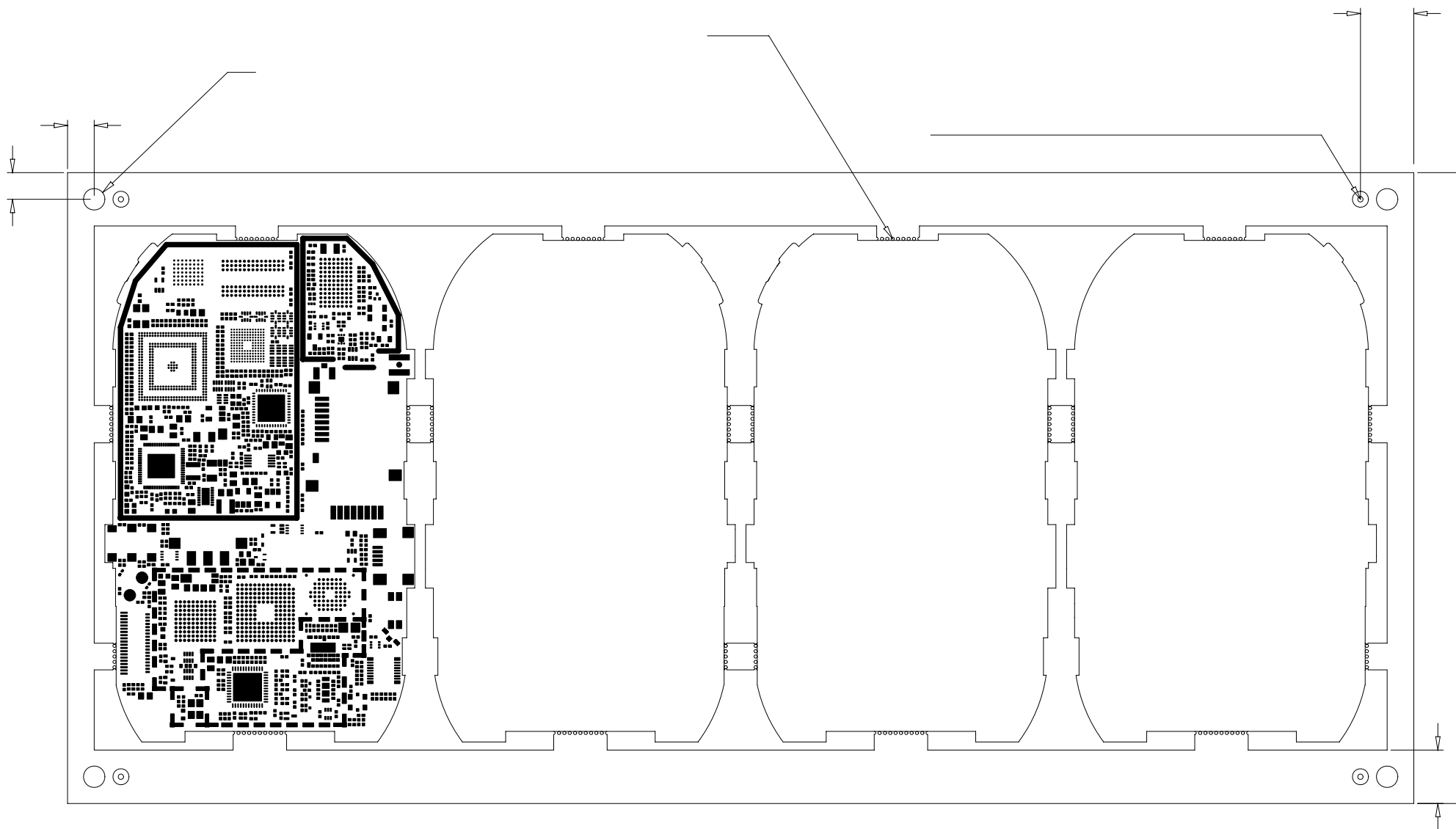
ST	GTA02	8 LAYER
	FIC011	
LAYER:	TOP SOLDER MASK	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0



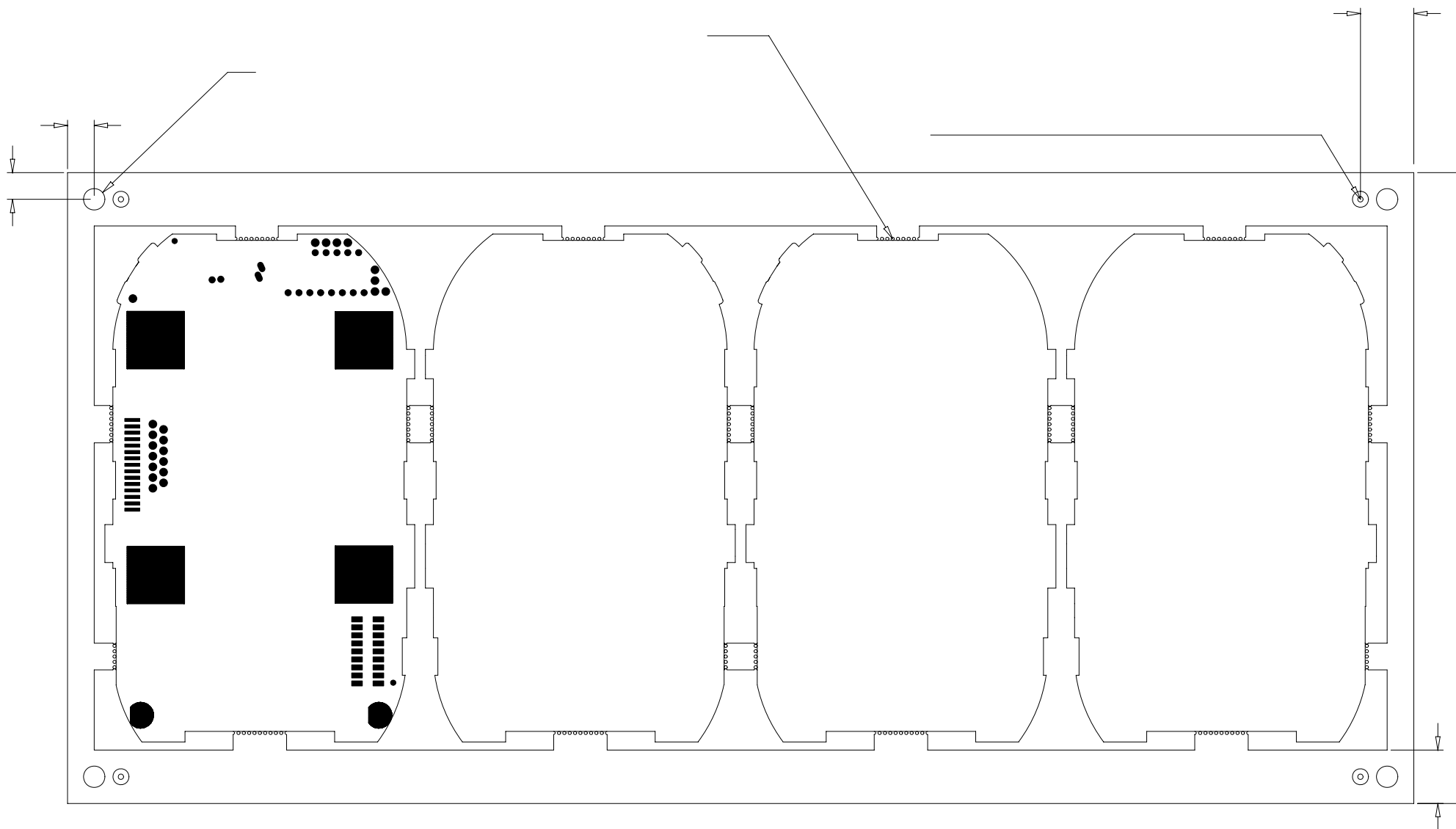
ST	GTA02	8 LAYER
	FIC011	
LAYER:	BO SOLDER MASK	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0



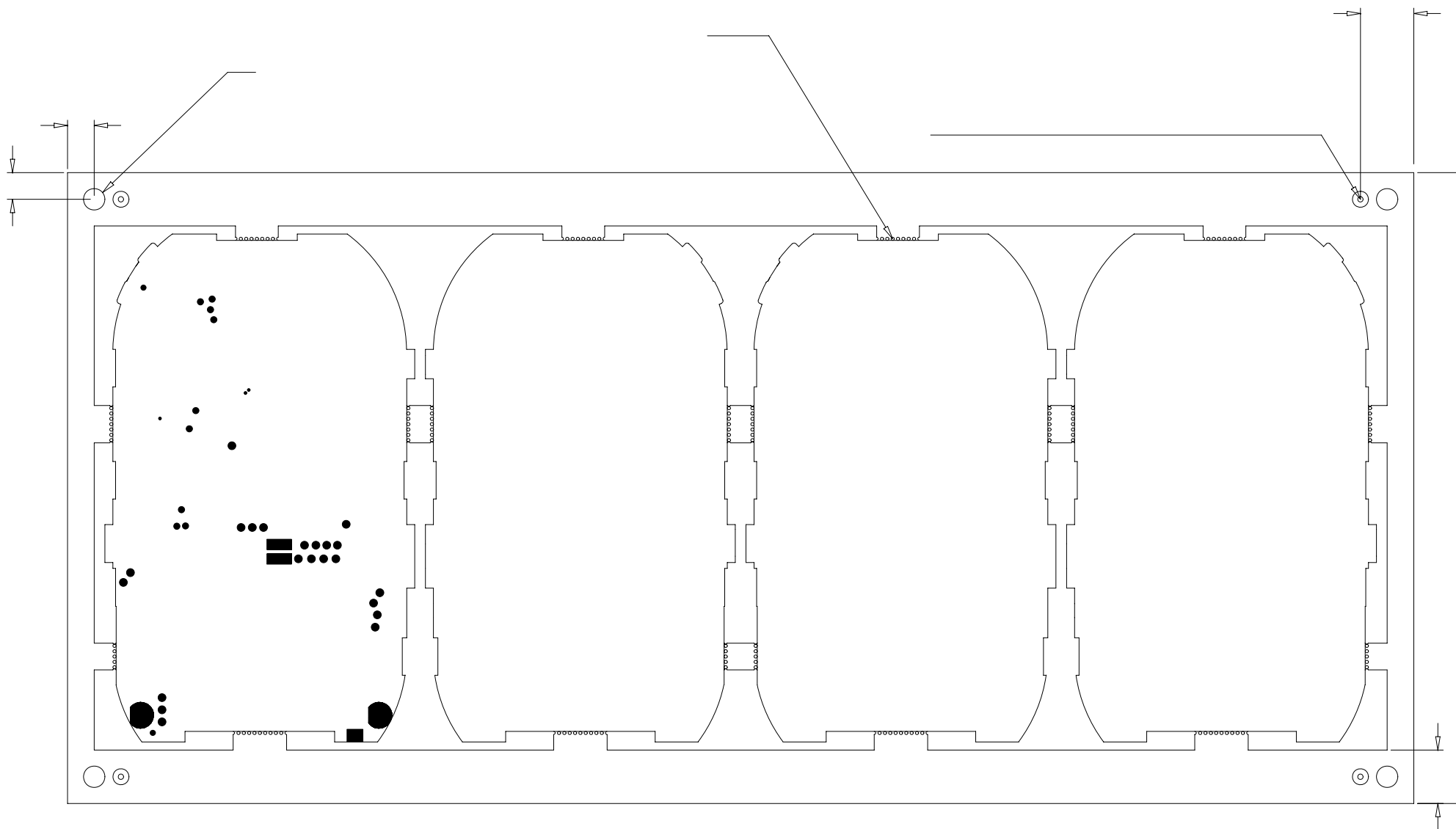
ST	GTA02	8 LAYER
	FIC011	
LAYER: SMD PASTE MASK(TOP)		
DATE: 2007-12-20 12:00		
Clearance error		8
Connectivity error		0



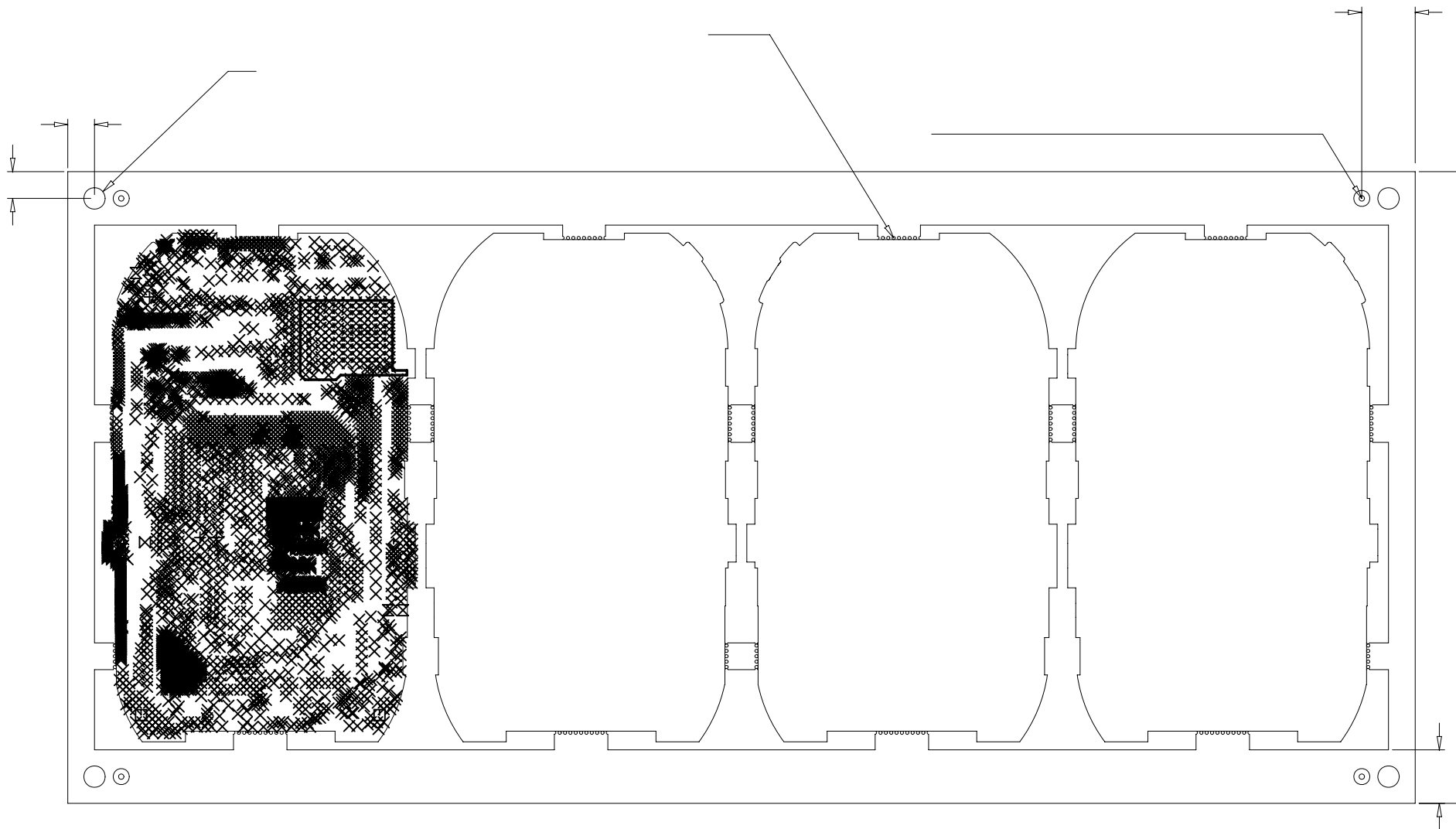
ST	GTA02	8 LAYER
	FIC011	
LAYER: (TOP)2AM 1T2A9 DM2		
DATE: 2007-12-20 12:00		
Clearance error		8
Connectivity error		0



ST	GTA02	8 LAYER
	FIC011	
LAYER:		
DATE: 2007-12-20 12:00		
Clearance error		8
Connectivity error		0

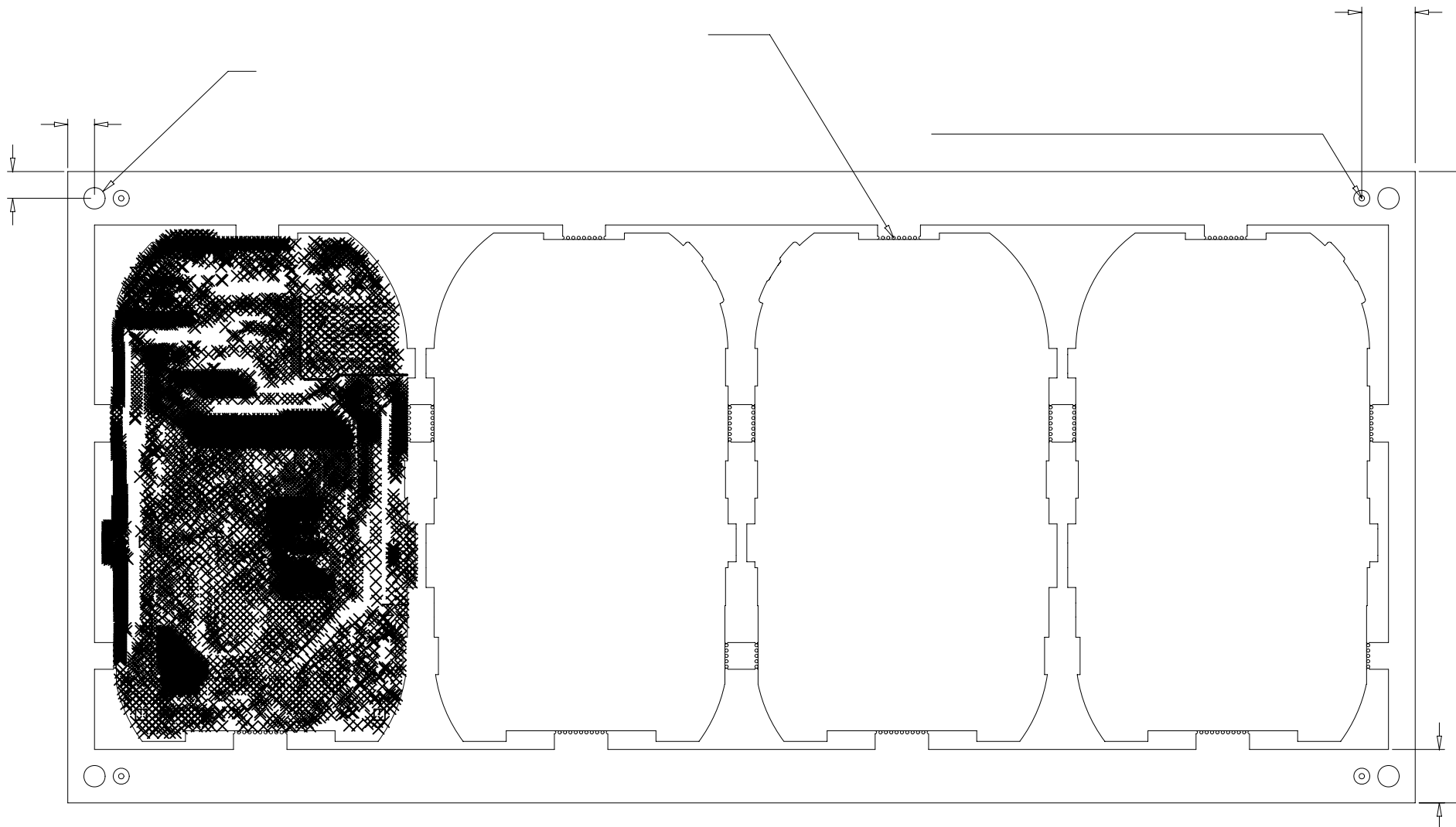


ST	GTA02	8 LAYER
	FIC011	
LAYER:		
DATE: 2007-12-20 12:00		
Clearance error		8
Connectivity error		0



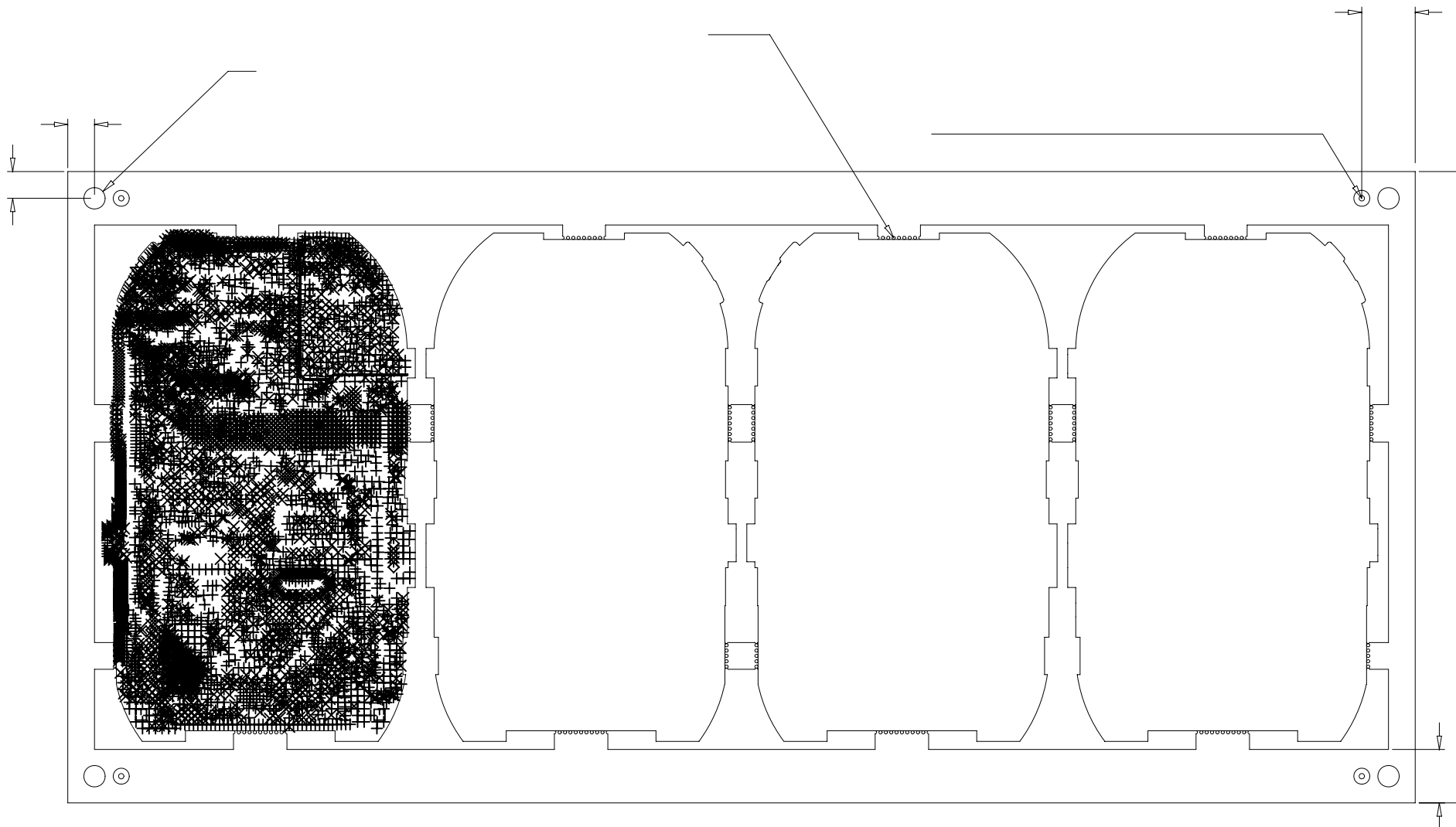
ST	GTA02	8 LAYER
	FIC011	
LAYER: L1 TOP SIDE		DRILL
DATE: 2007-12-20 12:00		
Clearance error		8
Connectivity error		0

SIZE	QTY	SYM	PLATED	TOL
4	3071	✕	YES	+/-0.0
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	⊠	NO	+/-0.0
59.06	2	⊞	NO	+/-0.0
43.31	2	+ ^A	NO	+/-0.0
80	1	+ ^B	NO	+/-0.0



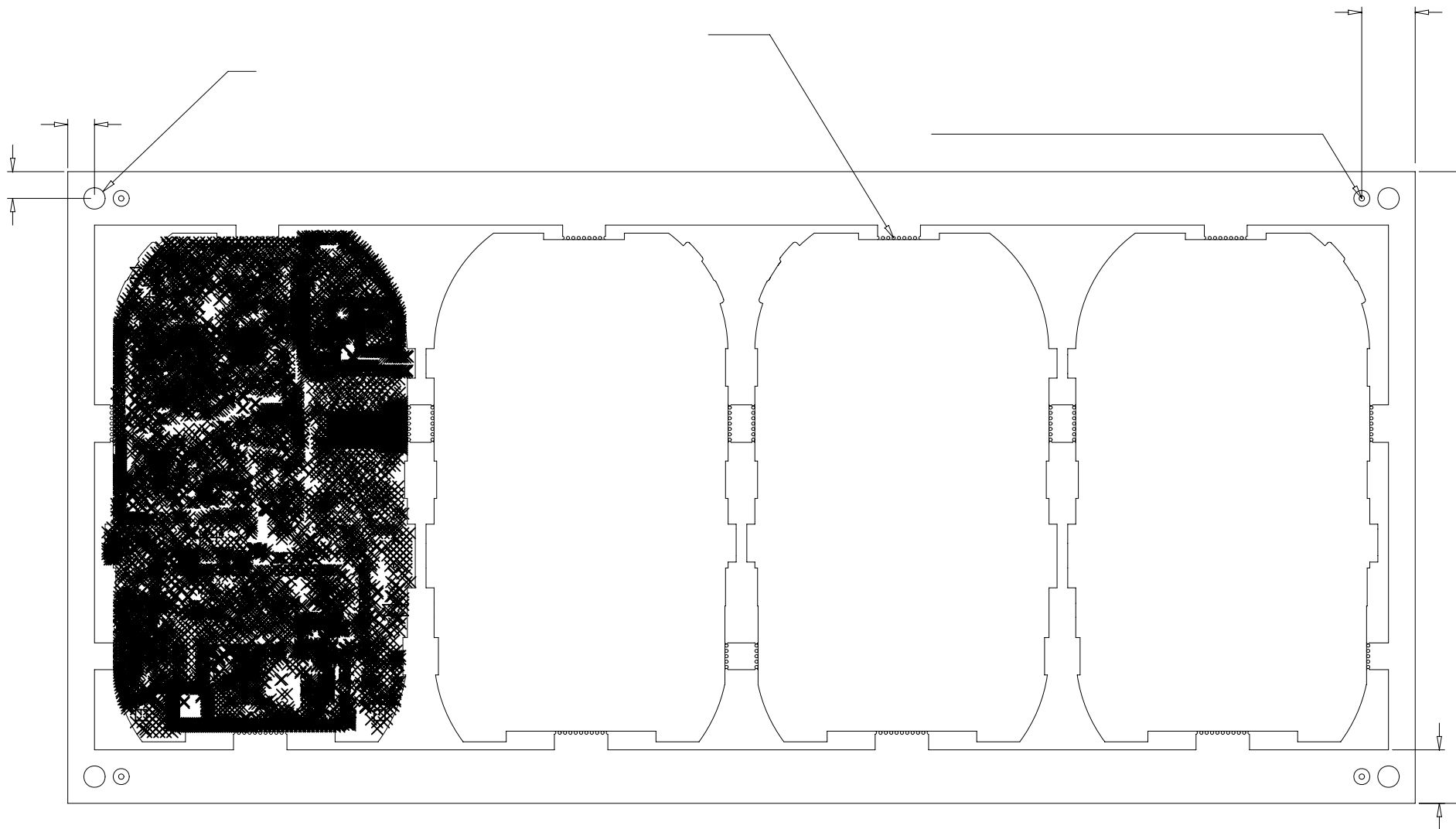
ST	GTA02	8 LAYER
	FIC011	
LAYER: L2 GND(TRK)		DRILL
DATE: 2007-12-20 12:00		
Clearance error		8
Connectivity error		0

SIZE	QTY	SYM	PLATED	TOL
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	⊗	NO	+/-0.0
59.06	2	⊗	NO	+/-0.0
43.31	2	+ ^A	NO	+/-0.0
80	1	+ ^B	NO	+/-0.0



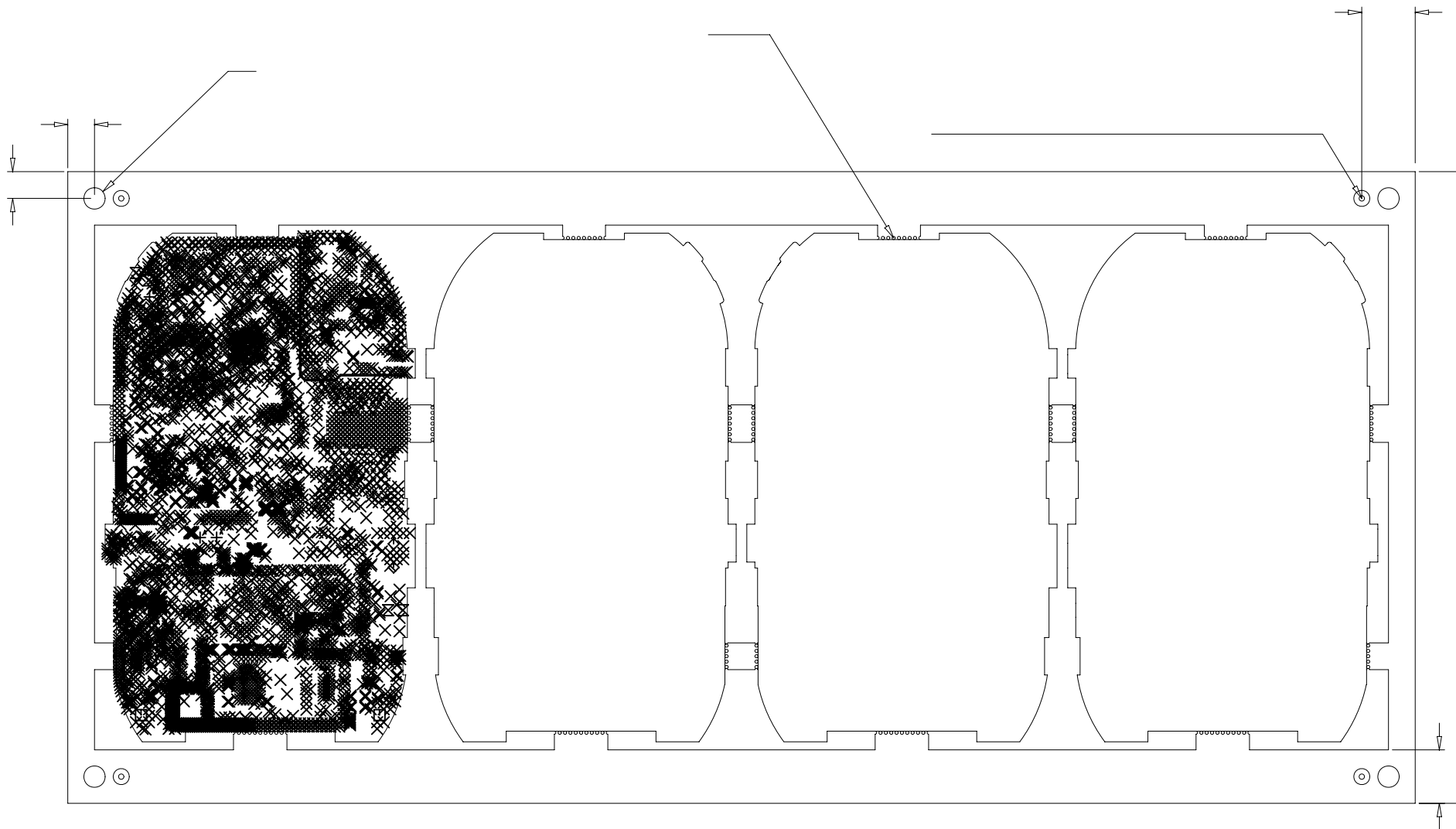
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L3 GND(TRK)	DRILL
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0

SIZE	QTY	SYM	PLATED	TOL
12	2356	+	YES	+/-0.0
4	2573	×	YES	+/-0.0
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	⊠	NO	+/-0.0
59.06	2	⊠	NO	+/-0.0
43.31	2	+ ^A	NO	+/-0.0
80	1	+ ^B	NO	+/-0.0



ST	GTA02	8 LAYER
	FIC011	
LAYER:	L7 GND(TRK)	DRILL
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0

SIZE	QTY	SYM	PLATED	TOL
4	7176	✕	YES	+/-0.0
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	⊠	NO	+/-0.0
59.06	2	⊞	NO	+/-0.0
43.31	2	⊕	NO	+/-0.0
80	1	⊕	NO	+/-0.0



ST	GTA02	8 LAYER
	FIC011	
LAYER:	SOLDER SIDE	
DATE:	2007-12-20 12:00	
Clearance error		8
Connectivity error		0

SIZE	QTY	SYM	PLATED	TOL
4	3718	×	YES	+/-0.0
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	⊠	NO	+/-0.0
59.06	2	⊞	NO	+/-0.0
43.31	2	+ ^A	NO	+/-0.0
80	1	+ ^B	NO	+/-0.0