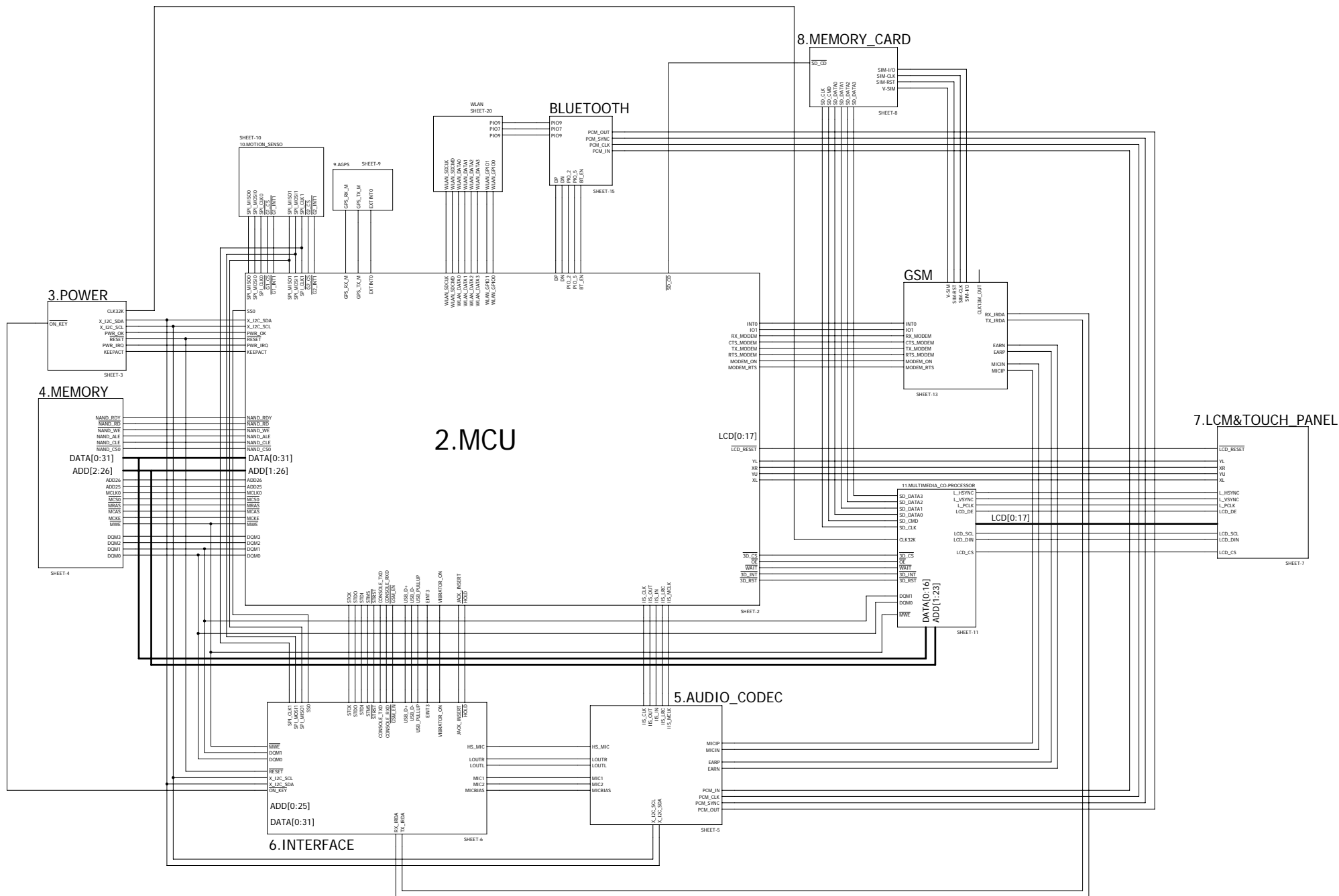


STRUCTURE



MCU LOCATION:15XX

[1,20] WLAN_DATA3
[1,20] WLAN_DATA2
[1,20] WLAN_DATA1
[1,20] WLAN_DATA0
[1,20] WLAN_SDCHD
[1,20] WLAN_SCLK
[1,10] SPI_CLK1
[1,10] SPI_MOSI1
[1,10] SPI_MISO1
[1,4] SPI_CLK0
[1,4] SPI_MOSI0
[1,4] SPI_MISO0
[1,4] CONSOLE_RXD
[1,4] CONSOLE_TXD
[1,9] GPS_TX_M
[1,9] GPS_RX_M
[6] USB_D-
[6] USB_D+
[1,2,13,16] TX_MODEM
[1,13,16] RX_MODEM
[1,13,16] CTS_MODEM
[1,13,16] RTS_MODEM
[1,4] NAND_RDV
[2] NAND_CS0
[1,2] NAND_CS1

[1,4] NAND_RDV
[2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

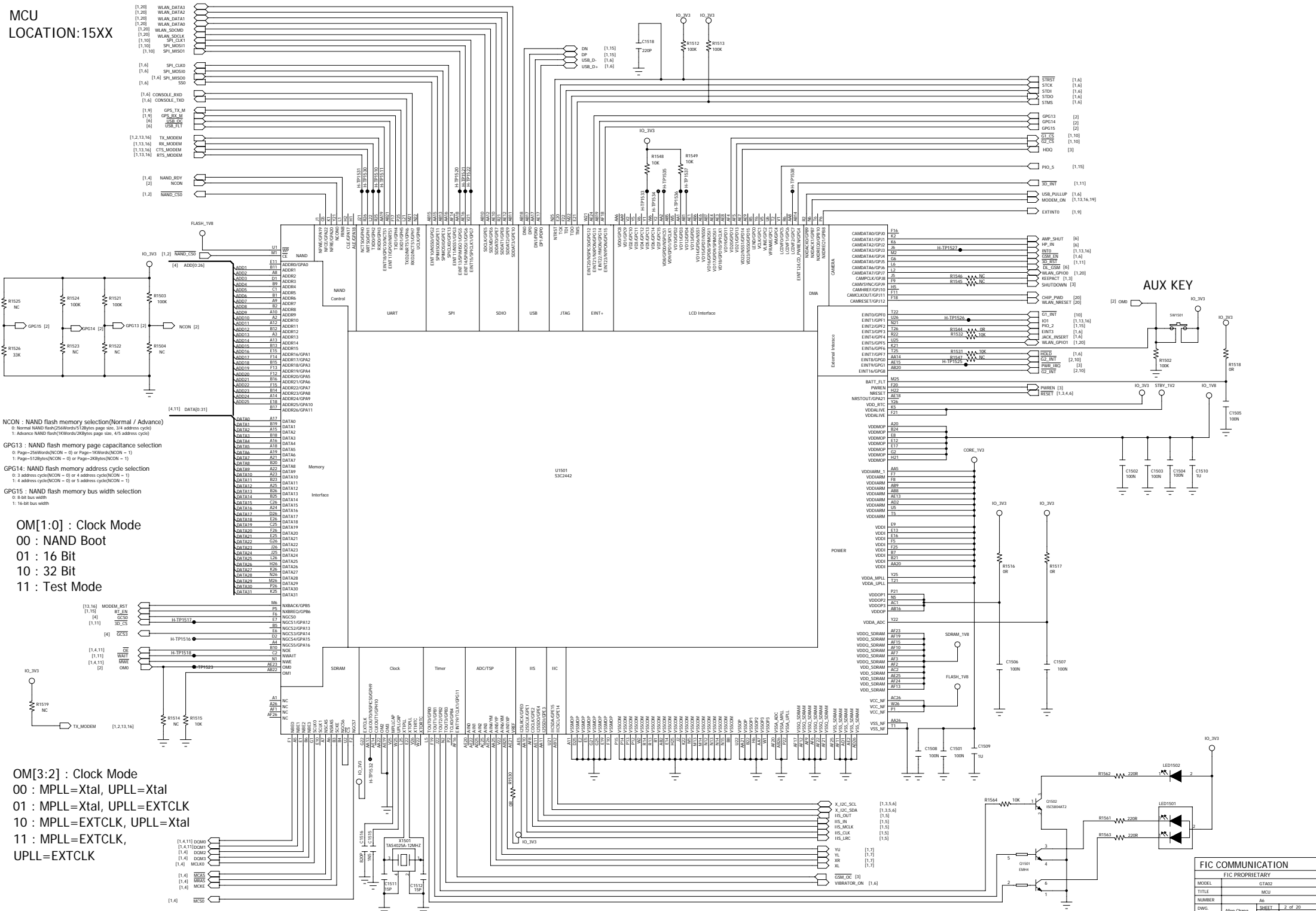
[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1

[1,2] NAND_CS0
[1,2] NAND_CS1



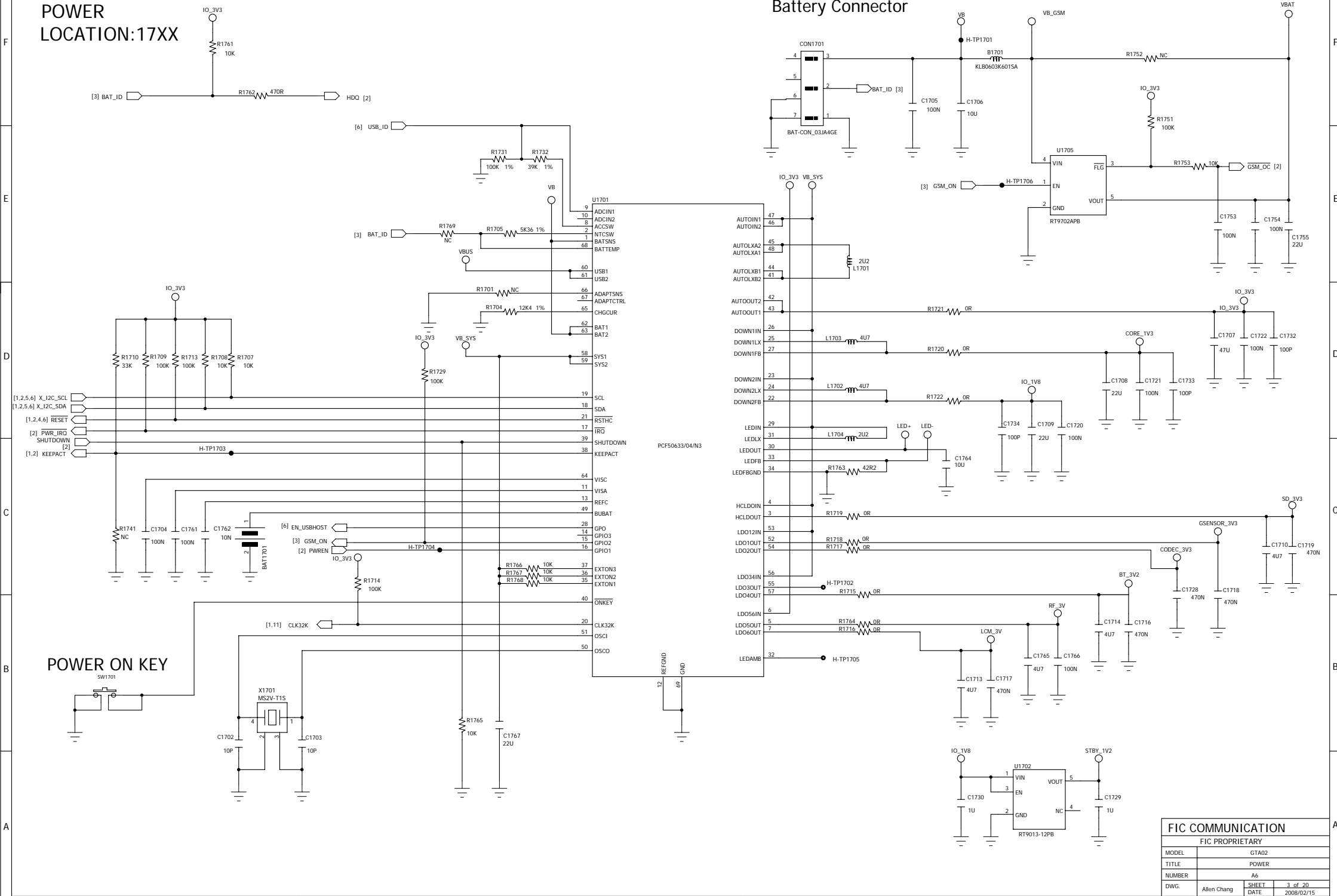
NAND: NAND flash memory selection(Normal / Advance)
0: Normal NAND flash(256KWords/128KWords page size, 34 address cycle)
1: Advance NAND flash(192KWords/256KWords page size, 45 address cycle)
GPG13: NAND flash memory page capacitance selection
0: Page=256Words(NCON = 0) or Page=108Words(NCON = 1)
1: Page=512Words(NCON = 0) or Page=204Words(NCON = 1)
GPG14: NAND flash memory address cycle selection
0: 3 address cycle(NCON = 0) or 4 address cycle(NCON = 1)
1: 4 address cycle(NCON = 0) or 5 address cycle(NCON = 1)
GPG15: NAND flash memory bus width selection
0: 8-bit bus width
1: 16-bit bus width

OM[1:0]: Clock Mode
00: NAND Boot
01: 16 Bit
10: 32 Bit
11: Test Mode

OM[3:2]: Clock Mode
00: MPLL=Xtal, UPLL=Xtal
01: MPLL=Xtal, UPLL=EXTCLK
10: MPLL=EXTCLK, UPLL=Xtal
11: MPLL=EXTCLK, UPLL=EXTCLK

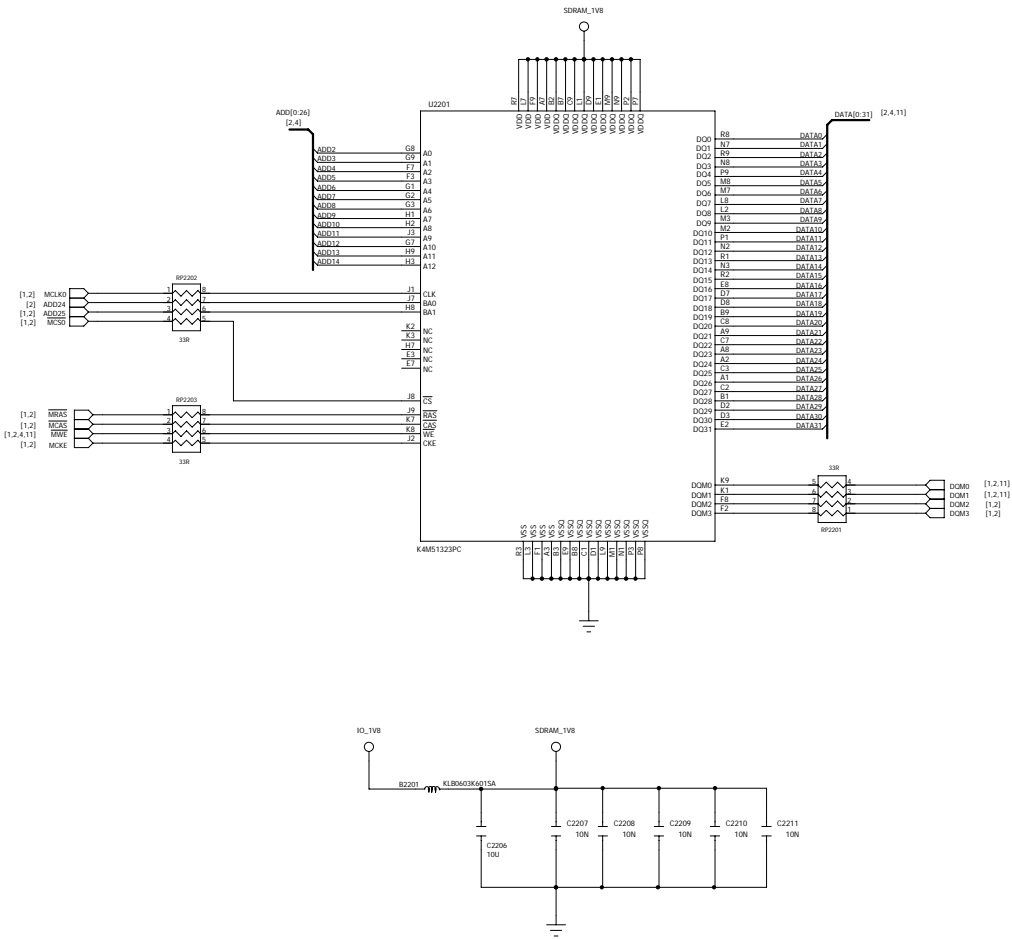
FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GT402		
TITLE	MCU		
NUMBER	A0		
DWG	Allen Chang	SHEET	2 of 20
DATE	2008/02/27		

POWER
LOCATION:17XX

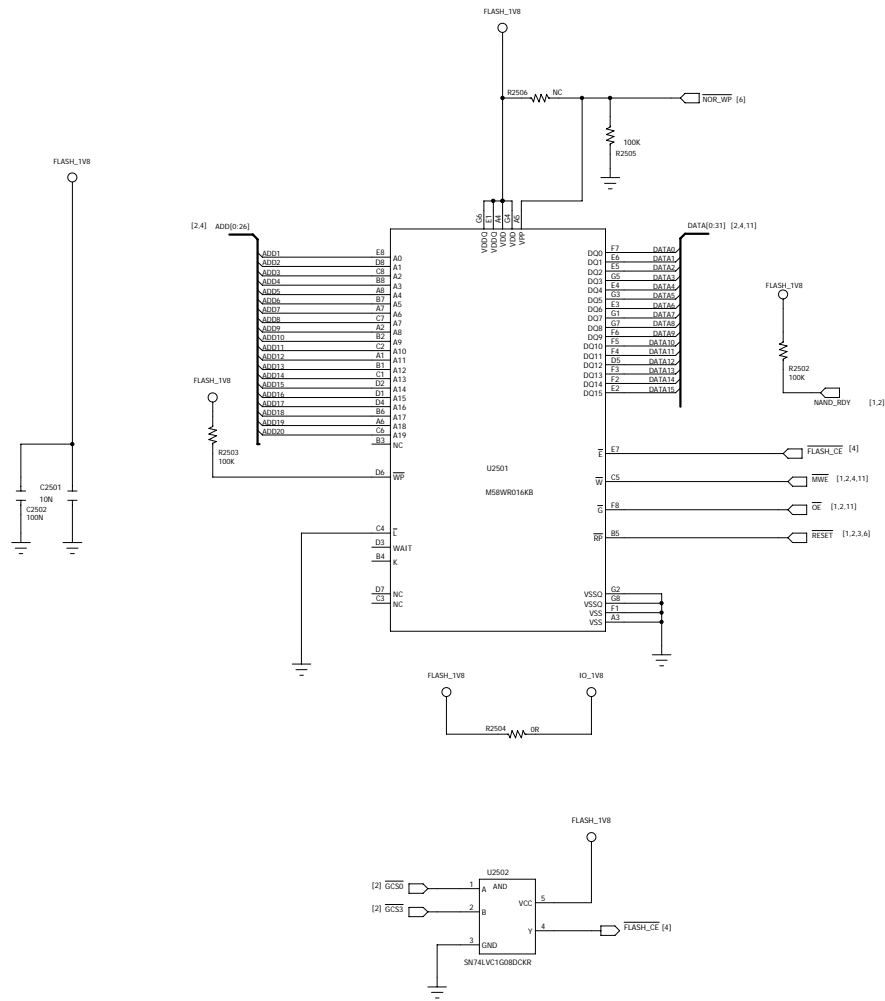


FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	POWER		
NUMBER	A6		
DWG.	Allen Chang	SHEET	3 of 20
		DATE	2008/02/15

SDRAM
LOCATION:22XX

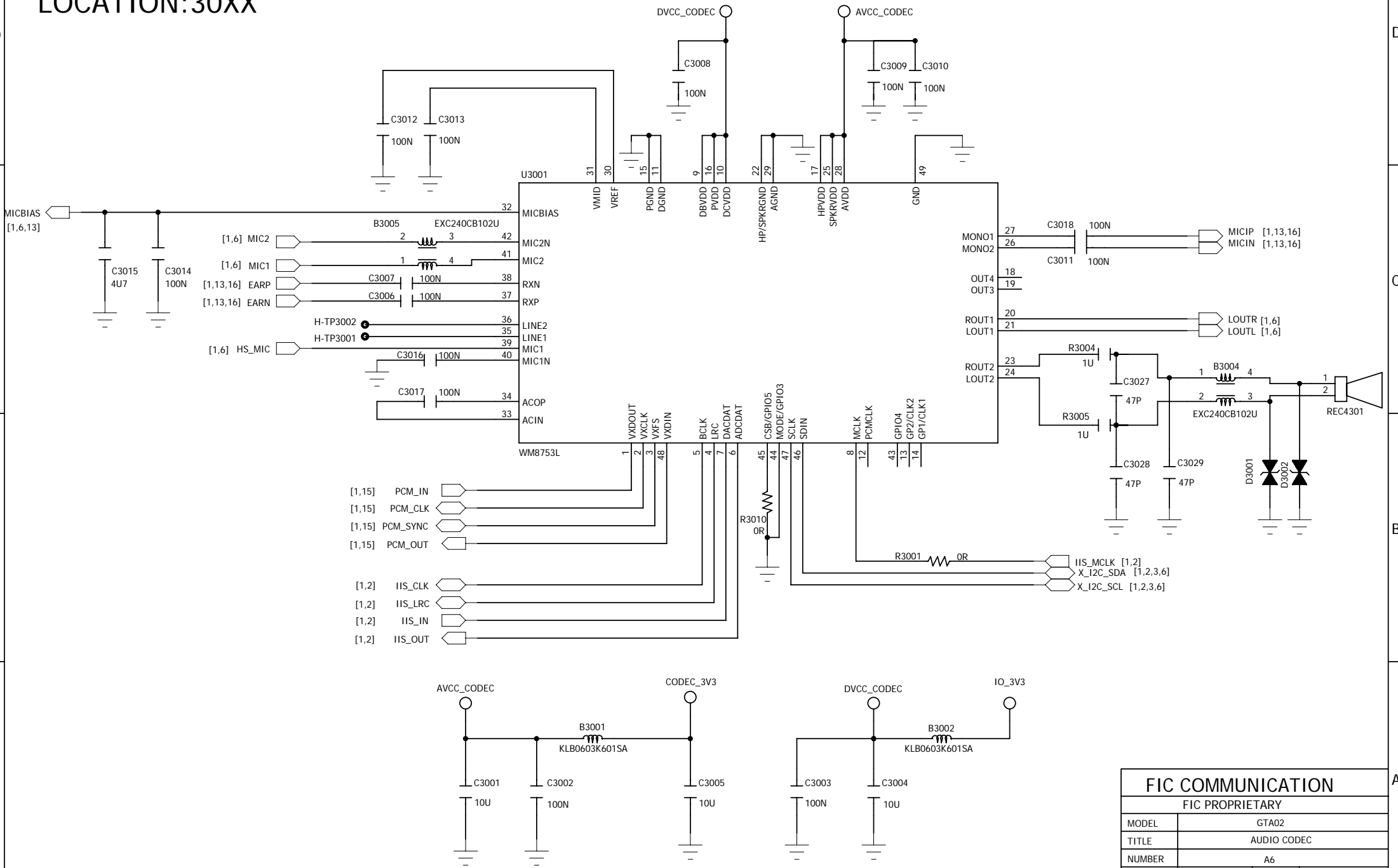


NOR FLASH
LOCATION:25XX



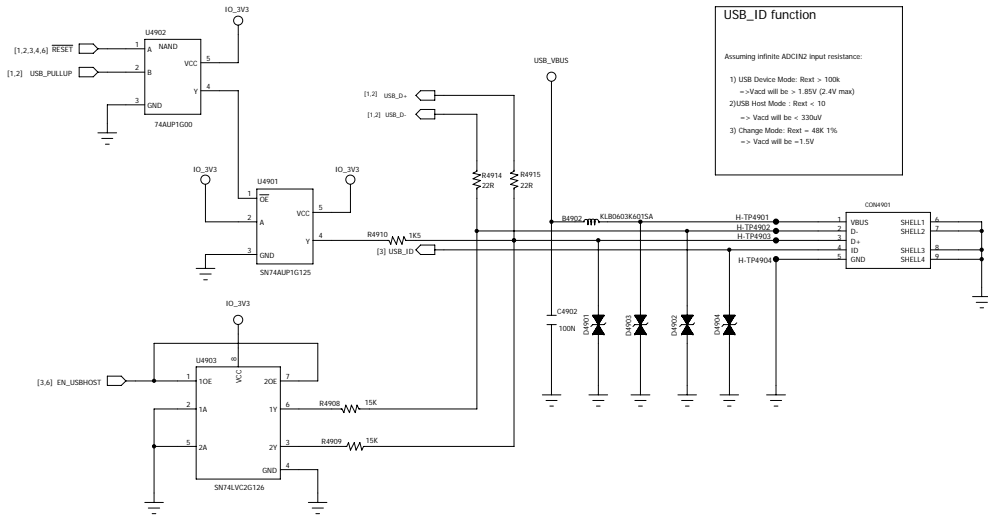
FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	CITAD0		
TITLE	MEMORY		
NUMBER	A6		
DWG:	Allen Chang	SHEET	4 of 20
	DATE		2008/02/19

AUDIO_CODEC
LOCATION:30XX

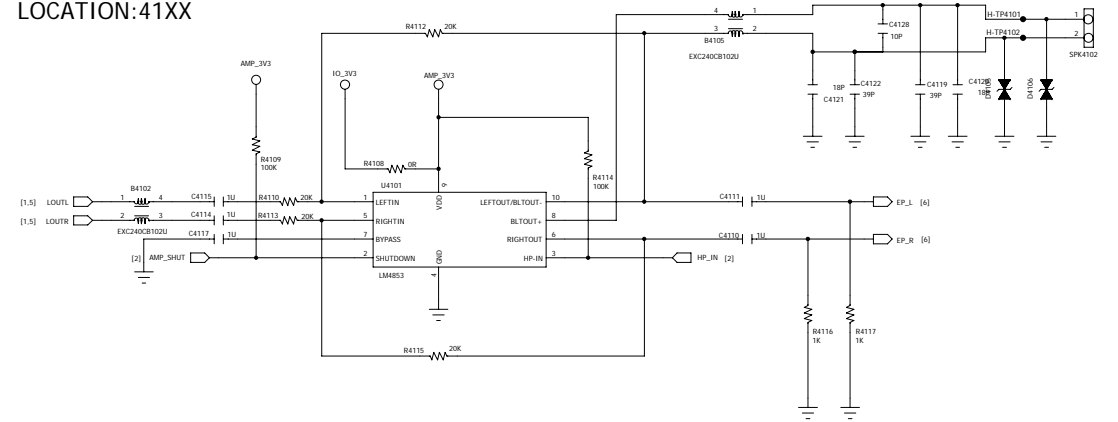


FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	AUDIO_CODEC		
NUMBER	A6		
DWG.	Allen Chang	SHEET	5 of 20
		DATE	2008/02/19

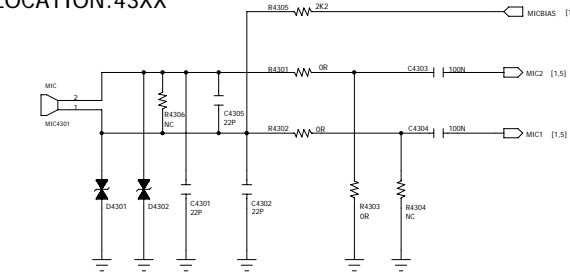
USB LOCATION:49XX



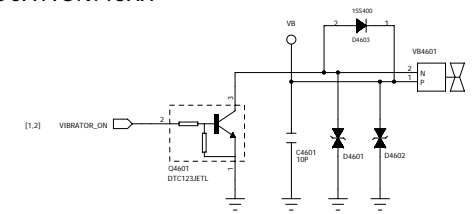
SPK LOCATION:41XX



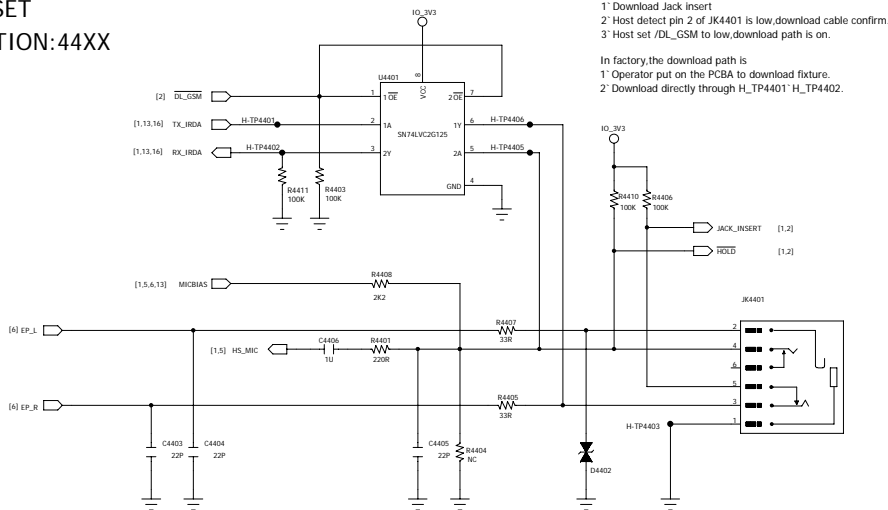
MIC LOCATION:43XX



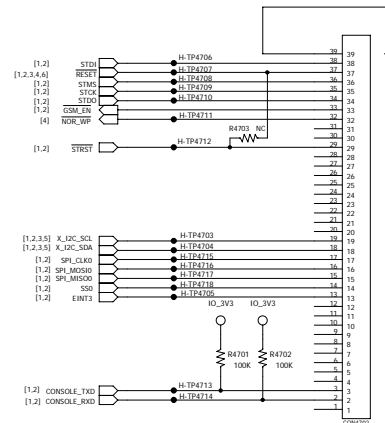
VIBRATOR LOCATION:46XX



HEADSET LOCATION:44XX



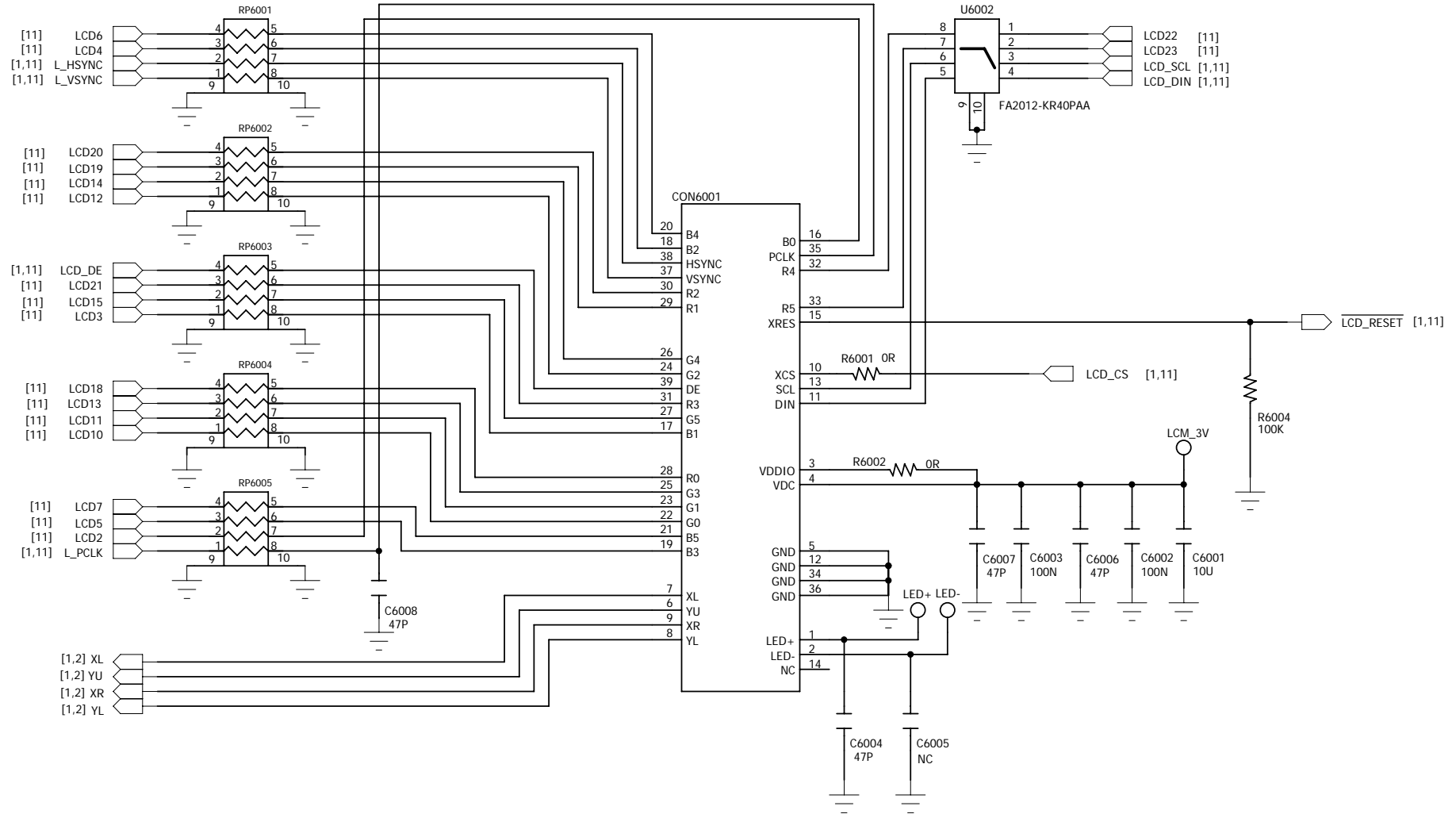
DEBUG LOCATION:47XX



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	CTA02		
TITLE	INTERFACE		
NUMBER	A6		
DWG.	Allen Chang	SHEET	6 of 20
		DATE	2008/02/25

LCM_TOUCH_PANEL

LOCATION:6XXX

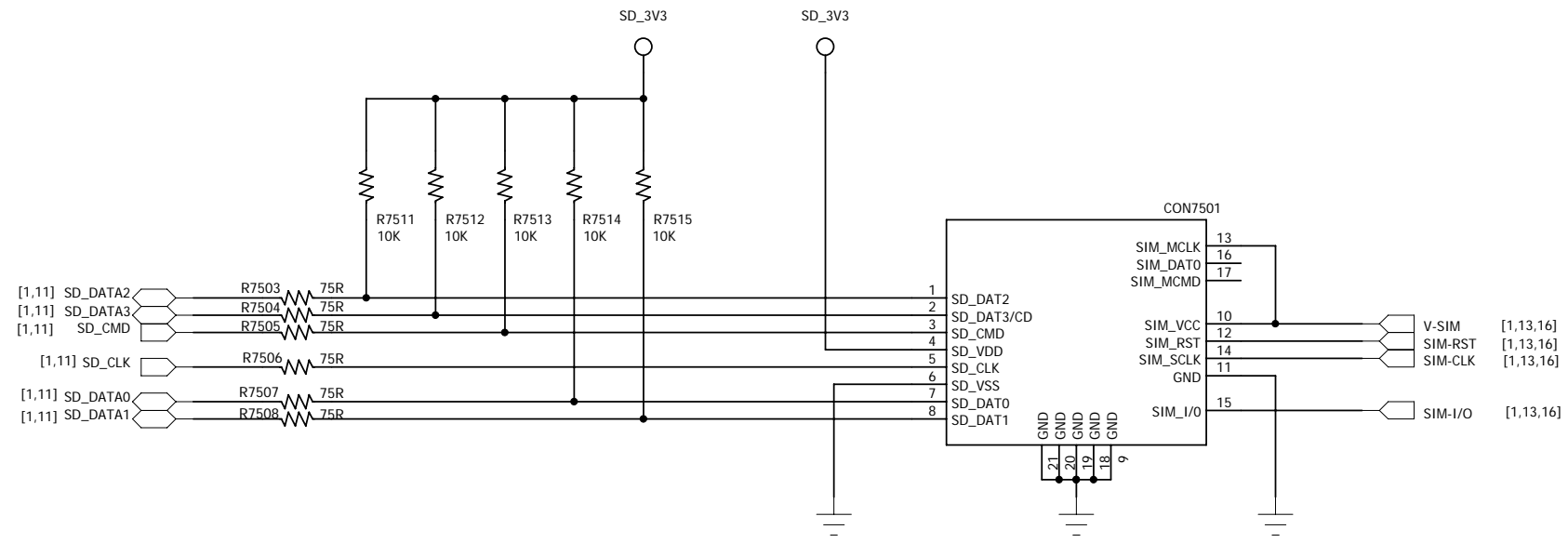


FIC COMMUNICATION

FIC PROPRIETARY

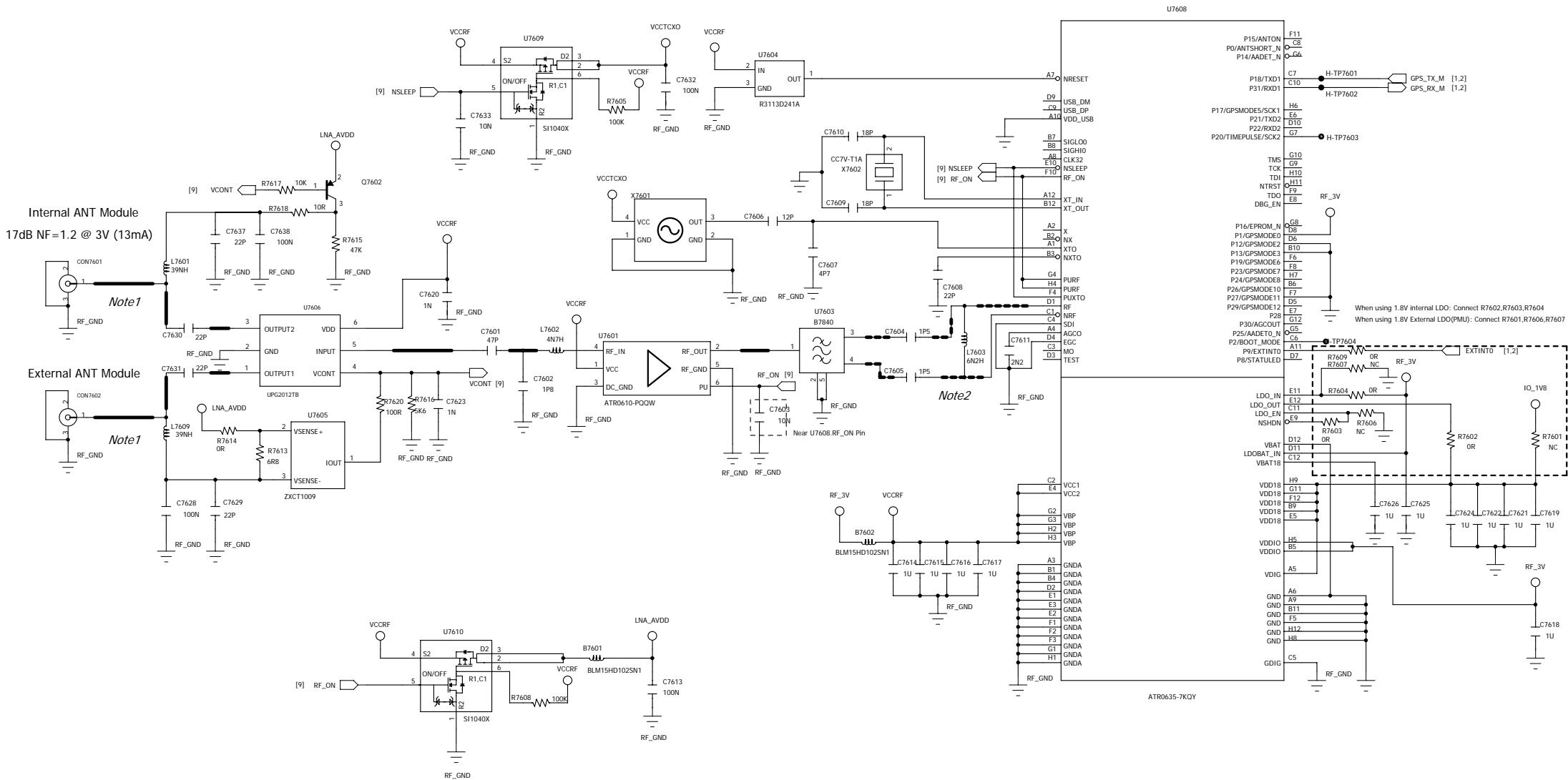
MODEL	GTA02		
TITLE	LCM & TOUCH PANEL		
NUMBER	A6		
DWG.	Allen Chang	SHEET	7 of 20
		DATE	2008/02/19

MEMORY_CARD
LOCATION:75XX



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	MEMORY CARD		
NUMBER	A6		
DWG.	Allen Chang	SHEET	8 of 20
		DATE	2008/02/19

ublox_ANTARIS 4_ATR0635&ATR0610
LOCATION:76XX

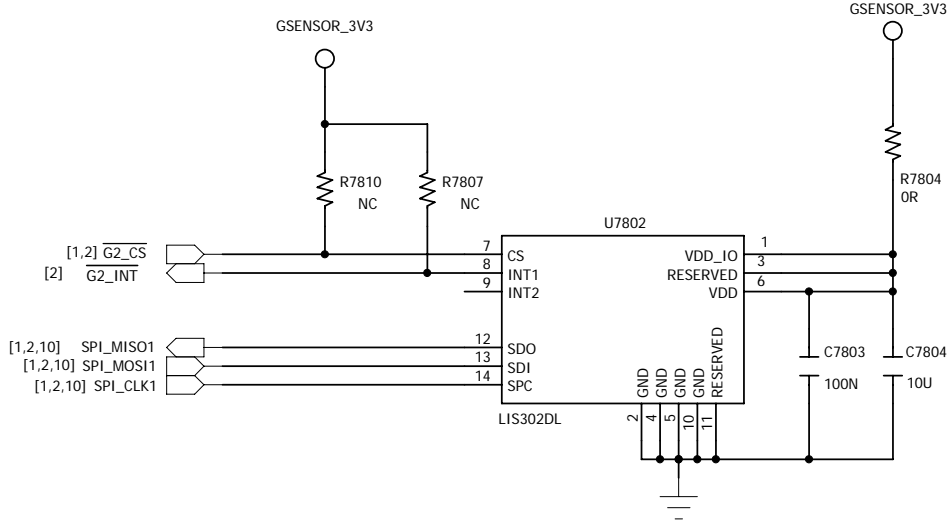
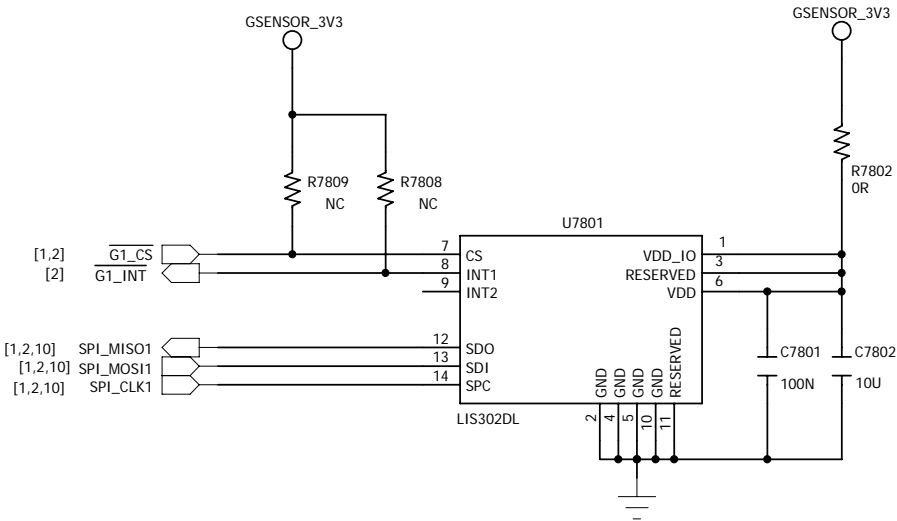


Note1: These traces are RF transmission line and the characteristic impedance is 50 ohm.

Note2: Differential RF traces and the balanced impedance is 100 ohm

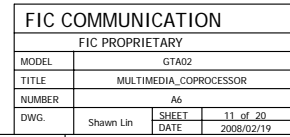
FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	AGPS		
NUMBER	A6		
DWG.	Shawn Lin	SHEET	9 of 20
		DATE	2008/02/19

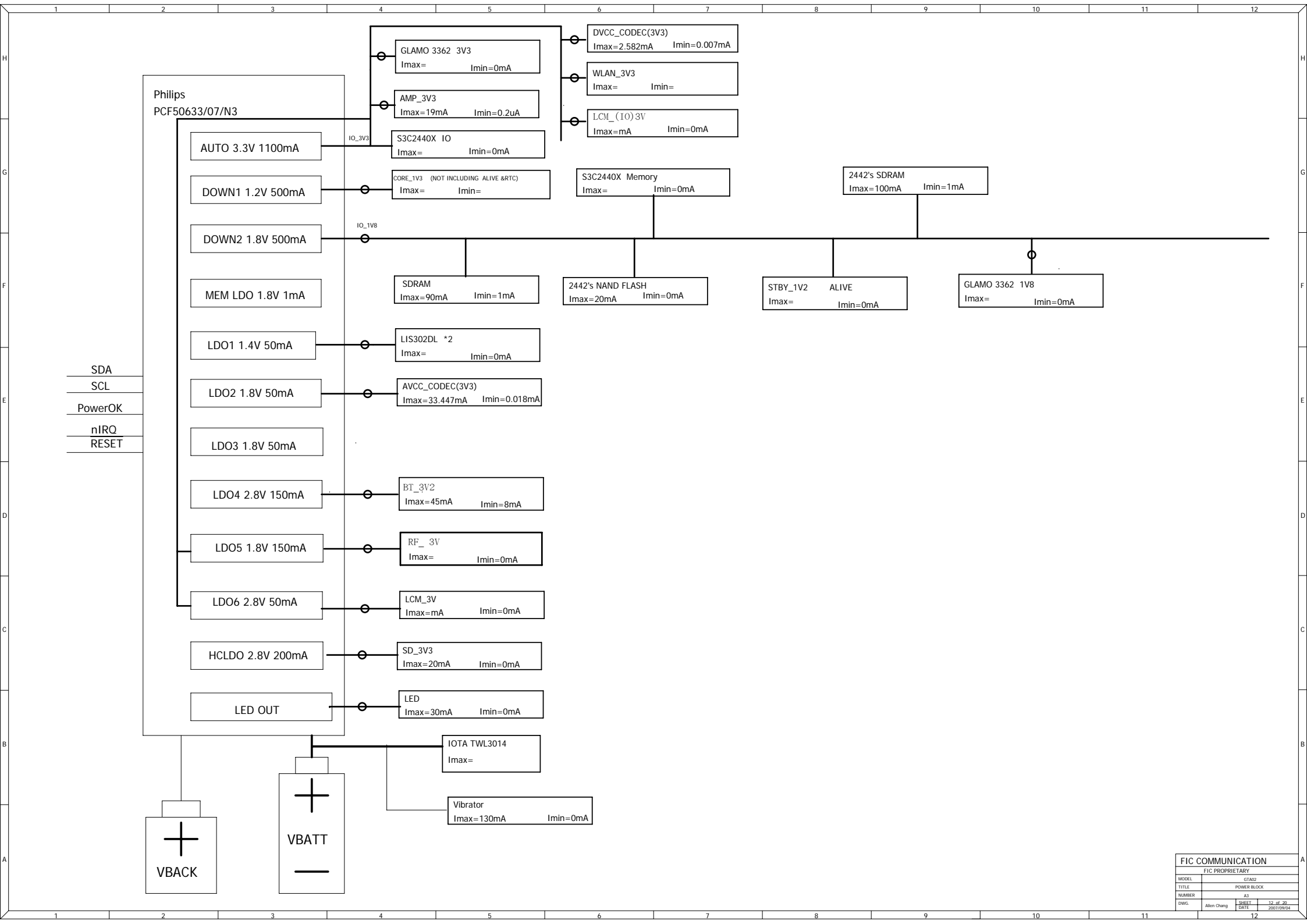
MOTION_SENSOR
LOCATION:78XX



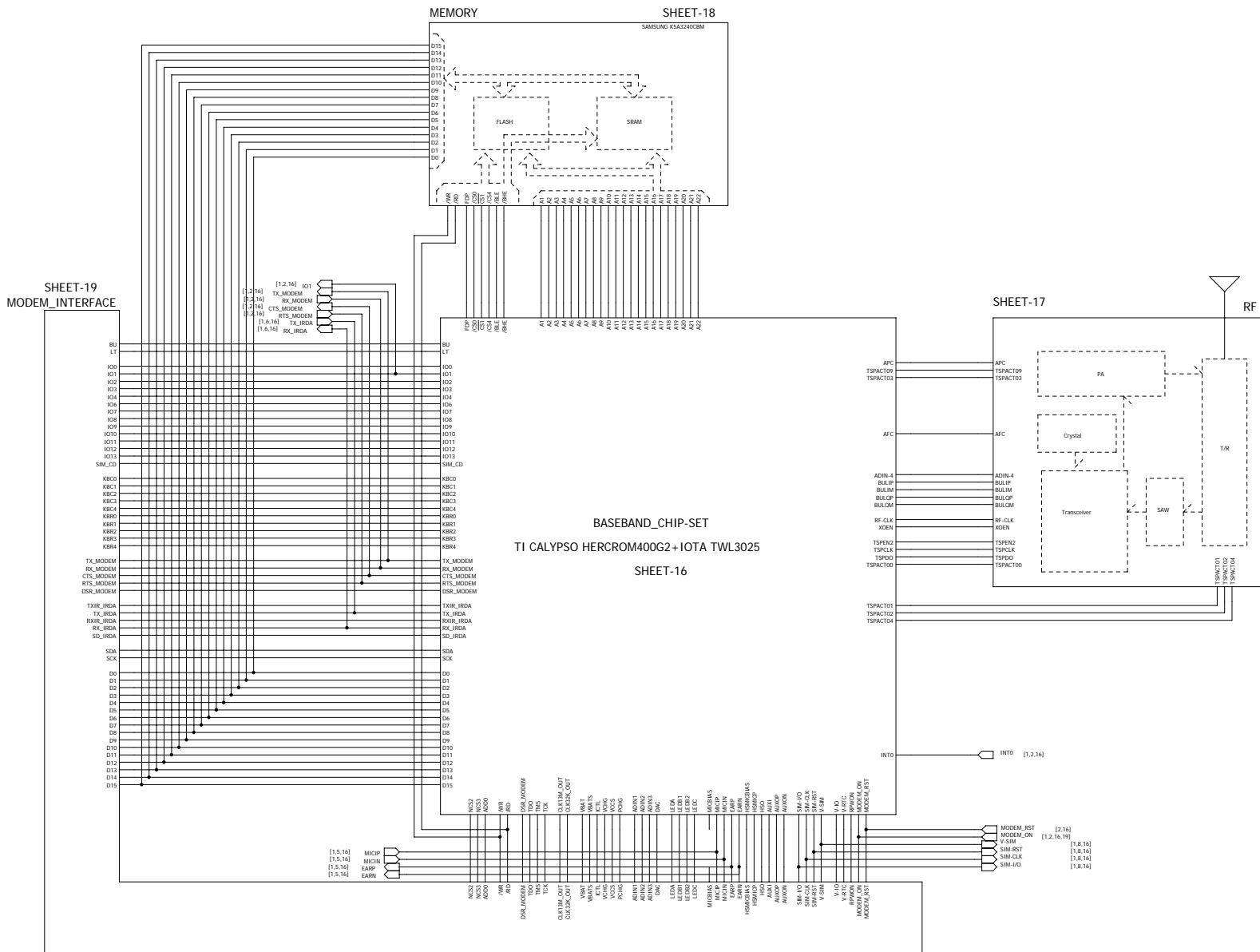
FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	MOTION_SENSOR		
NUMBER	A6		
DWG.	Allen Chang	SHEET	10 of 20
		DATE	2008/02/19

CFG0	CFG1	HCLK	HADV	HA10	Host Bus Type	Description
0	0	0	0	A10	Direct addressing	16-bit 80 type 1
0	0	0	1	A10	Direct addressing	16-bit 80 type 2
0	0	1	N/A	A10	Direct addressing	16-bit 68 type
0	1	0	ADV	A10	Indirect addressing	16-bit 80 type
0	1	1	ADV	A10	Indirect addressing	16-bit 68 type
0	1	0	ADV	1	Indirect addressing	8-bit 80 type
0	1	1	ADV	1	Indirect addressing	8-bit 68 type
1	1	ITF CLK	ADV	NA	Synchronous iBus type	





FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	POWER BLOCK		
NUMBER	A3		
DWG:	Allen Chang	SHEET	12 of 20
		DATE	2007/09/04



FIC COMMUNICATION

FIC PROPRIETARY

MODEL GTM2

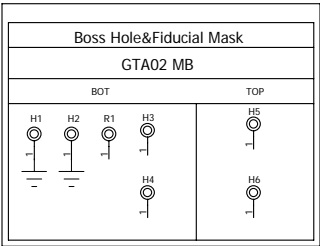
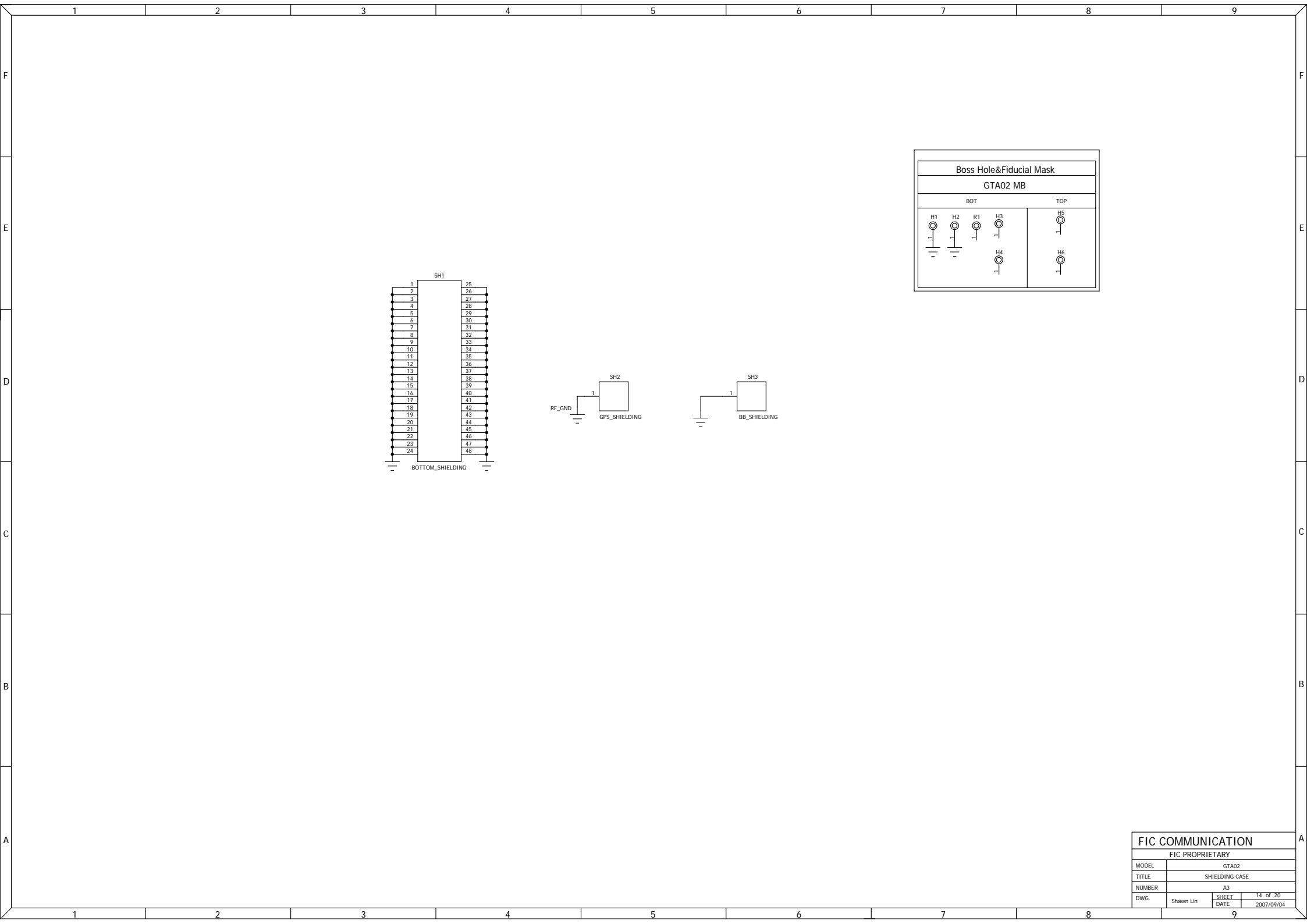
TITLE BLOCK DIAGRAM

NUMBER 13

DWG. Sheen Lin

SHEET 13 OF 20

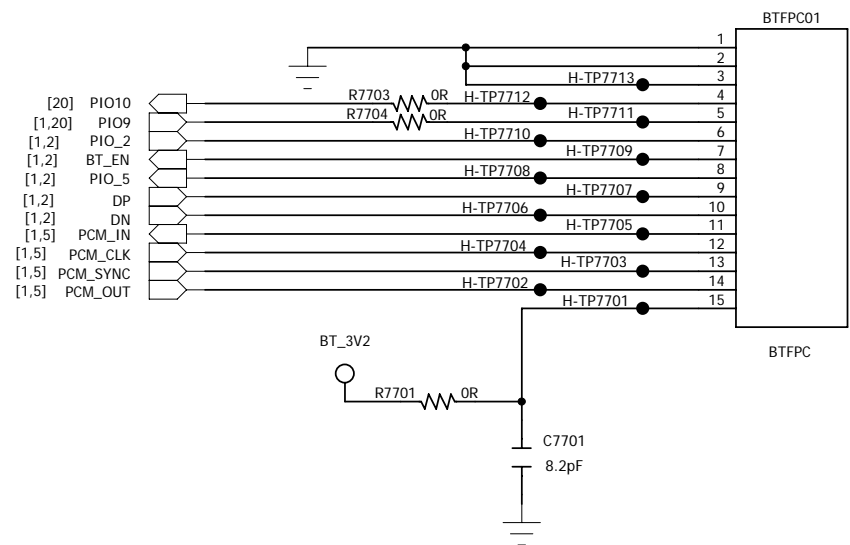
DATE 2008/02/19



FIC COMMUNICATION				
FIC PROPRIETARY				
MODEL	GTA02			
TITLE	SHIELDING CASE			
NUMBER	A3			
DWG.	Shawn Lin	SHEET	14	of 20
		DATE	2007/09/04	

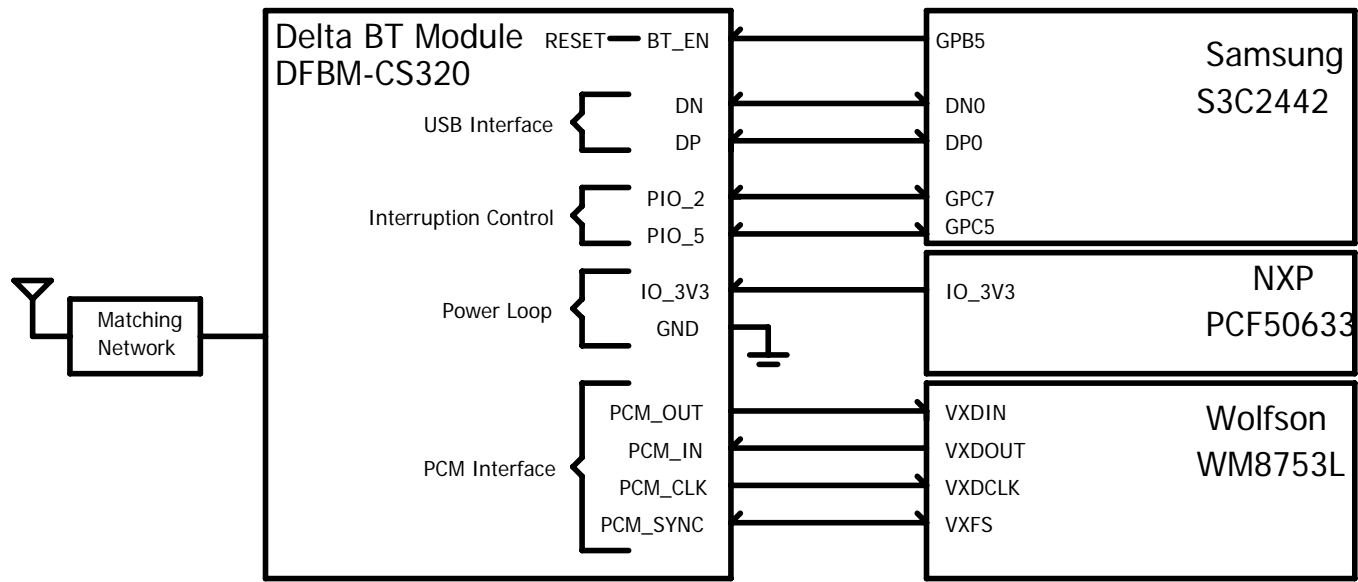
Bluetooth

LOCATION:77XX



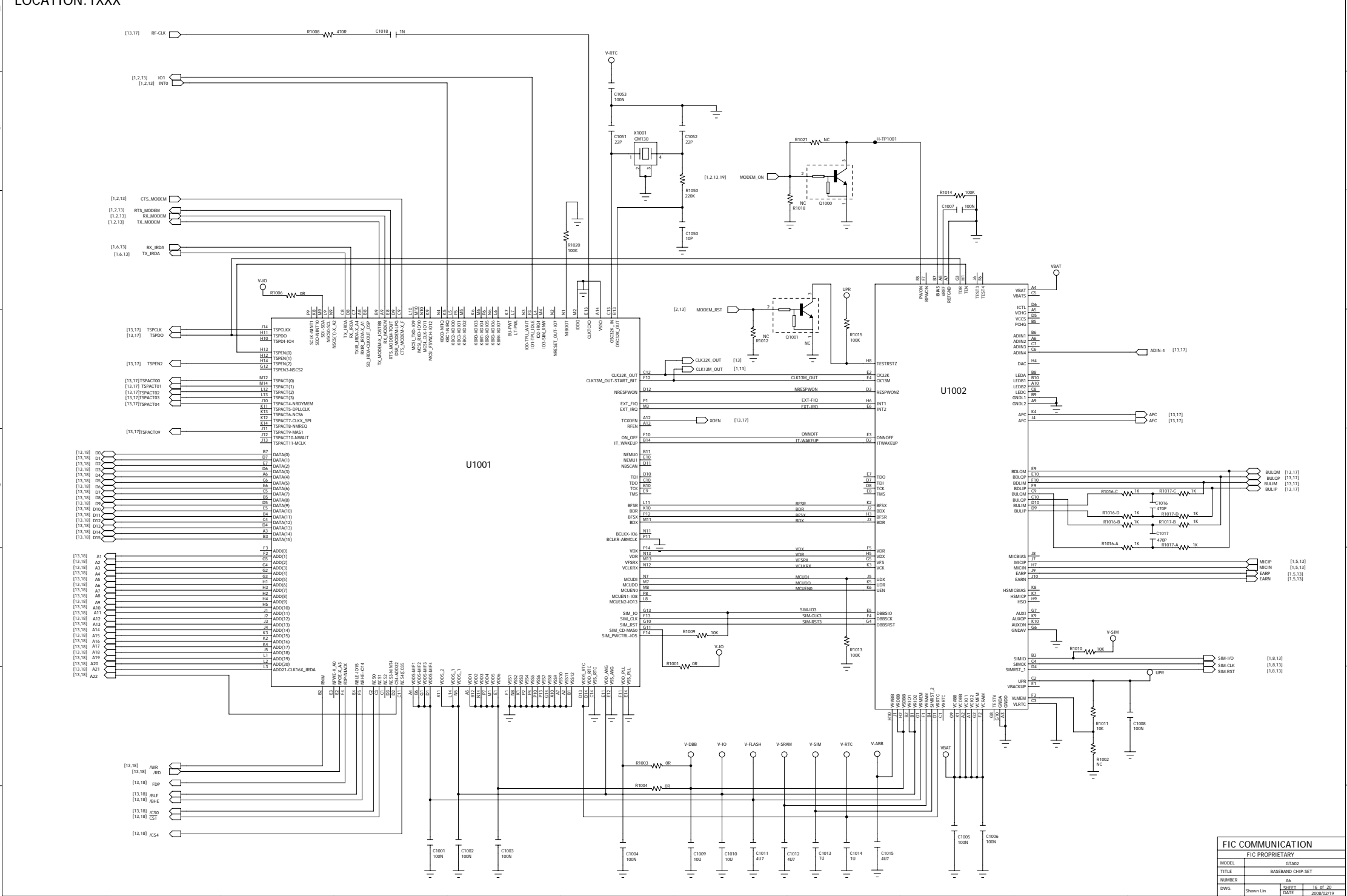
2-wire Co-existence

PIO9	BT_PRIORITY
PIO10	WLAN_ACTIVE



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	Bluetooth		
NUMBER	A6		
DWG.	Shawn Lin	SHEET	15 of 20
		DATE	2008/02/19

BASEBAND_CHIP
LOCATION:1XXX



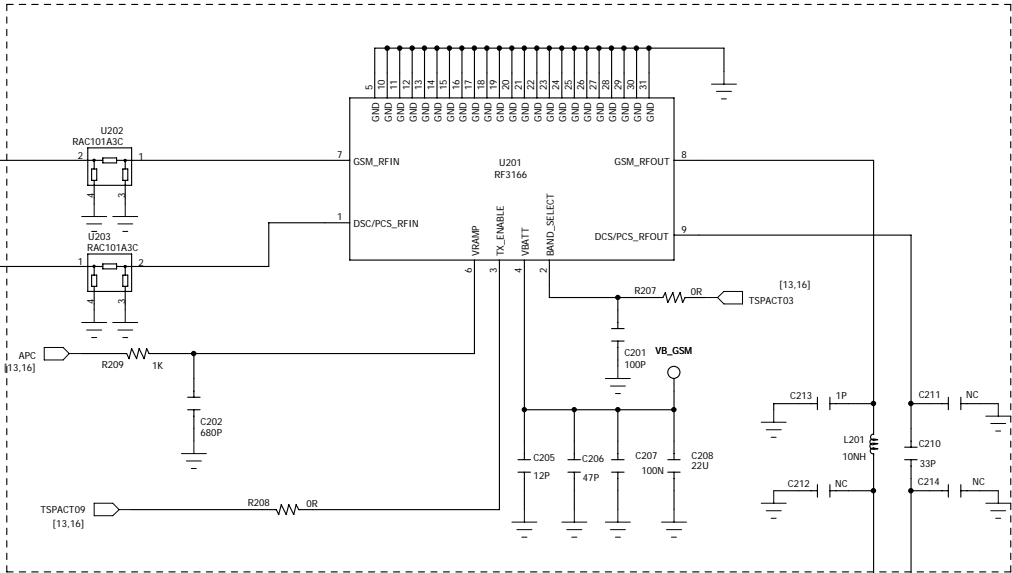
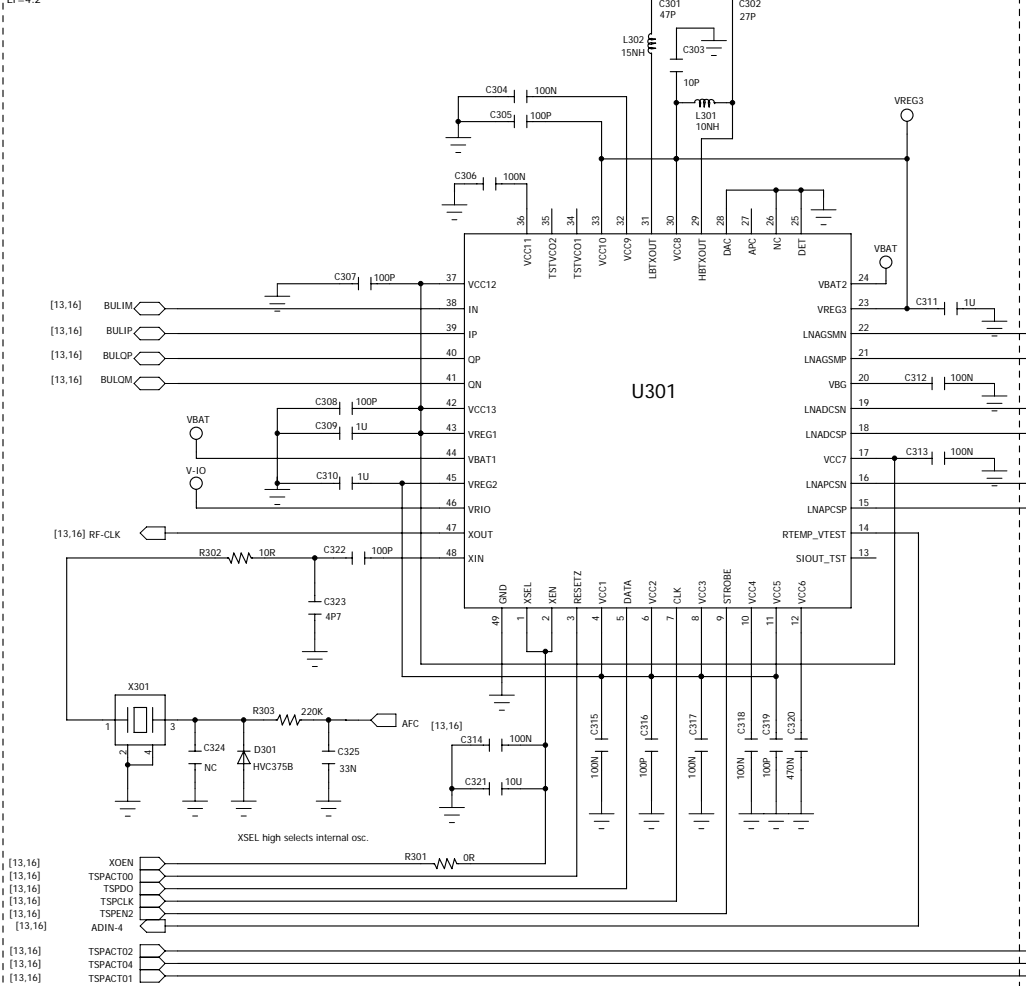
FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTAB2		
TITLE	BASEBAND_CHIP-SET		
NUMBER	No.		
DWG.	Shawn Lin	SHEET	16 of 20
		DATE	2008/02/19

RF
LOCATION:0XXX

LOCATION:03XX

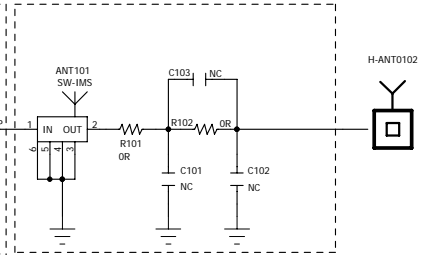
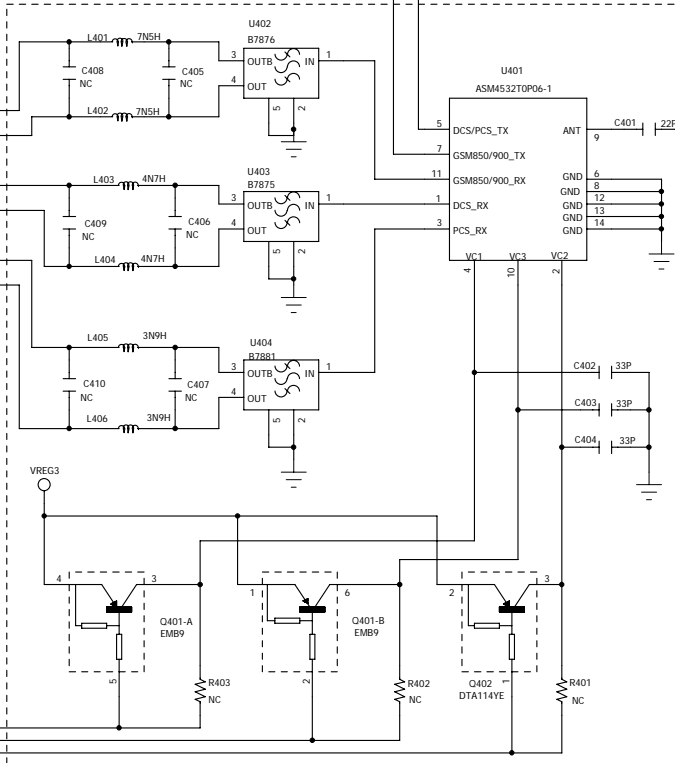
L8	1.40	PP 1078	2.51
L7	1.375(0.50z)	PP 1080	3.31
L6	1.277(10z)	FR4 Core	5.5
L5	1.242(10z)	PP 7628	8.33
L4	1.254(10z)	FR4 Core	4.00
L3	1.273(10z)	PP 1080	3.31
L2	1.525(0.50z)	PP 1078	2.51
L1	1.40		

Unit:mils
Er=4.2



LOCATION:02XX

LOCATION:04XX



LOCATION:01XX

mode	VC1	VC2	VC3
GSM850/900_TX	0V	0V	2.6V
DCS/PCS_TX	2.6V	0V	0V
GSM850/900_RX	0V	0V	0V
DCS_RX	0V	0V	0V
PCS_RX	0V	2.6V	0V

FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	RF		
NUMBER	A6		
DWG.	Shawn Lin	SHEET	17 of 20
		DATE	2008/02/19

MEMORY LOCATION: 2XXX

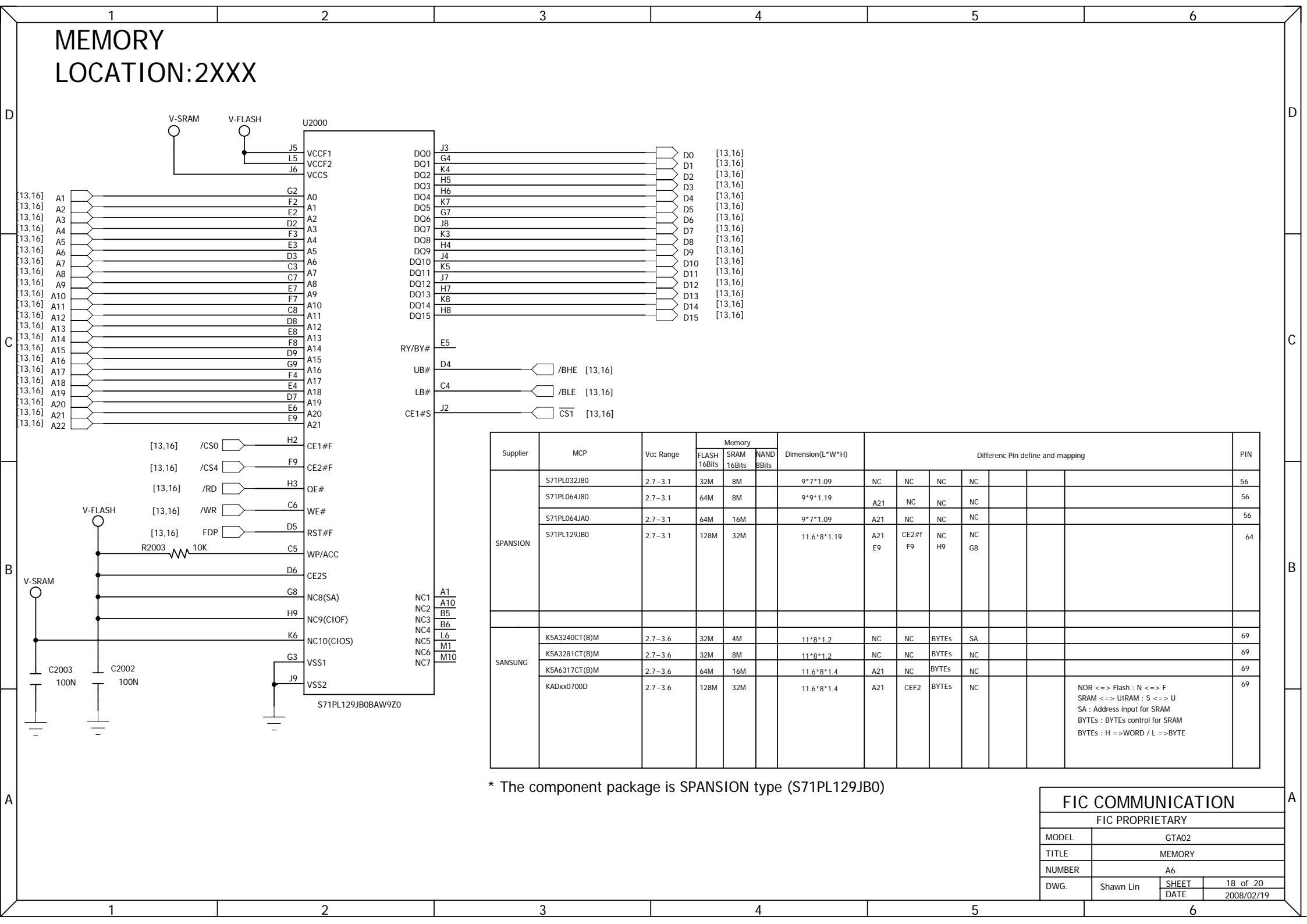
The schematic shows the following components and their connections:

- V-SRAM**: Connected to VSS1 and VSS2.
- V-FLASH**: Connected to VCCF1, VCCF2, and VCCS.
- U2000**: A multi-pin device with connections for address (A0-A22), data (D0-D15), and control signals (CE1#F, CE2#F, OE#, WE#, RST#F, WP/ACC, CE2S, NC8(SA), NC9(CIOF), NC10(CIOS), VSS1, VSS2).
- S71PL129JB0BAW9Z0**: A SPANSION type memory component connected to U2000.
- C2003** and **C2002**: 100N capacitors connected to ground.
- R2003**: A 10K resistor connected to V-FLASH.

Supplier	MCP	Vcc Range	Memory	Dimension(L*W*H)	Differenc Pin define and mapping	PIN												
			FLASH 16Bits	SRAM 16Bits	NAND 8Bits													
SPANSION	S71PL032J80	2.7~3.1	32M	8M		9*7*1.09	NC	NC	NC	NC							56	
	S71PL064J80	2.7~3.1	64M	8M		9*9*1.19	A21	NC	NC	NC							56	
	S71PL064JA0	2.7~3.1	64M	16M		9*7*1.09	A21	NC	NC	NC							56	
	S71PL129JB0	2.7~3.1	128M	32M		11.6*8*1.19	A21	E9	CE2#f F9	NC H9	NC G8						64	
SANSUNG	K5A3240CT(B)M	2.7~3.6	32M	4M		11*8*1.2	NC	NC	BYTES	SA								69
	K5A3281CT(B)M	2.7~3.6	32M	8M		11*8*1.2	NC	NC	BYTES	NC								69
	K5A6317CT(B)M	2.7~3.6	64M	16M		11.6*8*1.4	A21	NC	BYTES	NC								69
	KADxx0700D	2.7~3.6	128M	32M		11.6*8*1.4	A21	CEF2	BYTES	NC								69

* The component package is SPANSION type (S71PL129JB0)

FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	MEMORY		
NUMBER	A6		
DWG.	Shawn Lin	SHEET	18 of 20
		DATE	2008/02/19



MEMORY LOCATION: 2XXX

The schematic shows the following components and their connections:

- V-SRAM**: Connected to C2003 (100N) and C2002 (100N).
- V-FLASH**: Connected to R2003 (10K) and C2002 (100N).
- U2000**: A memory device with pins J5, L5, J6, G2, F2, E2, D2, F3, E3, D3, C3, C7, E7, F7, C8, D8, E8, F8, D9, G9, F4, E4, D7, E6, E9, H2, F9, H3, C6, D5, C5, D6, G8, H9, K6, G3, J9.
- Control Signals**: /CS0, /CS4, /RD, /WR, FDP, RST#F, WP/ACC, CE2S, NC8(SA), NC9(CIOF), NC10(CIOS), VSS1, VSS2.
- Memory Banks**: DO0-DQ15, DO1-DQ15, DO2-DQ15, DO3-DQ15, DO4-DQ15, DO5-DQ15, DO6-DQ15, DO7-DQ15, DO8-DQ15, DO9-DQ15, DO10-DQ15, DO11-DQ15, DO12-DQ15, DO13-DQ15, DO14-DQ15, DO15-DQ15.
- Other Pins**: J3, G4, H4, K4, H5, H6, K7, G7, J8, K3, H4, J4, K5, J7, H7, K8, H8, E5, D4, C4, J2, CS1, UB#, LB#, CE1#S, NC1, NC2, NC3, NC4, NC5, NC6, NC7, A1, A10, B5, B6, L6, M1, M10.

Supplier	MCP	Vcc Range	Flash 16Bits	SRAM 16Bits	NAND 8Bits	Dimension(L*W*H)	Differenc Pin define and mapping	PIN
SPANSION	S71PL032JB0	2.7~3.1	32M	8M		9*7*1.09	NC, NC, NC, NC	56
	S71PL064JB0	2.7~3.1	64M	8M		9*9*1.19	A21, NC, NC, NC	56
	S71PL064JA0	2.7~3.1	64M	16M		9*7*1.09	A21, NC, NC, NC	56
	S71PL129JB0	2.7~3.1	128M	32M		11.6*8*1.19	A21, E9, CE2#f, F9, NC, H9, NC, G8	64
SAMSUNG	K5A3240CT(B)M	2.7~3.6	32M	4M		11*8*1.2	NC, NC, BYTES, SA	69
	K5A3281CT(B)M	2.7~3.6	32M	8M		11*8*1.2	NC, NC, BYTES, NC	69
	K5A6317CT(B)M	2.7~3.6	64M	16M		11.6*8*1.4	A21, NC, BYTES, NC	69
	KADxx0700D	2.7~3.6	128M	32M		11.6*8*1.4	A21, CEF2, BYTES, NC	69

* The component package is SPANSION type (S71PL129JB0)

FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	MEMORY		
NUMBER	A6		
DWG.	Shawn Lin	SHEET	18 of 20
		DATE	2008/02/19

MEMORY LOCATION: 2XXX

The schematic shows the following components and their connections:

- V-SRAM**: Connected to VSS1, VSS2, and C2002.
- V-FLASH**: Connected to VCCF1, VCCF2, VCCS, and C2003.
- U2000**: A 128Mbit NAND Flash device with pins J5, L5, J6, G2, F2, E2, D2, F3, E3, D3, C3, C7, E7, F7, C8, D8, E8, F8, D9, G9, F4, E4, D7, E6, E9, H2, F9, H3, C6, D5, C5, D6, G8, H9, K6, G3, and J9.
- C2002**: 100N capacitor connected to VSS1 and VSS2.
- C2003**: 100N capacitor connected to VCCF1 and VCCF2.
- R2003**: 10K resistor connected to V-FLASH and C5.

Supplier	MCP	Vcc Range	Memory	Dimension(L*W*H)	Differenc Pin define and mapping	PIN
SPANSION	S71PL032JB0	2.7~3.1	Flash 16Bits SRAM 16Bits NAND 8Bits	9*7*1.09	NC NC NC NC	56
	S71PL064JB0	2.7~3.1	Flash 16Bits SRAM 16Bits NAND 8Bits	9*9*1.19	A21 NC NC NC	56
	S71PL064JA0	2.7~3.1	Flash 16Bits SRAM 16Bits NAND 8Bits	9*7*1.09	A21 NC NC NC	56
	S71PL129JB0	2.7~3.1	Flash 16Bits SRAM 16Bits NAND 8Bits	11.6*8*1.19	A21 E9 CE2#f F9 NC H9 NC G8	64
SAMSUNG	K5A3240CT(B)M	2.7~3.6	Flash 16Bits SRAM 16Bits NAND 8Bits	11*8*1.2	NC NC BYTES SA	69
	K5A3281CT(B)M	2.7~3.6	Flash 16Bits SRAM 16Bits NAND 8Bits	11*8*1.2	NC NC BYTES NC	69
	K5A6317CT(B)M	2.7~3.6	Flash 16Bits SRAM 16Bits NAND 8Bits	11.6*8*1.4	A21 NC BYTES NC	69
	KADxx0700D	2.7~3.6	Flash 16Bits SRAM 16Bits NAND 8Bits	11.6*8*1.4	A21 CEF2 BYTES NC	69

* The component package is SPANSION type (S71PL129JB0)

FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	MEMORY		
NUMBER	A6		
DWG.	Shawn Lin	SHEET	18 of 20
		DATE	2008/02/19

1

2

3

4

5

6

MEMORY
LOCATION:2XXX

V-SRAM

V-FLASH

U2000

J5

L5

J6

VCCF1

VCCF2

VCCS

G2

F2

E2

D2

F3

E3

D3

C3

C7

E7

F7

C8

D8

E8

F8

D9

G9

F4

E4

D7

E6

E9

A21

A1

A2

A3

A4

A5

A6

A7

A8

A9

A10

A11

A12

A13

A14

A15

A16

A17

A18

A19

A20

A21

A22

[13,16]

[13,16]

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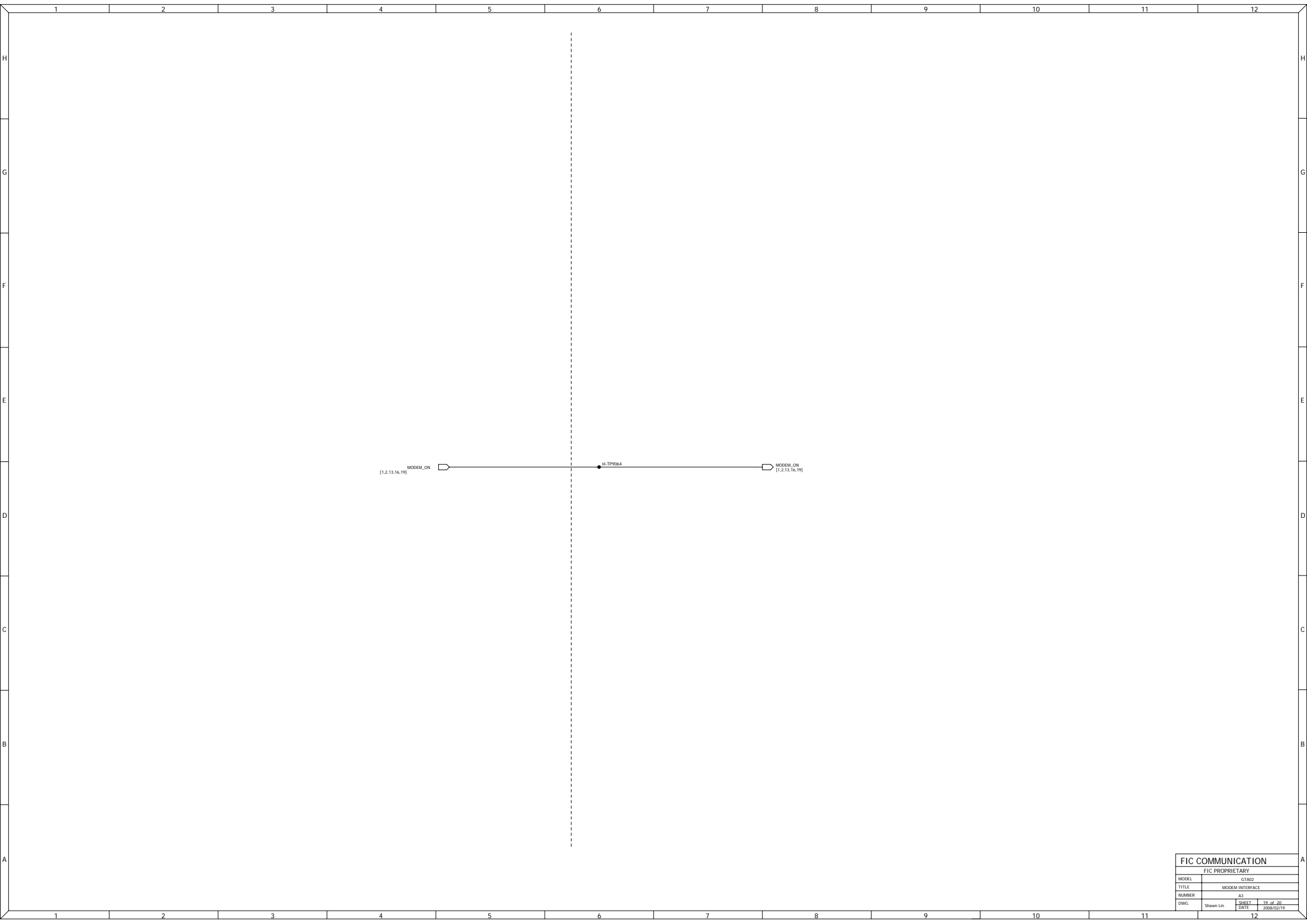
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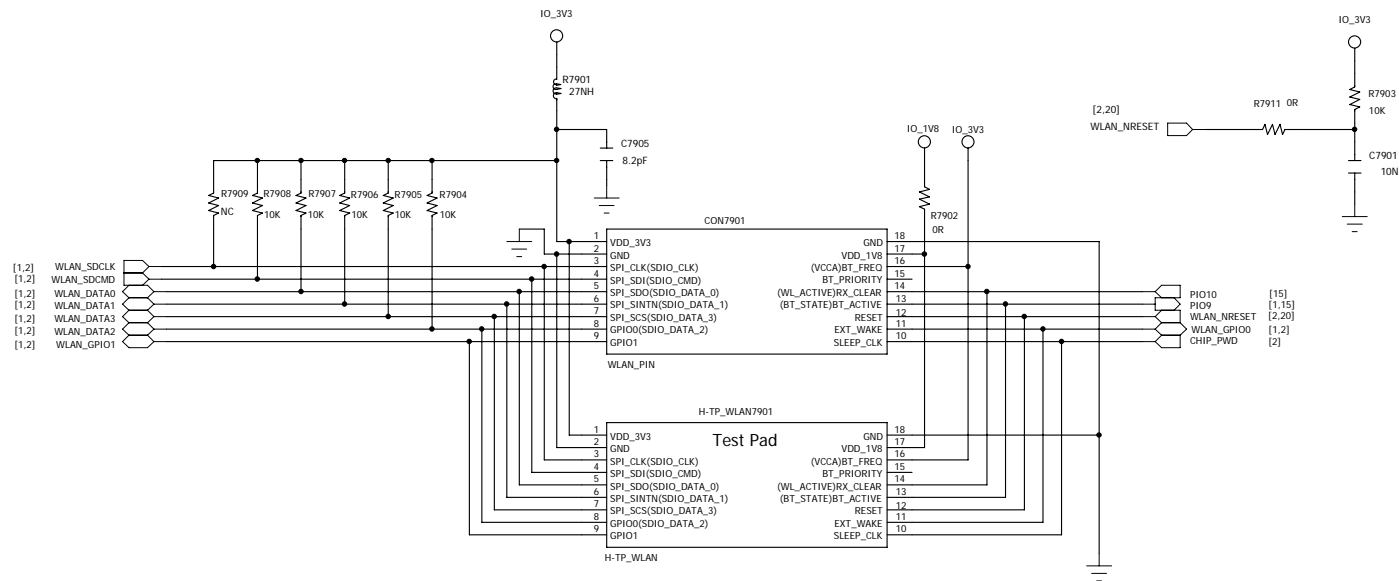
[13,16]

[13,



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTAD2		
TITLE	MODEM INTERFACE		
NUMBER	AS	SHEET	19 of 20
DWG:	Shawn Lin	DATE	2008/02/19

WLAN
LOCATION:79XX



Module Pin	DFWM-MHG21	DFWM-AHG11	SWL-2480	SWL-2700S
	DELTA		SAMSUNG	
	Marvell 88W8686	Atheros AR6001GZ	Marvell 88W8686	Atheros AR6001GZ
1	⊗	⊗	⊗	⊗
2	⊗	⊗	⊗	⊗
3	⊗ △	⊗ △	⊗ △	⊗ △
4	⊗ △	⊗ △	⊗ △	⊗ △
5	⊗ △	⊗ △	⊗ △	⊗ △
6	⊗ △	⊗ △	⊗ △	⊗ △
7	⊗ △	⊗ △	⊗ △	⊗ △
8	△	△	△	△
9				
10	□		□	
11	□		□	
12	□		□	
13	⊗	⊗	⊗	⊗
14	⊗	⊗	⊗	⊗
15	⊗	⊗	⊗	⊗
16		⊗		⊗
17		⊗		⊗
18	⊗	⊗	⊗	⊗

*3-wire co-existence supported only use CS324 BT module

- ⊗:Power Loop
- ⊗:SPI Interface
- △:SDIO Interface
- :Interruption Control
- ⊗:3-wire Co-existence if BT supports
- ⊗:4-wire Co-existence if BT supports

