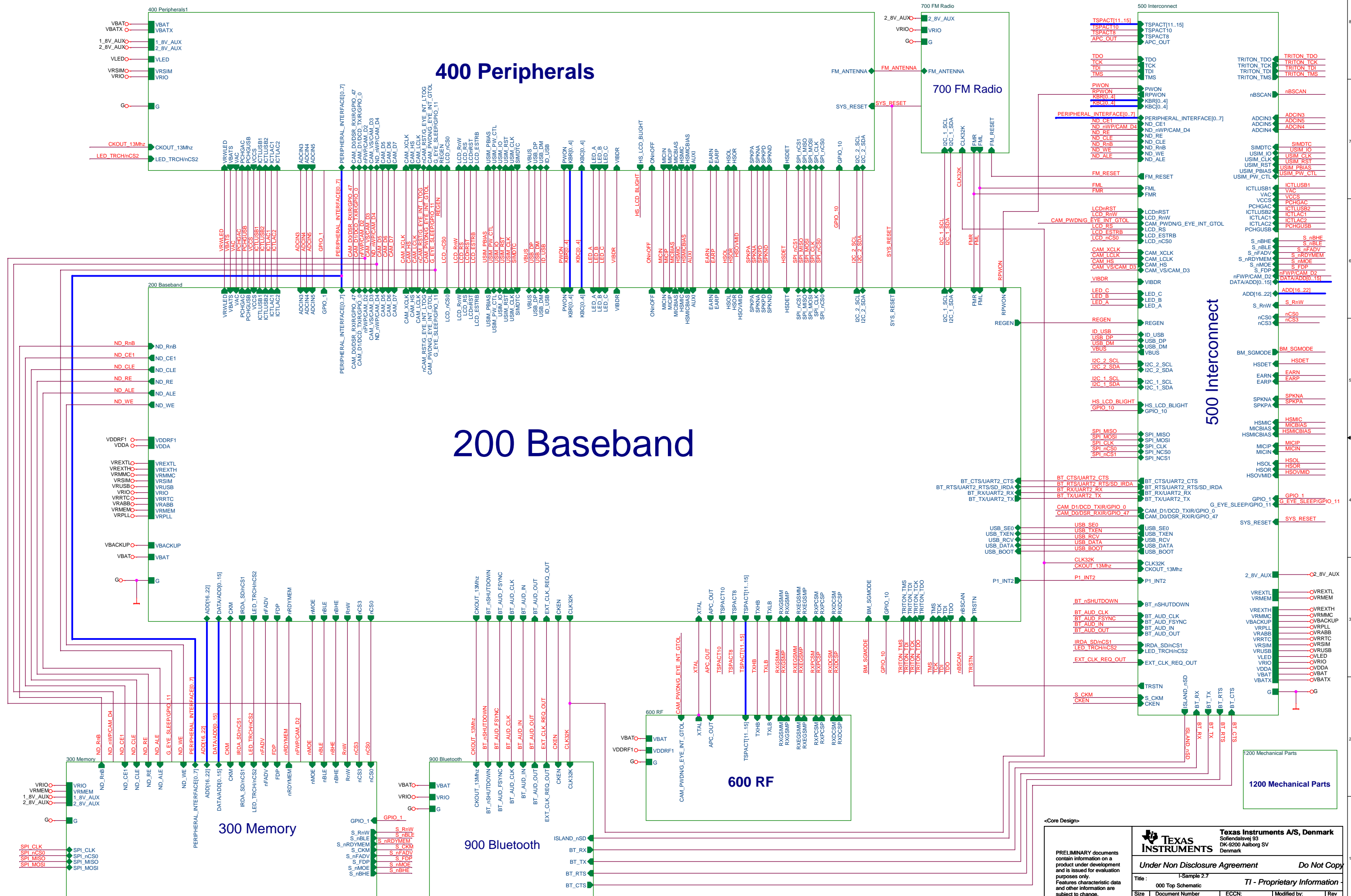
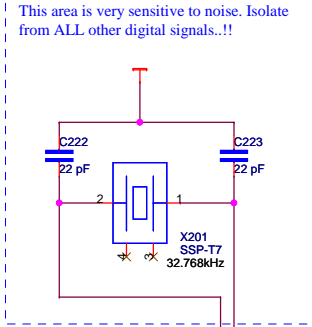
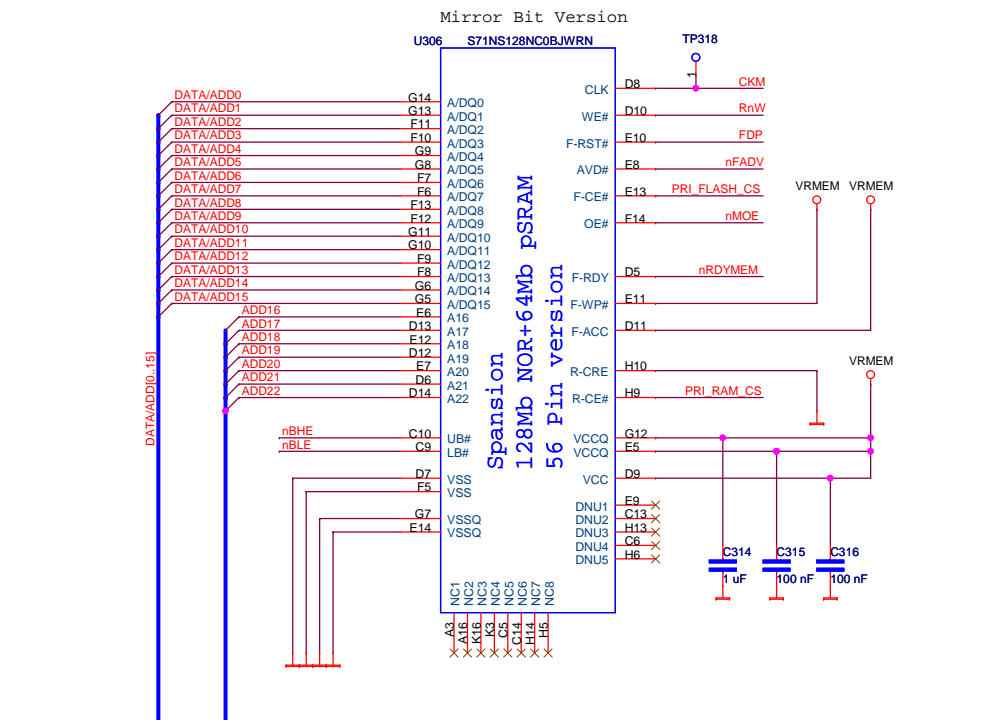


in the Change log located in Clearcase at: \\ti\_wtbu\_hwref\_documents\i-sample\documentation\2\_design\2\_schematics\Changelog\Rev5\_I-sample\_Review\_changelog.xls

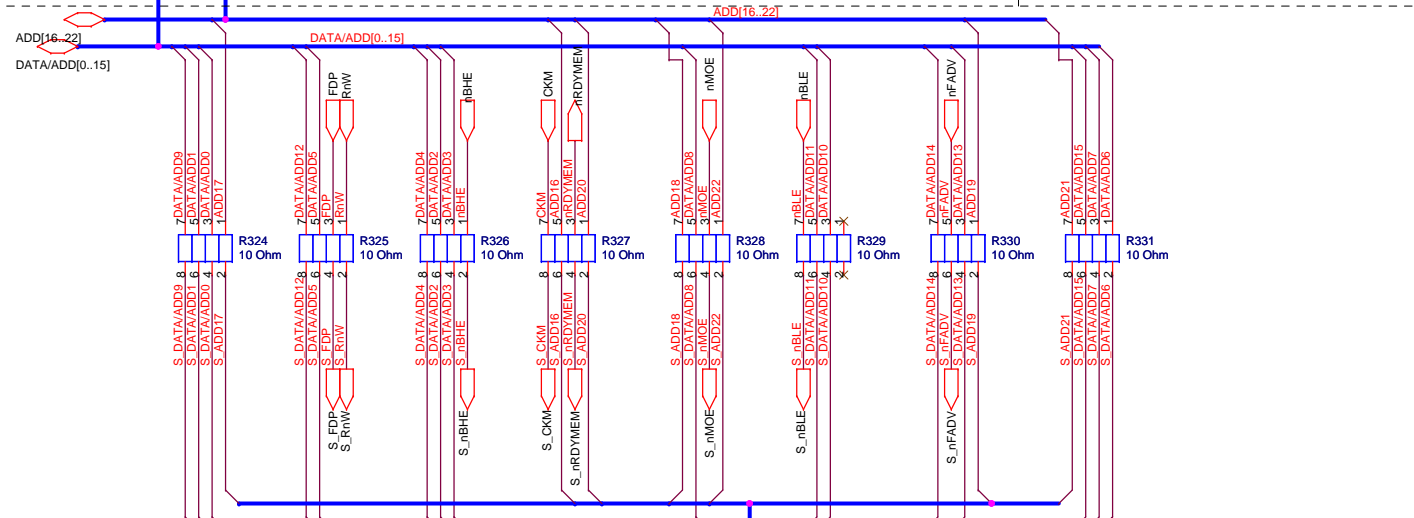




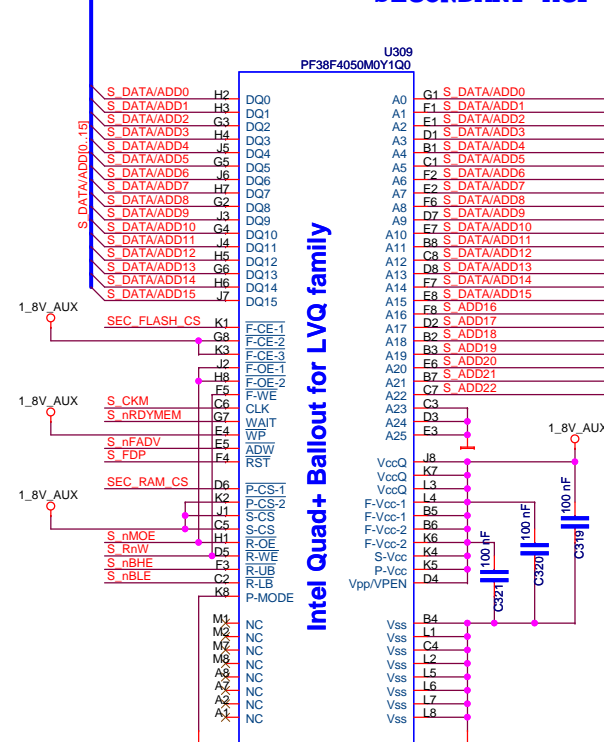
## PRIMARY MCP



(INSIDE REFERENCE AREA)

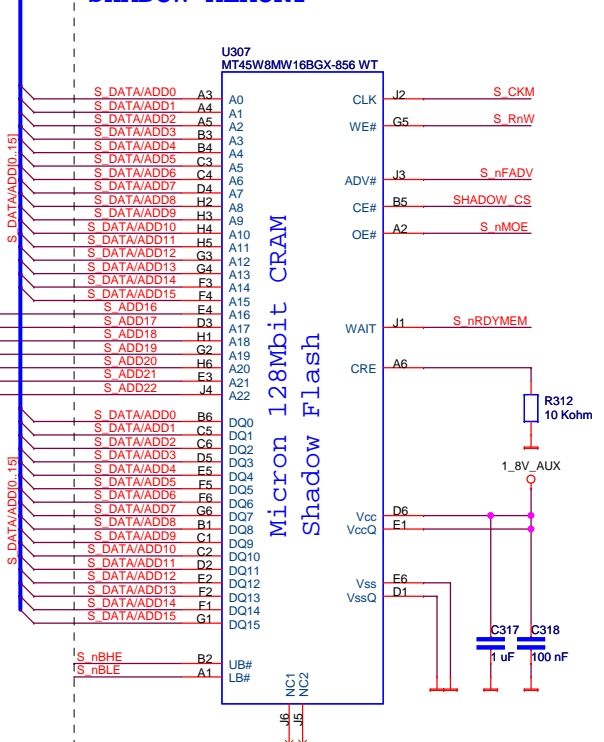


## SECONDARY MCF



(OUTSIDE REFERENCE AREA)

## SHADOW MEMORY

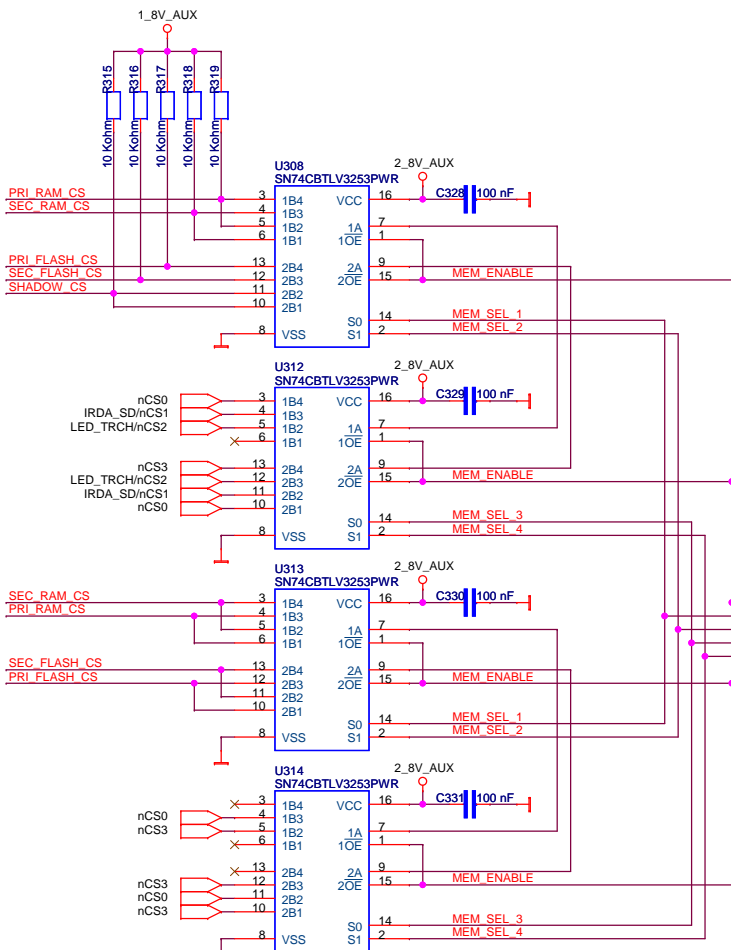


(OUTSIDE REFERENCE AREA)

MEM SEL 1 (S301 - SW 2)	MEM SEL 2 (S301 - SW 3)	MEM SEL 3 (S301 - SW 4)	MEM SEL 4 (S301 - SW 5)	MEMORY SELECTION TABLE			
				This table applies when S301 SW 1 is set to ON			
				nSC0	nSC1	nSC2	nSC3
OFF	OFF	OFF	OFF	PRIMARY MCP (RAM)	NC	NC	PRIMARY MCP (FLASH)
ON	OFF	OFF	OFF	SECONDARY MCP (RAM)	NC	NC	SECONDARY MCP (FLASH)
OFF	ON	OFF	OFF	PRIMARY MCP (RAM)	NC	NC	SHADOW MEMORY
ON	ON	OFF	OFF	SECONDARY MCP (RAM)	NC	NC	SHADOW MEMORY
OFF	OFF	ON	OFF	SECONDARY MCP (RAM)	PRIMARY MCP (RAM)	PRIMARY MCP (FLASH)	SECONDARY MCP (FLASH)
ON	OFF	ON	OFF	PRIMARY MCP (RAM)	SECONDARY MCP (RAM)	SECONDARY MCP (FLASH)	PRIMARY MCP (FLASH)
OFF	ON	ON	OFF	SECONDARY MCP (RAM)	PRIMARY MCP (RAM)	SHADOW MEMORY	SECONDARY MCP (FLASH)
ON	ON	ON	OFF	PRIMARY MCP (RAM)	SECONDARY MCP (RAM)	SHADOW MEMORY	PRIMARY MCP (FLASH)
OFF	OFF	OFF	ON	SECONDARY MCP (FLASH)	PRIMARY MCP (FLASH)	PRIMARY MCP (RAM)	SECONDARY MCP (RAM)
ON	OFF	OFF	ON	PRIMARY MCP (FLASH)	SECONDARY MCP (FLASH)	SECONDARY MCP (RAM)	PRIMARY MCP (RAM)
OFF	ON	OFF	ON	SECONDARY MCP (FLASH)	SHADOW MEMORY	PRIMARY MCP (RAM)	SECONDARY MCP (RAM)
ON	ON	OFF	ON	PRIMARY MCP (FLASH)	SHADOW MEMORY	SECONDARY MCP (RAM)	PRIMARY MCP (RAM)
OFF	OFF	ON	ON	PRIMARY MCP (FLASH)	NC	NC	SECONDARY MCP (FLASH)
ON	OFF	ON	ON	SECONDARY MCP (FLASH)	NC	NC	PRIMARY MCP (FLASH)
OFF	ON	ON	ON	SHADOW MEMORY	NC	NC	SECONDARY MCP (FLASH)
ON	ON	ON	ON	SHADOW MEMORY	NC	NC	PRIMARY MCP (FLASH)

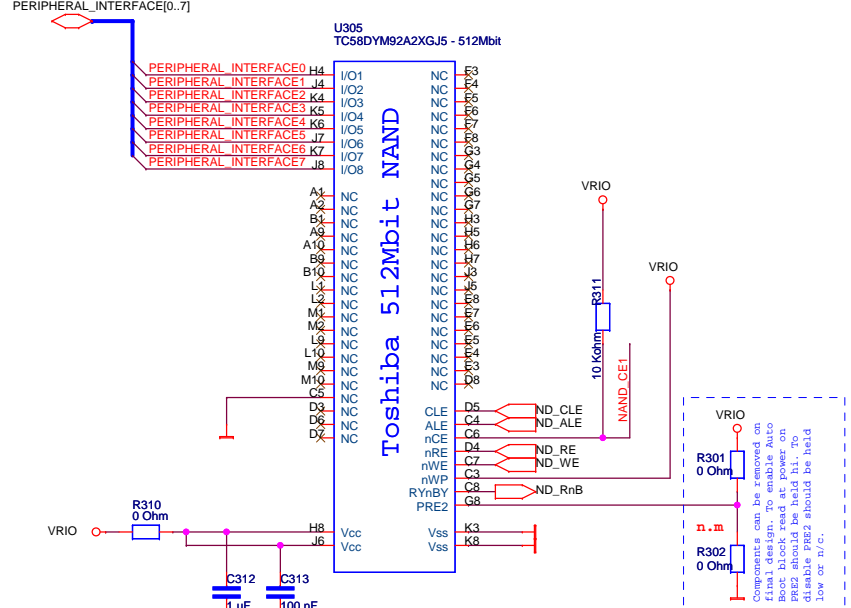
## MEMORY SELECTION LOGIC

S301	ON	OFF	DEFAULT
SW 1	Onboard Memory Enabled	Onboard Memory Disabled	ON
SW 2	Memory Select 1 ON	Memory Select 1 OFF	OFF
SW 3	Memory Select 2 ON	Memory Select 2 OFF	OFF
SW 4	Memory Select 3 ON	Memory Select 3 OFF	OFF
SW 5	Memory Select 4 ON	Memory Select 4 OFF	OFF
SW 6	NAND CS Connected	NAND CS Disconnected	OFF



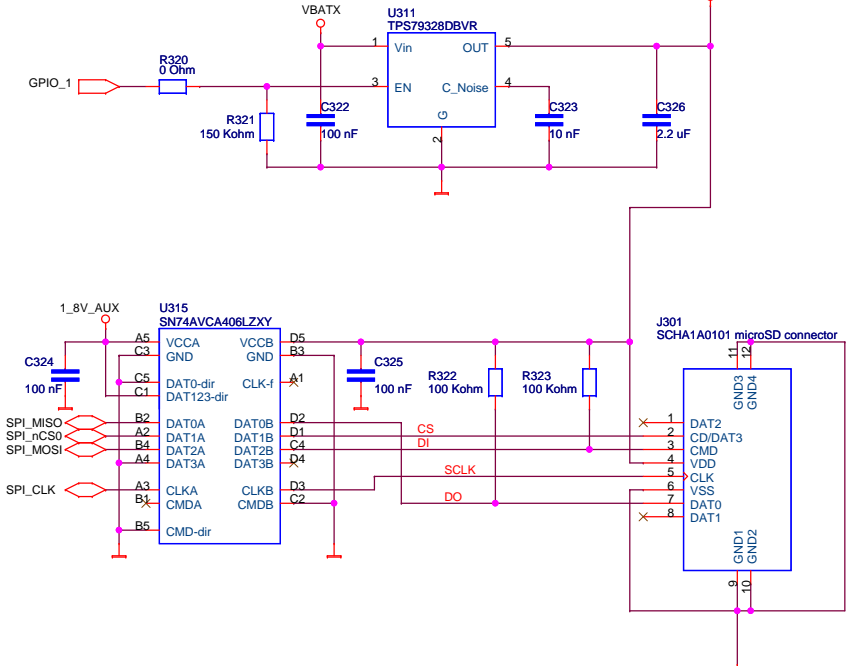
(OUTSIDE REFERENCE AREA)

## NAND FLASH MEMORY

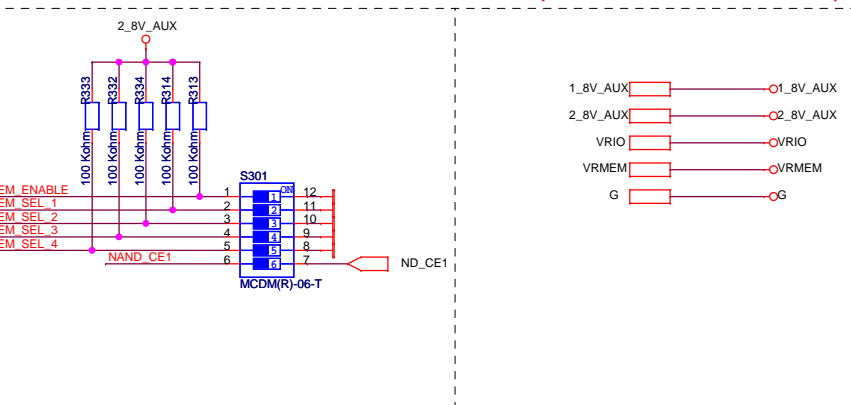


(OUTSIDE REFERENCE AREA)


## MICRO SECURE DIGITAL CARD (uSD)



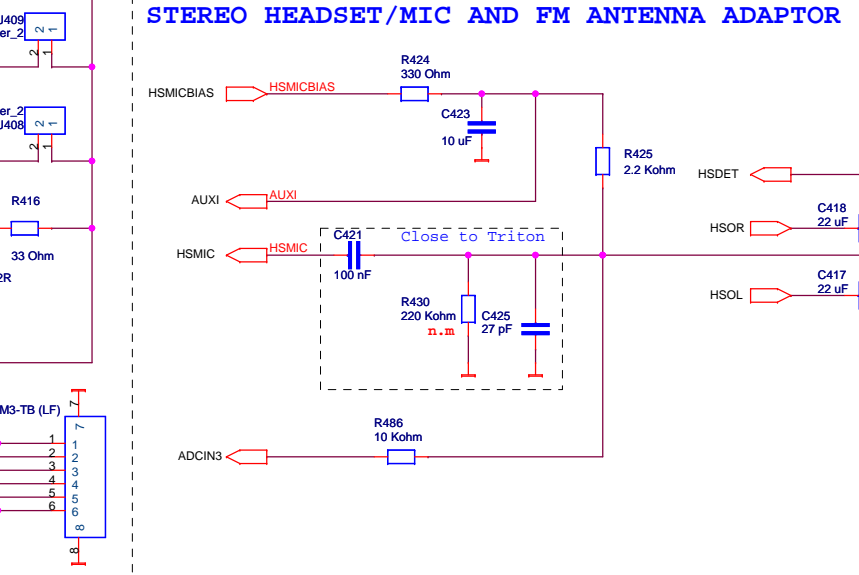
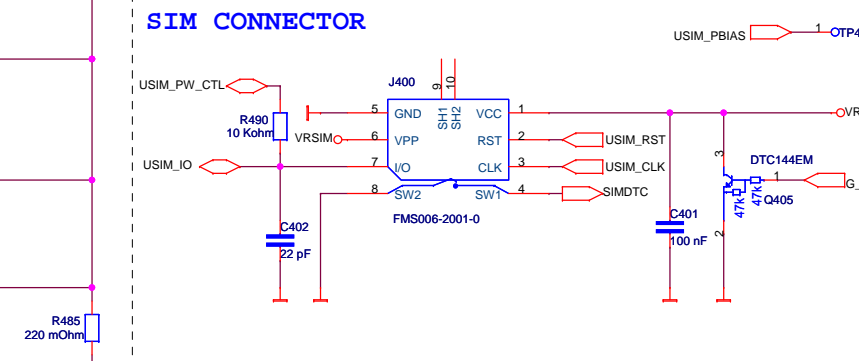
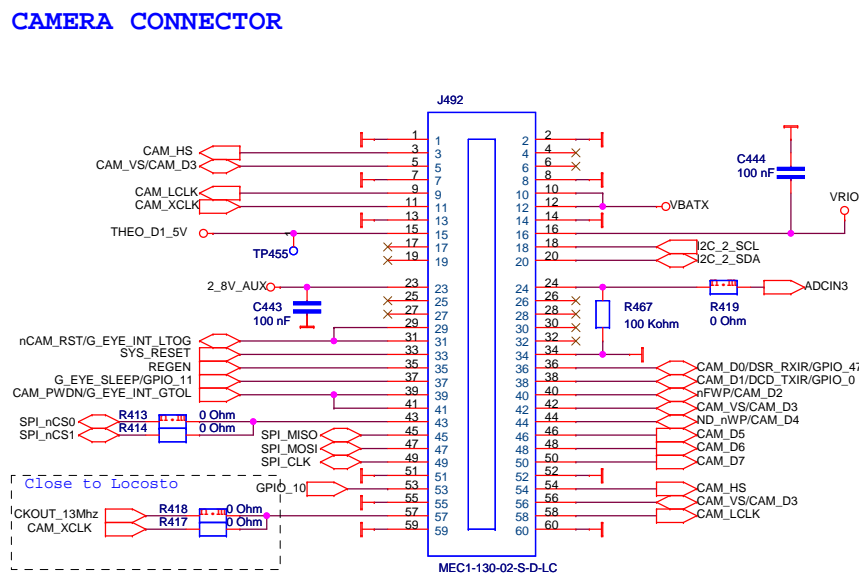
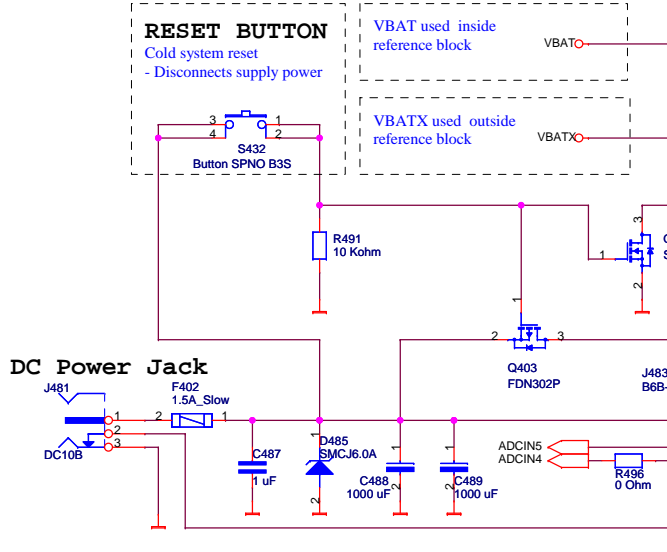
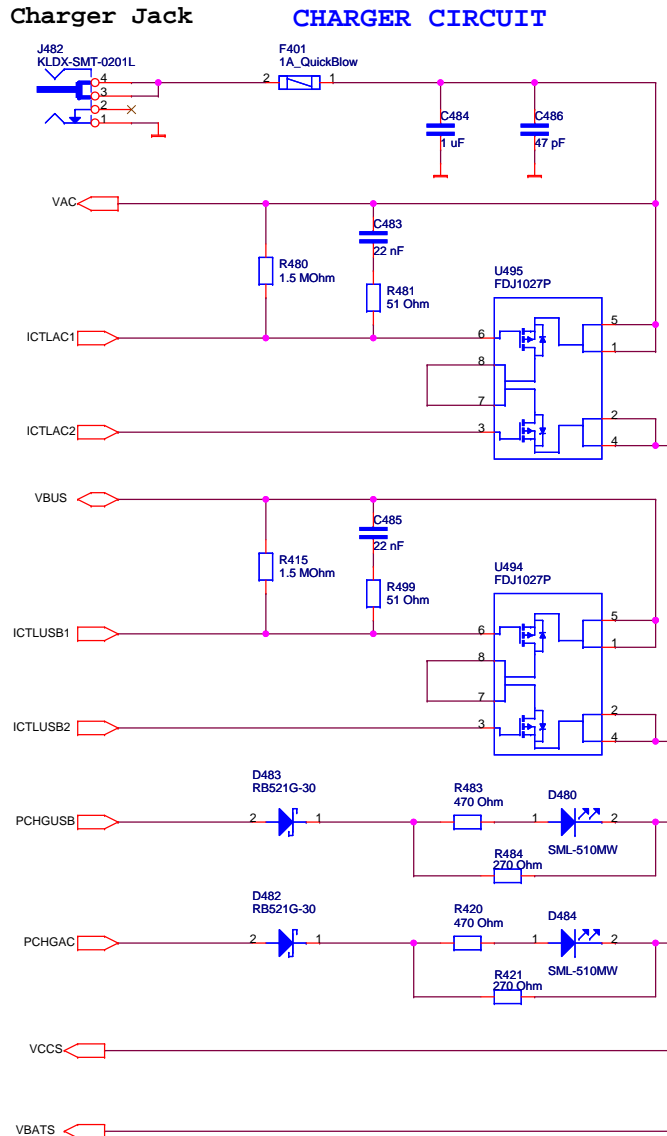
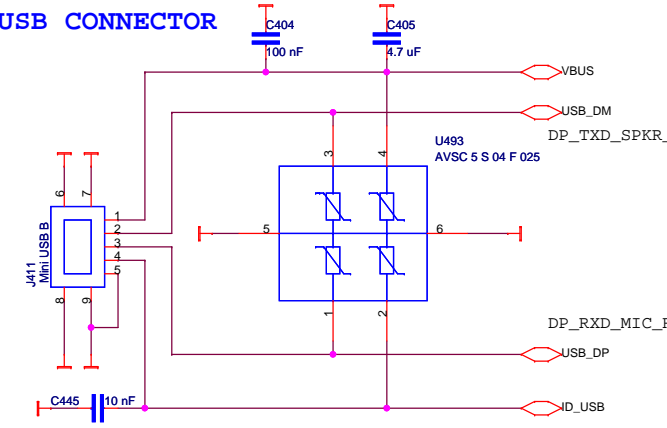
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## <Core Design>

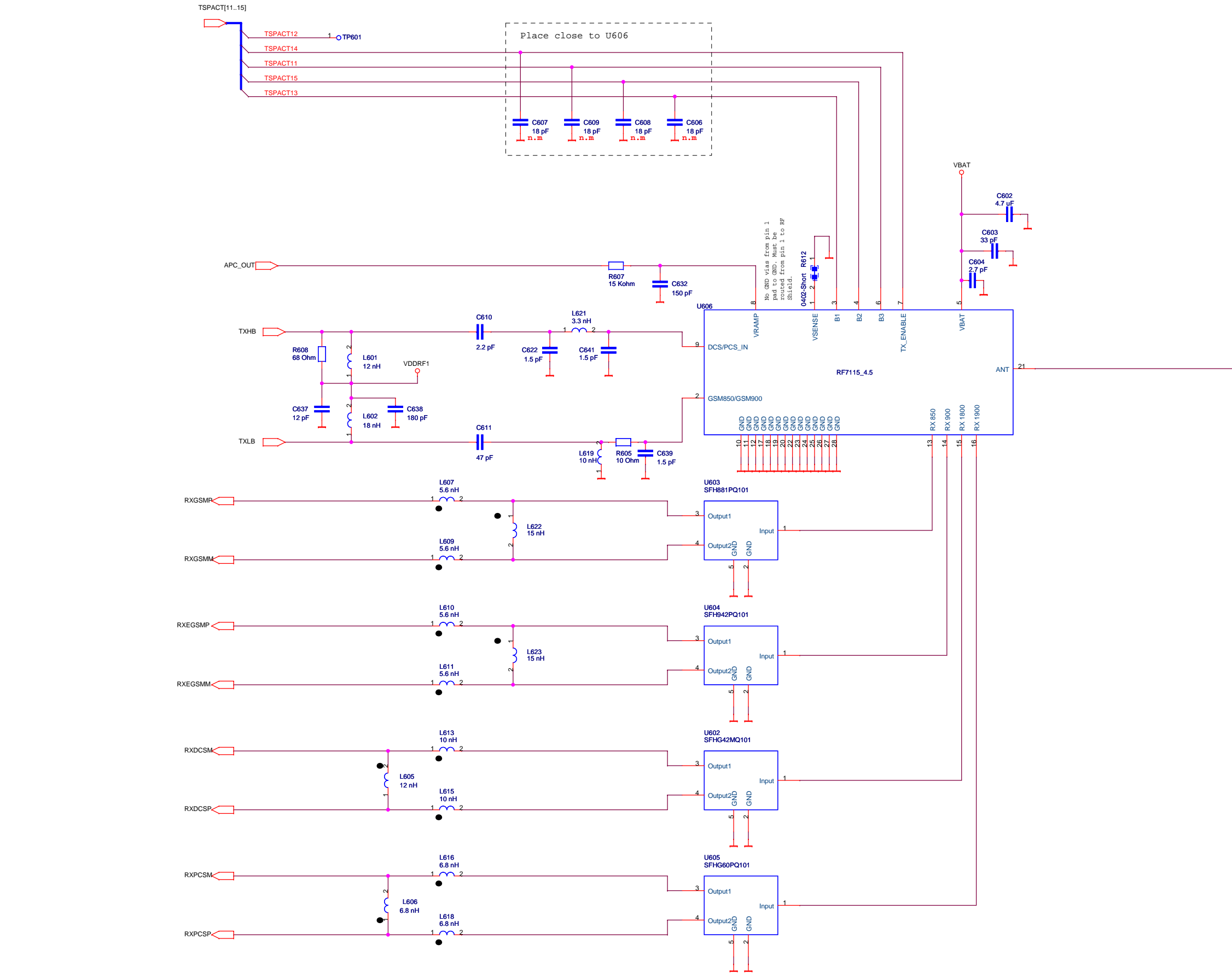
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	<div style="display: flex; justify-content: space-between;"> <span><i>Under Non Disclosure Agreement</i></span> <span><i>Do Not Copy</i></span> </div>					
	<b>Title :</b> I-Sample 2.7 300 Memory			<i>TI - Proprietary Information</i>		
<b>Date:</b> Wednesday, November 29, 2006	<b>Size A2</b>	<b>Document Number</b> 13_03_04_04143	<b>ECCN:</b> EU EAR99 US SE001	<b>Modified by:</b> TEA	<b>Rev</b> 5.3	
<div style="display: flex; justify-content: space-between;"> <span>2006</span> <span>Sheet 3 of 9</span> </div>						







PA AND MATCH COMPONENTS

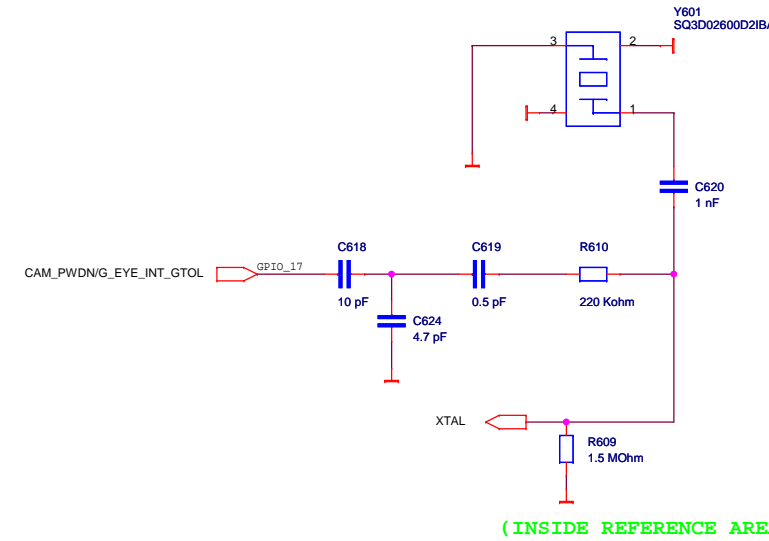


Module Mode Table

MODULE MODE				
TX_EN	B1	B2	B3	MODULE MODE
0	0	0	0	Standby Mode
0	0	0	1	RX1
0	1	0	1	RX2
0	0	1	1	RX3
0	1	1	1	RX4
1	X	0	1	TX Lo Band (GSM 850/EGSM 900)
1	1	1	1	TX Hi Band (DCS 1800/PCS 1900)

RF CONNECTOR

XTAL WITH DITHERING CIRCUIT



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600 RF

Size A2

Document Number 13\_03\_04\_04143

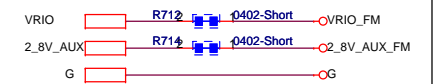
ECN: EU EAR99  
US 5E001

Modified by: TEA

Rev 5.3

Date: Wednesday, November 29, 2006

Sheet 6 of 9



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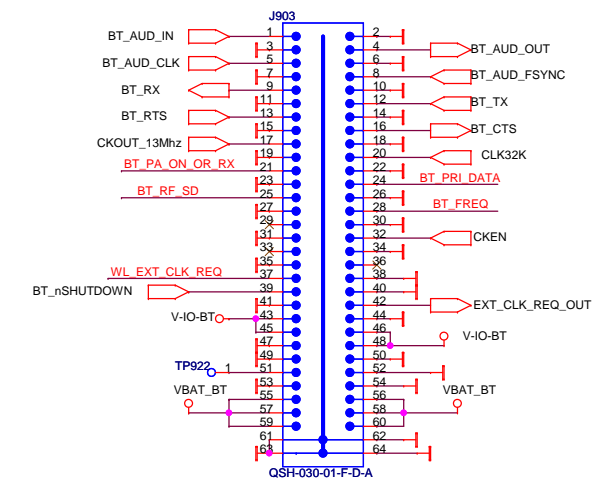
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Date:	Wednesday, November 29, 2006	Sheet	7 of 9	

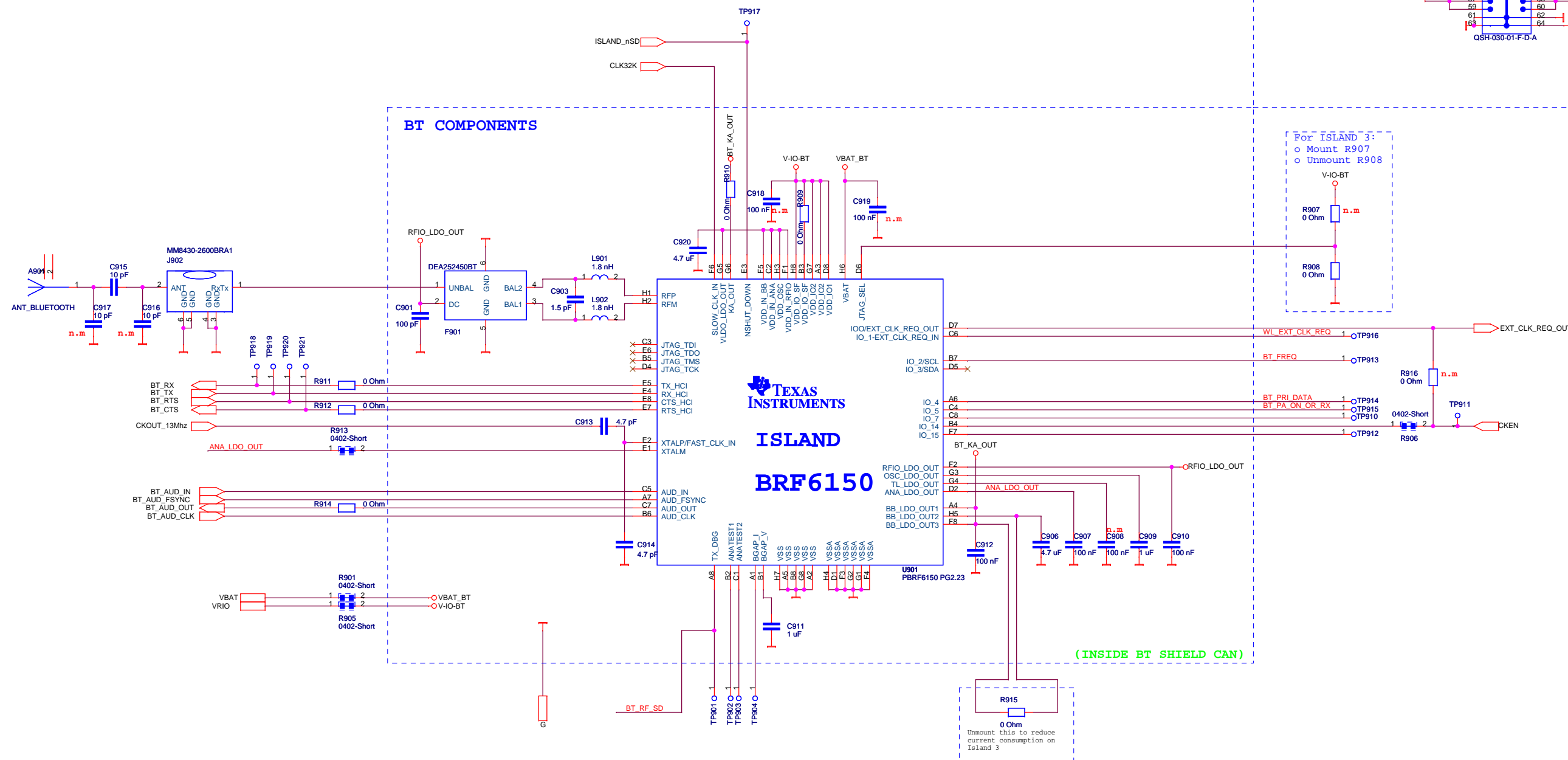
# BT CONNECTOR



(OUTSIDE REFERENCE AREA)

# BT COMPONENTS

For ISLAND 3:  
 o Mount R907  
 o Unmount R908

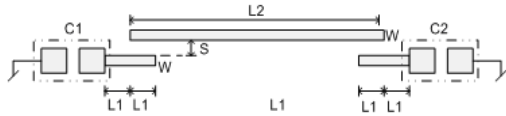
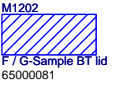


(INSIDE BT SHIELD CAN)

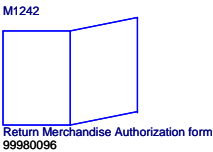
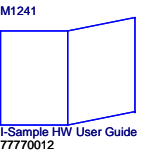
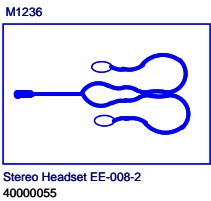
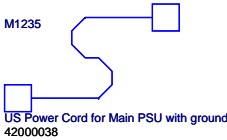
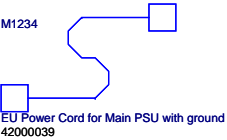
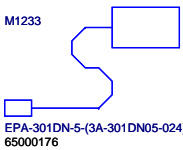
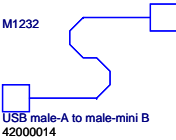
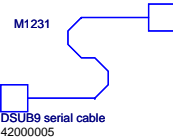
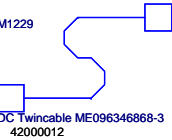
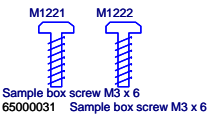
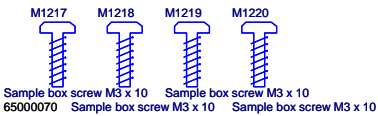
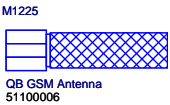
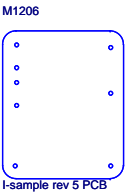
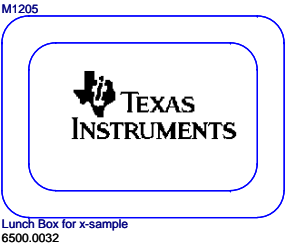
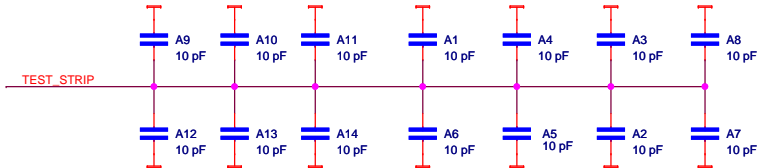
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TEST STRIP 1	TEST STRIP 2
W = 275 $\mu$ m	W = 275 $\mu$ m
S = 300 $\mu$ m	S = 300 $\mu$ m
L1 = 7.5 mm	L1 = 15 mm
L2 = 43.668 mm	L2 = 43.668 mm
TEST STRIP 3	TEST STRIP 4
W = 275 $\mu$ m	W = 275 $\mu$ m
S = 300 $\mu$ m	S = 300 $\mu$ m
L1 = 7.5 mm	L1 = 15 mm
L2 = 45.0 mm	L2 = 45 mm



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