

GSM General Packet Radio Services

Test Specification LLC

Confidential

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0.1 Document History

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8441.402.99.001	18-Oct-1999	DB	Initial
8441.402.99.002	30-Nov-1999	DB	Removed flow control for SAPI 1, test cases renumbered
8441.402.99.003	06-Jan-2000	DB	Included some XID negotiation test cases
8441.402.99.004	28-Jan-2000	DB	Changed SNDPCP to SND in LLC000
8441.402.99.005	10-Feb-2000	DB	Inserted xid_valid in LLC050, LLC058, LLC059
8441.402.99.006	22-Feb-2000	DB	Changed tlli of LL_UNITREADY_IND to NOT_USED, ABM establishment/termination test cases
8441.402.99.007	01-Mar-2000	DB	Removed T200 and N200 from SGSN-originated XID parameter fields
8441.402.99.008	03-Mar-2000	DB	Included further XID test cases and Collisions
8441.402.99.009	06-Mar-2000	DB	Included FRMR test cases, moved LL_GETUNITDATA_REQ in reception test cases
8441.402.99.010	08-Mar-2000	DB	More test cases for receipt of unacknowledged data, some reorganisation of existing test cases
8441.402.99.011	03-Apr-2000	DB	Trigger Request test cases, corrected SAPI 1 behaviour: reliability class 3 -> GRR_DATA_REQ
8441.402.99.012	22-May-2000	DB	Inserted reserved_* parameters for GRR and LL primitives due to changed SAPs
8441.402.99.013	24-May-2000	DB	Changed GRRREQ_QOS_* PSTRUCTs to skip all members but "peak"

8441.402.99.014	29-May-2000	DB	Modified LLC550 to reflect new trigger request behaviour
8441.402.99.015	30-May-2000	DB	Suspended Mode test cases
8441.402.99.016	21-Jun-2000	DB	Modified test cases due to changed LL/GRR SAPs
8441.402.99.017	31-Jul-2000	AK, DB	Made compliant to new TAP2 version, but deactivated changes for now
8441.402.99.018	01-Aug-2000	DB	Corrected XID test cases
8441.402.99.019	03-Aug-2000	DB	Inserted a TIMEOUT() in TC 245, due to timing issues w/o ciphering thread
8441.402.99.020	14-Sep-2000	SLM, DB	Inserted some acknowledged mode data transfer test cases
8441.402.99.021	26-Sep-2000	DB	Activated changes for TAP2 and removed old declarations
8441.402.99.022	12-Nov-2000	GS	Additional Tests for ABM Operation
8441.402.99.023	27-Jun-2001	GS	Additional Tests added. Parameter reference renamed to reference1.
8441.402.99.024	20-Jul-2001	GS	Test 245 changed, because stack behaviour changed (now queueing).
8441.402.99.025	01-Feb-2002	GS	GRR suspend/resume handling added.
8441.402.99.026	14-Apr-2003	UT	Additional Tests simulating GCF 390 for ABM Operation added
8441.402.99.027	22-May-2003	UT	1. Modification of some TC due to modification of Cell Update Procedure. 2. Update of description of GCF related test cases. 3. Some missed flow control primitives added.
8441.402.99.028	30-May-2003	UT	Wrong declaration of field length for supervisory frames.

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0.3 Abbreviations

ACI	Application Control Interface
AGCH	Access Grant Channel
AT	Attention sequence "AT" to indicate valid commands of the ACI
BCCH	Broadcast Control Channel
BS	Base Station
BSIC	Base Station Identification Code
C/R	Command/Response
C1	Path Loss Criterion
C2	Reselection Criterion
CBCH	Cell Broadcast Channel
CBQ	Cell Bar Qualify
CC	Call Control
CCCH	Common Control Channel
CCD	Condat Coder Decoder
CCI	Compression and Ciphering Interface
CHAP	Challenge Handshake Authentication Protocol
CKSN	Ciphering Key Sequence Number
CRC	Cyclic Redundancy Check
DCCH	Dedicated Control Channel
DCOMP	Identifier of the user data compression algorithm used for the N-DPU
DISC	Disconnect Frame
DL	Data Link Layer
DM	Disconnected Mode Frame
DTX	Discontinuous Transmission
E	Extension bit
EA	Extension Bit Address Field
EL	Extension Bit Length Field
EMMI	Electrical Man Machine Interface
F	Final Bit
FACCH	Fast Associated Control Channel
FHO	Forced Handover
GACI	GPRS Application Control Interface
GMM	GPRS Mobility Management
GP	Guard Period
GRR	GPRS RR
GSM	Global System for Mobile Communication
HDLC	High-level Data Link Control
HISR	High level Interrupt Service Routine
HPLMN	Home Public Land Mobile Network
I	Information Frame
IMEI	International Mobile Equipment Identity
IMSI	International Mobile Subscriber Identity
IP	Internet Protocol
IPCP	Internet Protocol Control Protocol
ITU	International Telecommunication Union
IWF	Interworking Function
Kc	Ciphering Key
L	Length Indicator
LAI	Location Area Information
LCP	Link Control Protocol

LISR	Low level Interrupt Service Routine
LLC	Logical Link Control
LPD	Link Protocol Discriminator
LQM	Link Quality Monitoring
M	More bit used to indicate the last segment of N-DPU
MAC	Medium Access Control
MCC	Mobile Country Code
MM	Mobility Management
MMI	Man Machine Interface
MNC	Mobile Network Code
MS	Mobile Station
MT	Mobile Termination
N(R)	Receive Number
N(S)	Send Number
NC	Network Control
NCC	National Colour Code
NCP	Network Control Protocol
NECI	New Establishment Causes included
N-PDU	Network Protocol Data Unit
NSAPI	Network Layer Service Access Point Identifier
OTD	Observed Time Difference
P	Poll Bit
P/F	Poll/Final Bit
PACCH	Packet Associated Control Channel
PAP	Password Authentication Protocol
PBCCH	Packet BCCH
PCCCH	Packet CCCH
PCOMP	Identifier of the protocol control information compression algorithm used for the N-DPU
PDCH	Packet Data Channel
PDP	Packet Data Protocol e.g. IP or X.25
PDTCH	Packet Data Traffic Channel
PRACH	Packet RACH
PSI	Packet System Information
PCH	Paging Channel
PCO	Point of Control and Observation
PDU	Protocol Data Unit
PL	Physical Layer
PLMN	Public Land Mobile Network
PPC	Packet Physical Convergence
PPP	Point-to-Point Protocol
PTP	Point to Point
QoS	Quality of Service
RACH	Random Access Channel
REJ	Reject Frame
RLC	Radio Link Control
RNR	Receive Not Ready Frame
RR	Radio Resource Management
RR	Receive Ready Frame
RTD	Real Time Difference
RTOS	Real Time Operating System
SABM	Set Asynchronous Balanced Mode
SACCH	Slow Associated Control Channel
SAP	Service Access Point
SAPI	Service Access Point Identifier

SDCCH	Stand alone Dedicated Control Channel
SDU	Service Data Unit
SGSN	Serving GPRS Support Node
SIM	Subscriber Identity Module
SM	Session Management
SMS	Short Message Service
SMSCB	Short Message Service Cell Broadcast
SNDCP	Subnetwork Dependant Convergence Protocol
SNSM	SNDCP-SM
SS	Supplementary Services
TAP	Test Application Program
TBF	Temporary Block Flow
TCH	Traffic Channel
TCH/F	Traffic Channel Full Rate
TCH/H	Traffic Channel Half Rate
TCP	Transmission Control Protocol
TDMA	Time Division Multiple Access
TE	Terminal Equipment - e. g. a PC
TFI	Temporary Flow Identifier
TLLI	Temporary Logical Link Identifier
TMSI	Temporary Mobile Subscriber Identity
TOM	Tunnelling of Messages
TQI	Temporary Queuing Identifier
UA	Unnumbered Acknowledgement Frame
UART	Universal Asynchronous Receiver Transmitter
UI	Unnumbered Information Frame
USF	Uplink State Flag
V(A)	Acknowledgement State Variable
V(R)	Receive State Variable
V(S)	Send State Variable
VPLMN	Visited Public Land Mobile Network

0.4 Terms

Entity:	Program which executes the functions of a layer
Message:	A message is a data unit which is transferred between the entities of the same layer (peer-to-peer) of the mobile and infrastructure side. Message is used as a synonym to protocol data unit (PDU). A message may contain several information elements.
Primitive:	A primitive is a data unit which is transferred between layers on one component (mobile station or infrastructure). The primitive has an operation code which identifies the primitive and its parameters.
Service Access Point	A Service Access Point is a data interface between two layers on one component (mobile station or infrastructure).

1 Overview

The Protocol Stacks are used to define the functionality of the GSM protocols for interfaces. The GSM specifications are normative when used to describe the functionality of interfaces, but the stacks and the subdivision of protocol layers does not imply or restrict any implementation.

The protocol stack for GPRS consists of several entities. Each entity has one or more service access points, over which the entity provides a service for the upper entity.

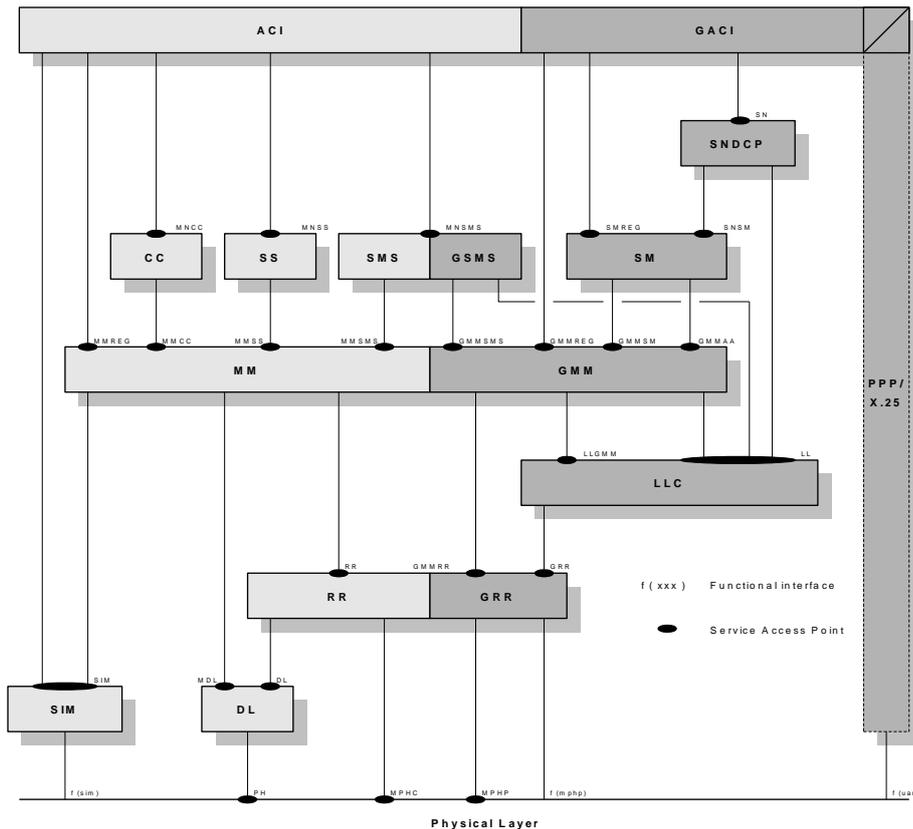


Figure 1-1: Architecture of the GSM/GPRS protocol stack

The information units passed via the SAPs are called primitives and consists of an operation code and several parameters. See the Users Guide for details.

The entities of the GPRS protocol stack are:

1.1 GRR (RLC/MAC) – Radio Link Control/Medium Access Control

This layer contains two functions: The Radio Link Control function provides a radio-solution-dependent reliable link. The Medium Access Control function controls the access signalling (request and grant) procedures for the radio channel, and the mapping of LLC frames onto the GSM physical channel.

1.2 LLC – Logical Link Control

The LLC entity provides multiple highly reliable logical links for asynchronous data transfer between the MS and the network. It supports variable-length information frames, acknowledged and unacknowledged data transfer, flow and sequence control, error detection and recovery, notification of unrecoverable errors, user identity confidentiality, and ciphering of user and signaling data.

1.3 GMM – GPRS Mobility Management

The GMM entity provides procedures for the mobility of the MS, such as informing the network of its present location, and user identity confidentiality. It manages the GMM context (attach, detach, routing area updating), supports security functions such as authentication of user and MS, controls ciphering of data, and initiates the response to paging messages.

1.4 SM – Session Management

The main function of the session management (SM) is to support PDP context handling of the user terminal. Session Management activates, modifies and deletes the contexts for packet data protocols (PDP). Session Management services are provided at the SMREG-SAP and the SNSM-SAP for anonymous and non-anonymous access. The non-anonymous and anonymous access procedures for PDP context activation and PDP context deactivation are available at the SMREG-SAP. In addition there exists a PDP context modification for non-anonymous PDP contexts.

1.5 SNDCP - Subnetwork Dependant Convergence Protocol

SNDCP carries out all functions related to transfer of Network layer Protocol Data Units (N-PDUs) over GPRS in a transparent way. SNDCP helps to improve channel efficiency by means of compression techniques. The set of protocol entities above SNDCP consists of commonly used network protocols. They all use the same SNDCP entity, which then performs multiplexing of data coming from different sources to be sent using the service provided by the LLC layer.

1.6 GACI – GPRS Application Control Interface

The GACI is the GPRS extension of the ACI. It is specified in GSM 07.07 and 07.60. It is responsible for processing of the GPRS related AT Commands to setup, activate and deactivate the PDP context parameter. It also provides functionality for the interworking between GMM/SM/SNDCP and a packet oriented protocol like PPP.

1.7 USART - Universal Synchronous Asynchronous Receiver Transmitter Driver

The USART is a hardware component that facilitates a connection between the mobile station and terminal equipment (e.g. a PC). This interface uses some of the circuits described in V.24.

The data exchange provided by this unit is serial and asynchronous (synchronous communication is not in the scope of this document). A driver that uses interrupts to manage a circular buffer for the sending and receiving direction is necessary in order to use this component in the GPRS. The driver has to be able to perform flow control.

1.8 TOM – Tunnelling of Messages

The TOM entity is present if and only if HS136 is supported (the feature flag FF_HS136 is enabled).

The main function of TOM is to tunnel non-GSM signalling messages between the MS and the SGSN. The only non-GSM signalling which is currently supported by TOM is for the EGPRS-136 system (according to TIA/EIA-136-376). Data transfer in both uplink and downlink direction is possible. Two different priorities (high, low) of signalling data transfer are supported. TOM uses the unacknowledged mode of LLC and the acknowledged mode of GRR (RLC/MAC).

2 Parameters

/*

2.1 Common Parameters

*/

/*

2.1.1 TLLI

*/

LONG	TLLI_LOCAL_1	0xC1234567L
------	--------------	-------------

/*

2.1.2 N201-U

*/

SHORT	N201_U_DEF_SAPI1	400
SHORT	N201_U_DEF_SAPI3	500
SHORT	N201_U_DEF_SAPI5	500
SHORT	N201_U_DEF_SAPI7	270
SHORT	N201_U_DEF_SAPI9	500
SHORT	N201_U_DEF_SAPI11	500
SHORT	N201_U_MIN_SAPI1	400
SHORT	N201_U_MIN_SAPI7	270
SHORT	N201_U_MIN	140
SHORT	N201_U_200	200
SHORT	N201_U_300	300
SHORT	N201_U_600	600
SHORT	N201_U_400	400
SHORT	N201_U_500	500
SHORT	N201_U_520	520
SHORT	N201_U_1520	1520
SHORT	N201_U_1519	1519
SHORT	N201_U_1517	1517
SHORT	N201_U_1510	1510
SHORT	N201_U_MAX	1520

/*

2.1.3 N201-I

*/

SHORT	N201_I_ZERO	0
SHORT	N201_I_DEF	1503
SHORT	N201_I_520	520
SHORT	N201_I_1510	1510
SHORT	N201_I_1503	1503
SHORT	N201_I_1520	1520
SHORT	N201_I_MIN	140
SHORT	N201_I_MAX	1520

/*

2.1.4 LL Reference

*/

BYTE	NSAPI_1	1
SHORT	NPDU_NUM_1	42
BYTE	SEGNUM_0	0
BYTE	SEGNUM_1	1
BYTE	SEGNUM_2	2
BYTE	SEGNUM_3	3
BYTE	SEGNUM_4	4
BYTE	SEGNUM_5	5
BYTE	SEGNUM_6	6
BYTE	SEGNUM_7	7
BYTE	SEGNUM_8	8
BYTE	SEGNUM_9	9
BYTE	SEGNUM_10	10
BYTE	SEGNUM_11	11
BYTE	SEGNUM_12	12
BYTE	SEGNUM_13	13
BYTE	SEGNUM_14	14
BYTE	SEGNUM_15	15
BYTE	SEGNUM_16	16
BYTE	SEGNUM_17	17
BYTE	SEGNUM_18	18
BYTE	SEGNUM_19	19
BYTE	SEGNUM_20	20
BYTE	SEGNUM_21	21
BYTE	SEGNUM_22	22
BYTE	SEGNUM_23	23
BYTE	SEGNUM_24	24
BYTE	SEGNUM_25	25
BYTE	SEGNUM_26	26
BYTE	SEGNUM_27	27
BYTE	SEGNUM_28	28
BYTE	SEGNUM_29	29
BYTE	SEGNUM_30	30
BYTE	SEGNUM_31	31

/*

2.2 Parameter Fields

*/

/*

2.2.1 Empty SDU

*/

```

FIELD(EMPTY_SDU
      0, 0,
      0, 0,
ENDFIELD(EMPTY_SDU,4)

```

/* sdu.l_buf = 0 */
/* sdu.o_buf = 0 */

/*

2.2.2 LL SDU Fields

*/

/*

2.2.2.1 LL-REQ Primitives

```

*/
FIELD(LLREQ_SDU1)
    0x08, 0x00, /* sdu.l_buf = 8 */
    0x28, 0x01, /* sdu.o_buf = 296*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0x01
ENDFIELD(LLREQ_SDU1, 42)
FIELD(LLREQ_SDU3)
    0x18, 0x00, /* sdu.l_buf = 24 */
    0x28, 0x01, /* sdu.o_buf = 296*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0x42, 0x43, 0x44
ENDFIELD(LLREQ_SDU3, 44)

FIELD(LLREQ_SDU100)
    0x20, 0x03, /* sdu.l_buf = 800 */
    0x28, 0x01, /* sdu.o_buf = 296*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
ENDFIELD(LLREQ_SDU100, 141)

FIELD(LLREQ_SDU500)
    0xA0, 0x0F, /* sdu.l_buf = 4000 */
    0x28, 0x01, /* sdu.o_buf = 296*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0, 0, 0, 0, 0, 0, 0, 0, 0, /*reserved*/
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,

```

```

0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,

```

ENDFIELD(LLREQ_SDU500, 541)

FIELD(LLREQ_SDU1000)

```

0x40, 0x1F, /* sdu.l_buf = 8000 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x00, /* reserved */
0x00, 0x00, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,

```

```

0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* reserved */

```

```

ENDFIELD(LLREQ_SDU1000, 1010)

```

```

/*

```

2.2.2.2 LL-IND Primitives, 3 Byte Offset, small

```

*/

```

```

FIELD(LLIND_D3_NS0_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,                                /* sdu.l_buf= 24, sdu.o_buf = 32 */
    0x03,                                                  /* PD = 0, C/R = 0, SAPI = 1245056 */
    0x40, 0x00, 0x00,                                     /* I frame: N(S)=0 N(R)=0 A=1 RR */
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS0_NR0_A1_S3, 11)

```

```

FIELD(LLIND_D3_NS1_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,                                /* sdu.l_buf= 24, sdu.o_buf = 32 */
    0x03,                                                  /* PD = 0, C/R = 0, SAPI = 1245056 */
    0x40, 0x10, 0x00,                                     /* I frame: N(S)=1 N(R)=0 A=1 RR */
    0x42, 0x43, 0x44

```

ENDFIELD(LLIND_D3_NS1_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS2_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x40, 0x20, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=2 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS2_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS3_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x40, 0x30, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=3 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS3_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS4_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x40, 0x40, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=4 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS4_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS5_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x40, 0x50, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=5 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS5_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS6_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x40, 0x60, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=6 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS6_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS7_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x40, 0x70, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=7 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS7_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS8_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x40, 0x80, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=8 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS8_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS9_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x40, 0x90, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=9 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS9_NR0_A1_S3, 11)

```
FIELD(LLIND_D3_NS10_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x40, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS10_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=10 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS11_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x40, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS11_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=11 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS12_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x40, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS12_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=12 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS13_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x40, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS13_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=13 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS14_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x40, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS14_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=14 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS15_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x40, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS15_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=15 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS16_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x41, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS16_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=16 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS17_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x41, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS17_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=17 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS18_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
```

```

0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x41, 0x20, 0x00, /* I frame: N(S)=18 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS18_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS19_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x41, 0x30, 0x00, /* I frame: N(S)=19 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS19_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS20_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x41, 0x40, 0x00, /* I frame: N(S)=20 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS20_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS21_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x41, 0x50, 0x00, /* I frame: N(S)=21 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS21_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS22_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x41, 0x60, 0x00, /* I frame: N(S)=22 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS22_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS23_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x41, 0x70, 0x00, /* I frame: N(S)=23 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS23_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS24_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x41, 0x80, 0x00, /* I frame: N(S)=24 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS24_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS25_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x41, 0x90, 0x00, /* I frame: N(S)=25 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS25_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS26_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x41, 0xa0, 0x00, /* I frame: N(S)=26 N(R)=0 A=1 RR */

```

```

        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS26_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS27_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x41, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS27_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS28_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x41, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS28_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS29_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x41, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS29_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS30_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x41, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS30_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS31_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x41, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS31_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS32_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS32_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS33_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS33_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS34_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS34_NR0_A1_S3, 11)

```

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=27 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=28 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=29 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=30 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=31 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=32 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=33 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=34 N(R)=0 A=1 RR */*

```
FIELD(LLIND_D3_NS35_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS35_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS36_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS36_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS37_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS37_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS38_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS38_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS39_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS39_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS40_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS40_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS41_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS41_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS42_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x42, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS42_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS43_NR0_A1_S3)
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=35 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=36 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=37 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=38 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=39 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=40 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=41 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=42 N(R)=0 A=1 RR */
```

```

        0x18, 0x00, 0x20, 0x00,
        0x03,
        0x42, 0xb0, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS43_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS44_NR0_A1_S3)
        0x18, 0x00, 0x20, 0x00,
        0x03,
        0x42, 0xc0, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS44_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS45_NR0_A1_S3)
        0x18, 0x00, 0x20, 0x00,
        0x03,
        0x42, 0xd0, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS45_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS46_NR0_A1_S3)
        0x18, 0x00, 0x20, 0x00,
        0x03,
        0x42, 0xe0, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS46_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS47_NR0_A1_S3)
        0x18, 0x00, 0x20, 0x00,
        0x03,
        0x42, 0xf0, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS47_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS48_NR0_A1_S3)
        0x18, 0x00, 0x20, 0x00,
        0x03,
        0x43, 0x00, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS48_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS49_NR0_A1_S3)
        0x18, 0x00, 0x20, 0x00,
        0x03,
        0x43, 0x10, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS49_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS50_NR0_A1_S3)
        0x18, 0x00, 0x20, 0x00,
        0x03,
        0x43, 0x20, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS50_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS51_NR0_A1_S3)
        0x18, 0x00, 0x20, 0x00,
        0x03,

```

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=43 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=44 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=45 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=46 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=47 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=48 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=49 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=50 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*

```

        0x43, 0x30, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS51_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS52_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x43, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS52_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS53_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x43, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS53_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS54_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x43, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS54_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS55_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x43, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS55_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS56_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x43, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS56_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS57_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x43, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS57_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS58_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x43, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS58_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS59_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x43, 0xb0, 0x00,
    0x42, 0x43, 0x44

```

```

/* I frame: N(S)=51 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=52 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=53 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=54 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=55 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=56 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=57 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=58 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=59 N(R)=0 A=1 RR */

```

ENDFIELD(LLIND_D3_NS59_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS60_NR0_A1_S3)
 0x18, 0x00, 0x20, 0x00,
 0x03,
 0x43, 0xc0, 0x00,
 0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
 /* PD = 0, C/R = 0, SAPI = 1245056 */
 /* I frame: N(S)=60 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS60_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS61_NR0_A1_S3)
 0x18, 0x00, 0x20, 0x00,
 0x03,
 0x43, 0xd0, 0x00,
 0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
 /* PD = 0, C/R = 0, SAPI = 1245056 */
 /* I frame: N(S)=61 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS61_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS62_NR0_A1_S3)
 0x18, 0x00, 0x20, 0x00,
 0x03,
 0x43, 0xe0, 0x00,
 0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
 /* PD = 0, C/R = 0, SAPI = 1245056 */
 /* I frame: N(S)=62 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS62_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS63_NR0_A1_S3)
 0x18, 0x00, 0x20, 0x00,
 0x03,
 0x43, 0xf0, 0x00,
 0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
 /* PD = 0, C/R = 0, SAPI = 1245056 */
 /* I frame: N(S)=63 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS63_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS64_NR0_A1_S3)
 0x18, 0x00, 0x20, 0x00,
 0x03,
 0x44, 0x00, 0x00,
 0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
 /* PD = 0, C/R = 0, SAPI = 1245056 */
 /* I frame: N(S)=64 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS64_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS65_NR0_A1_S3)
 0x18, 0x00, 0x20, 0x00,
 0x03,
 0x44, 0x10, 0x00,
 0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
 /* PD = 0, C/R = 0, SAPI = 1245056 */
 /* I frame: N(S)=65 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS65_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS66_NR0_A1_S3)
 0x18, 0x00, 0x20, 0x00,
 0x03,
 0x44, 0x20, 0x00,
 0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
 /* PD = 0, C/R = 0, SAPI = 1245056 */
 /* I frame: N(S)=66 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS66_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS67_NR0_A1_S3)
 0x18, 0x00, 0x20, 0x00,
 0x03,
 0x44, 0x30, 0x00,
 0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
 /* PD = 0, C/R = 0, SAPI = 1245056 */
 /* I frame: N(S)=67 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS67_NR0_A1_S3, 11)

```

FIELD(LLIND_D3_NS68_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x44, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS68_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=68 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS69_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x44, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS69_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=69 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS70_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x44, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS70_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=70 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS71_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x44, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS71_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=71 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS72_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x44, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS72_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=72 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS73_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x44, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS73_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=73 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS74_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x44, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS74_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=74 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS75_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x44, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS75_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=75 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS76_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x44, 0xc0, 0x00, /* I frame: N(S)=76 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS76_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS77_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x44, 0xd0, 0x00, /* I frame: N(S)=77 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS77_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS78_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x44, 0xe0, 0x00, /* I frame: N(S)=78 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS78_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS79_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x44, 0xf0, 0x00, /* I frame: N(S)=79 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS79_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS80_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x45, 0x00, 0x00, /* I frame: N(S)=80 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS80_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS81_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x45, 0x10, 0x00, /* I frame: N(S)=81 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS81_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS82_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x45, 0x20, 0x00, /* I frame: N(S)=82 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS82_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS83_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x45, 0x30, 0x00, /* I frame: N(S)=83 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS83_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS84_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x45, 0x40, 0x00, /* I frame: N(S)=84 N(R)=0 A=1 RR */

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        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS84_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS85_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS85_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=85 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS86_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS86_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=86 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS87_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS87_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=87 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS88_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS88_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=88 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS89_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS89_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=89 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS90_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS90_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=90 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS91_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS91_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=91 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS92_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS92_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=92 N(R)=0 A=1 RR */

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FIELD(LLIND_D3_NS93_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS93_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=93 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS94_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS94_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=94 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS95_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x45, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS95_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=95 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS96_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x46, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS96_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=96 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS97_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x46, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS97_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=97 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS98_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x46, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS98_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=98 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS99_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x46, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS99_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=99 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS100_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x46, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS100_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=100 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS101_NR0_A1_S3)

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0x18, 0x00, 0x20, 0x00,
0x03,
0x46, 0x50, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS101_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS102_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x46, 0x60, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS102_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS103_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x46, 0x70, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS103_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS104_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x46, 0x80, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS104_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS105_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x46, 0x90, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS105_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS106_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x46, 0xa0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS106_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS107_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x46, 0xb0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS107_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS108_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x46, 0xc0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS108_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS109_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,

```

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=101 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=102 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=103 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=104 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=105 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=106 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=107 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=108 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*

```

        0x46, 0xd0, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS109_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS110_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x46, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS110_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS111_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x46, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS111_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS112_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x47, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS112_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS113_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x47, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS113_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS114_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x47, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS114_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS115_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x47, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS115_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS116_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x47, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS116_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS117_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x47, 0x50, 0x00,
    0x42, 0x43, 0x44

```

```

/* I frame: N(S)=109 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=110 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=111 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=112 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=113 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=114 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=115 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=116 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=117 N(R)=0 A=1 RR */

```

ENDFIELD(LLIND_D3_NS117_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS118_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x47, 0x60, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=118 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS118_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS119_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x47, 0x70, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=119 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS119_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS120_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x47, 0x80, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=120 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS120_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS121_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x47, 0x90, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=121 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS121_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS122_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x47, 0xa0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=122 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS122_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS123_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x47, 0xb0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=123 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS123_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS124_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x47, 0xc0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=124 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS124_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS125_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x47, 0xd0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=125 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS125_NR0_A1_S3, 11)

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FIELD(LLIND_D3_NS126_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x47, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS126_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=126 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS127_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x47, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS127_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=127 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS128_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x48, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS128_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=128 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS129_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x48, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS129_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=129 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS130_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x48, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS130_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=130 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS131_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x48, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS131_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=131 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS132_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x48, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS132_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=132 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS133_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x48, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS133_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=133 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS134_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x48, 0x60, 0x00, /* I frame: N(S)=134 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS134_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS135_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x48, 0x70, 0x00, /* I frame: N(S)=135 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS135_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS136_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x48, 0x80, 0x00, /* I frame: N(S)=136 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS136_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS137_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x48, 0x90, 0x00, /* I frame: N(S)=137 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS137_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS138_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x48, 0xa0, 0x00, /* I frame: N(S)=138 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS138_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS139_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x48, 0xb0, 0x00, /* I frame: N(S)=139 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS139_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS140_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x48, 0xc0, 0x00, /* I frame: N(S)=140 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS140_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS141_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x48, 0xd0, 0x00, /* I frame: N(S)=141 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS141_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS142_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x48, 0xe0, 0x00, /* I frame: N(S)=142 N(R)=0 A=1 RR */

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        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS142_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS143_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x48, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS143_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=143 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS144_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS144_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=144 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS145_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS145_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=145 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS146_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS146_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=146 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS147_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS147_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=147 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS148_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS148_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=148 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS149_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS149_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=149 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS150_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS150_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=150 N(R)=0 A=1 RR */

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FIELD(LLIND_D3_NS151_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS151_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS152_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS152_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS153_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS153_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS154_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS154_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS155_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS155_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS156_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS156_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS157_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS157_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS158_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x49, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS158_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS159_NR0_A1_S3)
```

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=151 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=152 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=153 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=154 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=155 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=156 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=157 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=158 N(R)=0 A=1 RR */*

```

0x18, 0x00, 0x20, 0x00,
0x03,
0x49, 0x10, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS159_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS160_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4a, 0x00, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS160_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS161_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4a, 0x10, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS161_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS162_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4a, 0x20, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS162_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS163_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4a, 0x30, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS163_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS164_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4a, 0x40, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS164_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS165_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4a, 0x50, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS165_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS166_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4a, 0x60, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS166_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS167_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,

```

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=159 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=160 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=161 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=162 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=163 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=164 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=165 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=166 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*

```

        0x4a, 0x70, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS167_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS168_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4a, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS168_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS169_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4a, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS169_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS170_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4a, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS170_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS171_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4a, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS171_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS172_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4a, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS172_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS173_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4a, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS173_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS174_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4a, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS174_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS175_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4a, 0xf0, 0x00,
    0x42, 0x43, 0x44

```

```

/* I frame: N(S)=167 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=168 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=169 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=170 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=171 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=172 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=173 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=174 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=175 N(R)=0 A=1 RR */

```

ENDFIELD(LLIND_D3_NS175_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS176_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4b, 0x00, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=176 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS176_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS177_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4b, 0x10, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=177 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS177_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS178_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4b, 0x20, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=178 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS178_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS179_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4b, 0x30, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=179 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS179_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS180_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4b, 0x40, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=180 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS180_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS181_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4b, 0x50, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=181 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS181_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS182_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4b, 0x60, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=182 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS182_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS183_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4b, 0x70, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=183 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS183_NR0_A1_S3, 11)

```
FIELD(LLIND_D3_NS184_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4b, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS184_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=184 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS185_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4b, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS185_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=185 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS186_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4b, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS186_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=186 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS187_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4b, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS187_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=187 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS188_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4b, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS188_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=188 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS189_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4b, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS189_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=189 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS190_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4b, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS190_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=190 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS191_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4b, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS191_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=191 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS192_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
```

```

0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4c, 0x00, 0x00, /* I frame: N(S)=192 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS192_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS193_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4c, 0x10, 0x00, /* I frame: N(S)=193 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS193_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS194_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4c, 0x20, 0x00, /* I frame: N(S)=194 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS194_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS195_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4c, 0x30, 0x00, /* I frame: N(S)=195 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS195_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS196_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4c, 0x40, 0x00, /* I frame: N(S)=196 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS196_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS197_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4c, 0x50, 0x00, /* I frame: N(S)=197 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS197_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS198_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4c, 0x60, 0x00, /* I frame: N(S)=198 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS198_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS199_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4c, 0x70, 0x00, /* I frame: N(S)=199 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS199_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS200_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4c, 0x80, 0x00, /* I frame: N(S)=200 N(R)=0 A=1 RR */

```

```

        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS200_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS201_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4c, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS201_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS202_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4c, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS202_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS203_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4c, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS203_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS204_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4c, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS204_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS205_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4c, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS205_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS206_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4c, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS206_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS207_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4c, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS207_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS208_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4d, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS208_NR0_A1_S3, 11)

```

```
FIELD(LLIND_D3_NS209_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4d, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS209_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS210_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4d, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS210_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS211_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4d, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS211_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS212_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4d, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS212_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS213_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4d, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS213_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS214_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4d, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS214_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS215_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4d, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS215_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS216_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4d, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS216_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS217_NR0_A1_S3)
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=209 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=210 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=211 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=212 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=213 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=214 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=215 N(R)=0 A=1 RR */
```

```
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=216 N(R)=0 A=1 RR */
```

```

0x18, 0x00, 0x20, 0x00,
0x03,
0x4d, 0x90, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS217_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS218_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4d, 0xa0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS218_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS219_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4d, 0xb0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS219_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS220_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4d, 0xc0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS220_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS221_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4d, 0xd0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS221_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS222_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4d, 0xe0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS222_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS223_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4d, 0xf0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS223_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS224_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x4e, 0x00, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS224_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS225_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=217 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=218 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=219 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=220 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=221 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=222 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=223 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=224 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */

```

        0x4e, 0x10, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS225_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS226_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4e, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS226_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS227_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4e, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS227_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS228_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4e, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS228_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS229_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4e, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS229_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS230_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4e, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS230_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS231_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4e, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS231_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS232_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4e, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS232_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS233_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4e, 0x90, 0x00,
    0x42, 0x43, 0x44

```

/* I frame: N(S)=225 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=226 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=227 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=228 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=229 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=230 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=231 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=232 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=233 N(R)=0 A=1 RR */

ENDFIELD(LLIND_D3_NS233_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS234_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4e, 0xa0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=234 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS234_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS235_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4e, 0xb0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=235 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS235_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS236_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4e, 0xc0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=236 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS236_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS237_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4e, 0xd0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=237 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS237_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS238_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4e, 0xe0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=238 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS238_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS239_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4e, 0xf0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=239 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS239_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS240_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4f, 0x00, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=240 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS240_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS241_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x4f, 0x10, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=241 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS241_NR0_A1_S3, 11)

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FIELD(LLIND_D3_NS242_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4f, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS242_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=242 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS243_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4f, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS243_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=243 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS244_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4f, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS244_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=244 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS245_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4f, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS245_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=245 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS246_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4f, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS246_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=246 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS247_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4f, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS247_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=247 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS248_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4f, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS248_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=248 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS249_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x4f, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS249_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=249 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS250_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
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0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4f, 0xa0, 0x00, /* I frame: N(S)=250 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS250_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS251_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4f, 0xb0, 0x00, /* I frame: N(S)=251 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS251_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS252_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4f, 0xc0, 0x00, /* I frame: N(S)=252 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS252_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS253_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4f, 0xd0, 0x00, /* I frame: N(S)=253 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS253_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS254_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4f, 0xe0, 0x00, /* I frame: N(S)=254 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS254_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS255_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x4f, 0xf0, 0x00, /* I frame: N(S)=255 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS255_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS256_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x50, 0x00, 0x00, /* I frame: N(S)=256 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS256_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS257_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x50, 0x10, 0x00, /* I frame: N(S)=257 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS257_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS258_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x50, 0x20, 0x00, /* I frame: N(S)=258 N(R)=0 A=1 RR */

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        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS258_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS259_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS259_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS260_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS260_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS261_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS261_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS262_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS262_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS263_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS263_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS264_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS264_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS265_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS265_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS266_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS266_NR0_A1_S3, 11)

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=259 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=260 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=261 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=262 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=263 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=264 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=265 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=266 N(R)=0 A=1 RR */

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FIELD(LLIND_D3_NS267_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS267_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS268_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS268_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS269_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS269_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS270_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS270_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS271_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x50, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS271_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS272_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x51, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS272_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS273_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x51, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS273_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS274_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x51, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS274_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS275_NR0_A1_S3)

```

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=267 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=268 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=269 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=270 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=271 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=272 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=273 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=274 N(R)=0 A=1 RR */*

```

0x18, 0x00, 0x20, 0x00,
0x03,
0x51, 0x30, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS275_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS276_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x51, 0x40, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS276_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS277_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x51, 0x50, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS277_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS278_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x51, 0x60, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS278_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS279_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x51, 0x70, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS279_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS280_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x51, 0x80, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS280_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS281_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x51, 0x90, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS281_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS282_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x51, 0xa0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS282_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS283_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=275 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=276 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=277 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=278 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=279 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=280 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=281 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=282 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */

```

        0x51, 0xb0, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS283_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS284_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x51, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS284_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS285_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x51, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS285_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS286_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x51, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS286_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS287_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x51, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS287_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS288_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x52, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS288_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS289_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x52, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS289_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS290_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x52, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS290_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS291_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x52, 0x30, 0x00,
    0x42, 0x43, 0x44

```

```

/* I frame: N(S)=283 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=284 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=285 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=286 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=287 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=288 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=289 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=290 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=291 N(R)=0 A=1 RR */

```

ENDFIELD(LLIND_D3_NS291_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS292_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x52, 0x40, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=292 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS292_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS293_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x52, 0x50, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=293 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS293_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS294_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x52, 0x60, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=294 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS294_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS295_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x52, 0x70, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=295 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS295_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS296_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x52, 0x80, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=296 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS296_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS297_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x52, 0x90, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=297 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS297_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS298_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x52, 0xa0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=298 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS298_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS299_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x52, 0xb0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=299 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS299_NR0_A1_S3, 11)

```

FIELD(LLIND_D3_NS300_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x52, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS300_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=300 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS301_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x52, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS301_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=301 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS302_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x52, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS302_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=302 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS303_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x52, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS303_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=303 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS304_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x53, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS304_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=304 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS305_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x53, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS305_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=305 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS306_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x53, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS306_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=306 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS307_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x53, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS307_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=307 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS308_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,

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```

0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x53, 0x40, 0x00, /* I frame: N(S)=308 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS308_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS309_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x53, 0x50, 0x00, /* I frame: N(S)=309 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS309_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS310_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x53, 0x60, 0x00, /* I frame: N(S)=310 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS310_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS311_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x53, 0x70, 0x00, /* I frame: N(S)=311 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS311_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS312_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x53, 0x80, 0x00, /* I frame: N(S)=312 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS312_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS313_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x53, 0x90, 0x00, /* I frame: N(S)=313 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS313_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS314_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x53, 0xa0, 0x00, /* I frame: N(S)=314 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS314_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS315_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x53, 0xb0, 0x00, /* I frame: N(S)=315 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS315_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS316_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x53, 0xc0, 0x00, /* I frame: N(S)=316 N(R)=0 A=1 RR */

```

```

        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS316_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS317_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x53, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS317_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=317 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS318_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x53, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS318_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=318 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS319_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x53, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS319_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=319 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS320_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS320_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=320 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS321_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS321_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=321 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS322_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS322_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=322 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS323_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS323_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=323 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS324_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS324_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=324 N(R)=0 A=1 RR */

```

```
FIELD(LLIND_D3_NS325_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS325_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=325 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS326_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS326_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=326 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS327_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS327_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=327 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS328_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS328_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=328 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS329_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS329_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=329 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS330_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS330_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=330 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS331_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS331_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=331 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS332_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x54, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS332_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=332 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS333_NR0_A1_S3)
```

```

0x18, 0x00, 0x20, 0x00,
0x03,
0x54, 0xd0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS333_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS334_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x54, 0xe0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS334_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS335_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x54, 0xf0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS335_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS336_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x55, 0x00, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS336_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS337_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x55, 0x10, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS337_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS338_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x55, 0x20, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS338_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS339_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x55, 0x30, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS339_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS340_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x55, 0x40, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS340_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS341_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=333 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=334 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=335 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=336 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=337 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=338 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=339 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=340 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */

```

        0x55, 0x50, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS341_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS342_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x55, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS342_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS343_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x55, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS343_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS344_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x55, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS344_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS345_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x55, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS345_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS346_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x55, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS346_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS347_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x55, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS347_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS348_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x55, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS348_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS349_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x55, 0xd0, 0x00,
    0x42, 0x43, 0x44

```

```

/* I frame: N(S)=341 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=342 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=343 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=344 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=345 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=346 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=347 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=348 N(R)=0 A=1 RR */

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=349 N(R)=0 A=1 RR */

```

ENDFIELD(LLIND_D3_NS349_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS350_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x55, 0xe0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=350 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS350_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS351_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x55, 0xf0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=351 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS351_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS352_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x56, 0x00, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=352 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS352_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS353_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x56, 0x10, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=353 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS353_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS354_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x56, 0x20, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=354 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS354_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS355_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x56, 0x30, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=355 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS355_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS356_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x56, 0x40, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=356 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS356_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS357_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x56, 0x50, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=357 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS357_NR0_A1_S3, 11)

```

FIELD(LLIND_D3_NS358_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x56, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS358_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS359_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x56, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS359_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS360_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x56, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS360_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS361_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x56, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS361_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS362_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x56, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS362_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS363_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x56, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS363_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS364_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x56, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS364_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS365_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x56, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS365_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS366_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,

```

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=358 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=359 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=360 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=361 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=362 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=363 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=364 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=365 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*

```

0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x56, 0xe0, 0x00, /* I frame: N(S)=366 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS366_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS367_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x56, 0xf0, 0x00, /* I frame: N(S)=367 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS367_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS368_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x57, 0x00, 0x00, /* I frame: N(S)=368 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS368_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS369_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x57, 0x10, 0x00, /* I frame: N(S)=369 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS369_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS370_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x57, 0x20, 0x00, /* I frame: N(S)=370 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS370_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS371_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x57, 0x30, 0x00, /* I frame: N(S)=371 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS371_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS372_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x57, 0x40, 0x00, /* I frame: N(S)=372 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS372_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS373_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x57, 0x50, 0x00, /* I frame: N(S)=373 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS373_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS374_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x57, 0x60, 0x00, /* I frame: N(S)=374 N(R)=0 A=1 RR */

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        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS374_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS375_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x57, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS375_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS376_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x57, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS376_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS377_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x57, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS377_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS378_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x57, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS378_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS379_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x57, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS379_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS380_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x57, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS380_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS381_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x57, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS381_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS382_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x57, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS382_NR0_A1_S3, 11)

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FIELD(LLIND_D3_NS383_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x57, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS383_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=383 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS384_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x58, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS384_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=384 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS385_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x58, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS385_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=385 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS386_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x58, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS386_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=386 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS387_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x58, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS387_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=387 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS388_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x58, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS388_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=388 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS389_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x58, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS389_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=389 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS390_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x58, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS390_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=390 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS391_NR0_A1_S3)

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0x18, 0x00, 0x20, 0x00,
0x03,
0x58, 0x70, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS391_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS392_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x58, 0x80, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS392_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS393_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x58, 0x90, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS393_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS394_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x58, 0xa0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS394_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS395_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x58, 0xb0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS395_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS396_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x58, 0xc0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS396_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS397_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x58, 0xd0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS397_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS398_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x58, 0xe0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS398_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS399_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=391 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=392 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=393 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=394 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=395 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=396 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=397 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=398 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */

```

        0x58, 0xf0, 0x00,
        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS399_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS400_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x59, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS400_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS401_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x59, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS401_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS402_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x59, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS402_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS403_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x59, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS403_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS404_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x59, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS404_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS405_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x59, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS405_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS406_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x59, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS406_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS407_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x59, 0x70, 0x00,
    0x42, 0x43, 0x44

```

/* I frame: N(S)=399 N(R)=0 A=1 RR */

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=400 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=401 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=402 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=403 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=404 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=405 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=406 N(R)=0 A=1 RR *//* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=407 N(R)=0 A=1 RR */

ENDFIELD(LLIND_D3_NS407_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS408_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x59, 0x80, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=408 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS408_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS409_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x59, 0x90, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=409 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS409_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS410_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x59, 0xa0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=410 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS410_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS411_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x59, 0xb0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=411 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS411_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS412_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x59, 0xc0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=412 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS412_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS413_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x59, 0xd0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=413 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS413_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS414_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x59, 0xe0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=414 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS414_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS415_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x59, 0xf0, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=415 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS415_NR0_A1_S3, 11)

```
FIELD(LLIND_D3_NS416_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5a, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS416_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=416 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS417_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5a, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS417_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=417 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS418_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5a, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS418_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=418 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS419_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5a, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS419_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=419 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS420_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5a, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS420_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=420 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS421_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5a, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS421_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=421 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS422_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5a, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS422_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=422 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS423_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5a, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS423_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=423 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS424_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
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0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5a, 0x80, 0x00, /* I frame: N(S)=424 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS424_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS425_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5a, 0x90, 0x00, /* I frame: N(S)=425 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS425_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS426_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5a, 0xa0, 0x00, /* I frame: N(S)=426 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS426_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS427_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5a, 0xb0, 0x00, /* I frame: N(S)=427 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS427_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS428_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5a, 0xc0, 0x00, /* I frame: N(S)=428 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS428_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS429_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5a, 0xd0, 0x00, /* I frame: N(S)=429 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS429_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS430_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5a, 0xe0, 0x00, /* I frame: N(S)=430 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS430_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS431_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5a, 0xf0, 0x00, /* I frame: N(S)=431 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS431_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS432_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5b, 0x00, 0x00, /* I frame: N(S)=432 N(R)=0 A=1 RR */

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        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS432_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS433_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS433_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS434_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS434_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS435_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS435_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS436_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS436_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS437_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS437_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS438_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS438_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS439_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS439_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS440_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS440_NR0_A1_S3, 11)

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FIELD(LLIND_D3_NS441_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS441_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=441 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS442_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS442_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=442 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS443_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS443_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=443 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS444_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS444_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=444 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS445_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS445_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=445 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS446_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS446_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=446 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS447_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5b, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS447_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=447 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS448_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5c, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS448_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=448 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS449_NR0_A1_S3)

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0x18, 0x00, 0x20, 0x00,
0x03,
0x5c, 0x10, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS449_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS450_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5c, 0x20, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS450_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS451_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5c, 0x30, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS451_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS452_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5c, 0x40, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS452_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS453_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5c, 0x50, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS453_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS454_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5c, 0x60, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS454_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS455_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5c, 0x70, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS455_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS456_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5c, 0x80, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS456_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS457_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,

```

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=449 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=450 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=451 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=452 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=453 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=454 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=455 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*
/ I frame: N(S)=456 N(R)=0 A=1 RR */*

/ sdu.l_buf = 24, sdu.o_buf = 32 */*
/ PD = 0, C/R = 0, SAPI = 1245056 */*

```

0x5c, 0x90, 0x00, /* I frame: N(S)=457 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS457_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS458_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5c, 0xa0, 0x00, /* I frame: N(S)=458 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS458_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS459_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5c, 0xb0, 0x00, /* I frame: N(S)=459 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS459_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS460_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5c, 0xc0, 0x00, /* I frame: N(S)=460 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS460_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS461_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5c, 0xd0, 0x00, /* I frame: N(S)=461 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS461_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS462_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5c, 0xe0, 0x00, /* I frame: N(S)=462 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS462_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS463_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5c, 0xf0, 0x00, /* I frame: N(S)=463 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS463_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS464_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5d, 0x00, 0x00, /* I frame: N(S)=464 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS464_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS465_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5d, 0x10, 0x00, /* I frame: N(S)=465 N(R)=0 A=1 RR */
0x42, 0x43, 0x44

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ENDFIELD(LLIND_D3_NS465_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS466_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x5d, 0x20, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=466 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS466_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS467_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x5d, 0x30, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=467 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS467_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS468_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x5d, 0x40, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=468 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS468_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS469_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x5d, 0x50, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=469 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS469_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS470_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x5d, 0x60, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=470 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS470_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS471_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x5d, 0x70, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=471 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS471_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS472_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x5d, 0x80, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=472 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS472_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS473_NR0_A1_S3)

0x18, 0x00, 0x20, 0x00,
0x03,
0x5d, 0x90, 0x00,
0x42, 0x43, 0x44

/ sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=473 N(R)=0 A=1 RR */*

ENDFIELD(LLIND_D3_NS473_NR0_A1_S3, 11)

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FIELD(LLIND_D3_NS474_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5d, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS474_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=474 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS475_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5d, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS475_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=475 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS476_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5d, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS476_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=476 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS477_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5d, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS477_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=477 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS478_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5d, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS478_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=478 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS479_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5d, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS479_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=479 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS480_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5e, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS480_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=480 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS481_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5e, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS481_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=481 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS482_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,

```

```
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5e, 0x20, 0x00, /* I frame: N(S)=482 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS482_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS483_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5e, 0x30, 0x00, /* I frame: N(S)=483 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS483_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS484_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5e, 0x40, 0x00, /* I frame: N(S)=484 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS484_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS485_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5e, 0x50, 0x00, /* I frame: N(S)=485 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS485_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS486_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5e, 0x60, 0x00, /* I frame: N(S)=486 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS486_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS487_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5e, 0x70, 0x00, /* I frame: N(S)=487 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS487_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS488_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5e, 0x80, 0x00, /* I frame: N(S)=488 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS488_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS489_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5e, 0x90, 0x00, /* I frame: N(S)=489 N(R)=0 A=1 RR */
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS489_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS490_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00, /* sdu.l_buf = 24, sdu.o_buf = 32 */
0x03, /* PD = 0, C/R = 0, SAPI = 1245056 */
0x5e, 0xa0, 0x00, /* I frame: N(S)=490 N(R)=0 A=1 RR */
```

```

        0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS490_NR0_A1_S3, 11)

FIELD(LLIND_D3_NS491_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5e, 0xb0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS491_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=491 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS492_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5e, 0xc0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS492_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=492 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS493_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5e, 0xd0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS493_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=493 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS494_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5e, 0xe0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS494_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=494 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS495_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5e, 0xf0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS495_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=495 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS496_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x00, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS496_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=496 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS497_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x10, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS497_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=497 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS498_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x20, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS498_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=498 N(R)=0 A=1 RR */

```

```
FIELD(LLIND_D3_NS499_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x30, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS499_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=499 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS500_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x40, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS500_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=500 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS501_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x50, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS501_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=501 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS502_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x60, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS502_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=502 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS503_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x70, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS503_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=503 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS504_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x80, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS504_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=504 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS505_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0x90, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS505_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=505 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS506_NR0_A1_S3)
    0x18, 0x00, 0x20, 0x00,
    0x03,
    0x5f, 0xa0, 0x00,
    0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS506_NR0_A1_S3, 11)
/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=506 N(R)=0 A=1 RR */

FIELD(LLIND_D3_NS507_NR0_A1_S3)
```

```

0x18, 0x00, 0x20, 0x00,
0x03,
0x5f, 0xb0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS507_NR0_A1_S3, 11)

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=507 N(R)=0 A=1 RR */

```

```

FIELD(LLIND_D3_NS508_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5f, 0xc0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS508_NR0_A1_S3, 11)

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=508 N(R)=0 A=1 RR */

```

```

FIELD(LLIND_D3_NS509_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5f, 0xd0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS509_NR0_A1_S3, 11)

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=509 N(R)=0 A=1 RR */

```

```

FIELD(LLIND_D3_NS510_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5f, 0xe0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS510_NR0_A1_S3, 11)

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=510 N(R)=0 A=1 RR */

```

```

FIELD(LLIND_D3_NS511_NR0_A1_S3)
0x18, 0x00, 0x20, 0x00,
0x03,
0x5f, 0xf0, 0x00,
0x42, 0x43, 0x44
ENDFIELD(LLIND_D3_NS511_NR0_A1_S3, 11)

```

```

/* sdu.l_buf = 24, sdu.o_buf = 32 */
/* PD = 0, C/R = 0, SAPI = 1245056 */
/* I frame: N(S)=511 N(R)=0 A=1 RR */

```

/*

2.2.2.2.1 N(U) 0

*/

```

FIELD(LLIND_SDU100_SAPI1)
0x20, 0x03,
0x18, 0x00,
0x01,
0xC0, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLIND_SDU100_SAPI1, 107)

```

```

/* sdu.l_buf = 800 */
/* sdu.o_buf = 24 */
/* reserved */
/* reserved */

```

```

FIELD(LLIND_SDU100_SAPI3)
0x20, 0x03,
0x18, 0x00,
0x03,
0xC0, 0x00,

```

```

/* sdu.l_buf = 800 */
/* sdu.o_buf = 24 */
/* reserved */
/* reserved */

```

```

0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDUI00_SAPI3, 107)
```

```
FIELD(LLIND_SDUI00_SAPI5)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x05, /* reserved */
0xC0, 0x00, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDUI00_SAPI5, 107)
```

```
FIELD(LLIND_SDUI00_SAPI7)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x07, /* reserved */
0xC0, 0x00, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDUI00_SAPI7, 107)
```

```
FIELD(LLIND_SDUI00_SAPI9)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x09, /* reserved */
0xC0, 0x00, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDUI00_SAPI9, 107)
```

```
FIELD(LLIND_SDUI00_SAPI11)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x0B, /* reserved */
0xC0, 0x00, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,

```

```

0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_SAPI11, 107)
```

```
/*
```

2.2.2.2.2 N(U) 1

```
*/
```

```
FIELD(LLIND_SDU100_NU1_SAPI1)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x01, /* reserved */
0xC0, 0x04, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU1_SAPI1, 107)
```

```
FIELD(LLIND_SDU100_NU1_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x03, /* reserved */
0xC0, 0x04, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU1_SAPI3, 107)
```

```
FIELD(LLIND_SDU100_NU1_SAPI5)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x05, /* reserved */
0xC0, 0x04, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU1_SAPI5, 107)
```

```
FIELD(LLIND_SDU100_NU1_SAPI7)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x07, /* reserved */
0xC0, 0x04, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,

```

```

0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLIND_SDU100_NU1_SAPI7, 107)

```

```

FIELD(LLIND_SDU100_NU1_SAPI9)
    0x20, 0x03,                /* sdu.l_buf = 800 */
    0x18, 0x00,                /* sdu.o_buf = 24 */
    0x09,                       /* reserved */
    0xC0, 0x04,                /* reserved */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64
ENDFIELD(LLIND_SDU100_NU1_SAPI9, 107)

```

```

FIELD(LLIND_SDU100_NU1_SAPI11)
    0x20, 0x03,                /* sdu.l_buf = 800 */
    0x18, 0x00,                /* sdu.o_buf = 24 */
    0x0B,                       /* reserved */
    0xC0, 0x04,                /* reserved */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64
ENDFIELD(LLIND_SDU100_NU1_SAPI11, 107)

```

```
/*
```

2.2.2.2.3 N(U) 2

```
*/
```

```

FIELD(LLIND_SDU100_NU2_SAPI1)
    0x20, 0x03,                /* sdu.l_buf = 800 */
    0x18, 0x00,                /* sdu.o_buf = 24 */
    0x01,                       /* reserved */
    0xC0, 0x08,                /* reserved */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64
ENDFIELD(LLIND_SDU100_NU2_SAPI1, 107)

```

```

FIELD(LLIND_SDU100_NU2_SAPI3)
    0x20, 0x03,                /* sdu.l_buf = 800 */
    0x18, 0x00,                /* sdu.o_buf = 24 */
    0x03,                       /* reserved */
    0xC0, 0x08,                /* reserved */

```

```

0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU2_SAPI3, 107)
```

```
FIELD(LLIND_SDU100_NU2_SAPI5)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x05, /* reserved */
0xC0, 0x08, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU2_SAPI5, 107)
```

```
FIELD(LLIND_SDU100_NU2_SAPI7)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x07, /* reserved */
0xC0, 0x08, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU2_SAPI7, 107)
```

```
FIELD(LLIND_SDU100_NU2_SAPI9)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x09, /* reserved */
0xC0, 0x08, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU2_SAPI9, 107)
```

```
FIELD(LLIND_SDU100_NU2_SAPI11)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x0B, /* reserved */
0xC0, 0x08, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,

```

```

0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU2_SAPI11, 107)
```

```
/*
```

2.2.2.2.4 N(U) 3

```
*/
```

```
FIELD(LLIND_SDU100_NU3_SAPI1)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x01, /* reserved */
0xC0, 0x0C, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU3_SAPI1, 107)
```

```
FIELD(LLIND_SDU100_NU3_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x03, /* reserved */
0xC0, 0x0C, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU3_SAPI3, 107)
```

```
FIELD(LLIND_SDU100_NU3_SAPI5)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x05, /* reserved */
0xC0, 0x0C, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU3_SAPI5, 107)
```

```
FIELD(LLIND_SDU100_NU3_SAPI7)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x07, /* reserved */
0xC0, 0x0C, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,

```

```

0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU3_SAPI7, 107)
```

```
FIELD(LLIND_SDU100_NU3_SAPI9)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x09, /* reserved */
0xC0, 0x0C, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU3_SAPI9, 107)
```

```
FIELD(LLIND_SDU100_NU3_SAPI11)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x0B, /* reserved */
0xC0, 0x0C, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64 /* reserved */

```

```
ENDFIELD(LLIND_SDU100_NU3_SAPI11, 107)
```

```
/*
```

2.2.2.2.5 N(U) 4

```
*/
```

```
FIELD(LLIND_SDU100_NU4_SAPI1)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x01, /* reserved */
0xC0, 0x10, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU4_SAPI1, 107)
```

```
FIELD(LLIND_SDU100_NU4_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x03, /* reserved */
0xC0, 0x10, /* reserved */

```

```

0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU4_SAPI3, 107)
```

```
FIELD(LLIND_SDU100_NU4_SAPI5)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x05, /* reserved */
0xC0, 0x10, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU4_SAPI5, 107)
```

```
FIELD(LLIND_SDU100_NU4_SAPI7)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x07, /* reserved */
0xC0, 0x10, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU4_SAPI7, 107)
```

```
FIELD(LLIND_SDU100_NU4_SAPI9)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x09, /* reserved */
0xC0, 0x10, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU4_SAPI9, 107)
```

```
FIELD(LLIND_SDU100_NU4_SAPI11)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x0B, /* reserved */
0xC0, 0x10, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,

```

```

0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLIND_SDU100_NU4_SAPI11, 107)
```

```
/*
```

2.2.2.2.6 Reversed content, N(U) 1

```
*/
```

```
FIELD(LLIND_SDU100A_SAPI1)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x01, /* reserved */
0xC0, 0x04, /* reserved */
0x64, 0x63, 0x62, 0x61,
0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01

```

```
ENDFIELD(LLIND_SDU100A_SAPI1, 107)
```

```
FIELD(LLIND_SDU100A_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x03, /* reserved */
0xC0, 0x04, /* reserved */
0x64, 0x63, 0x62, 0x61,
0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01

```

```
ENDFIELD(LLIND_SDU100A_SAPI3, 107)
```

```
FIELD(LLIND_SDU100A_SAPI5)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x05, /* reserved */
0xC0, 0x04, /* reserved */
0x64, 0x63, 0x62, 0x61,
0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01

```

```
ENDFIELD(LLIND_SDU100A_SAPI5, 107)
```

```
FIELD(LLIND_SDU100A_SAPI7)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x07, /* reserved */
0xC0, 0x04, /* reserved */

```

```

0x64, 0x63, 0x62, 0x61,
0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01
ENDFIELD(LLIND_SDU100A_SAPI7, 107)

```

```
FIELD(LLIND_SDU100A_SAPI9)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x09, /* reserved */
0xC0, 0x04, /* reserved */
0x64, 0x63, 0x62, 0x61,
0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01

```

```
ENDFIELD(LLIND_SDU100A_SAPI9, 107)
```

```
FIELD(LLIND_SDU100A_SAPI11)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x0B, /* reserved */
0xC0, 0x04, /* reserved */
0x64, 0x63, 0x62, 0x61,
0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01

```

```
ENDFIELD(LLIND_SDU100A_SAPI11, 107)
```

```
/*
```

2.2.2.3 LL-IND Primitives, large

```
/*
```

```
FIELD(LLIND_SDU1000_SAPI1)
```

```

0x40, 0x1F, /* sdu.l_buf = 8000 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x01, /* reserved */
0xC0, 0x00, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,

```


ENDFIELD(LLIND_SDU1000_SAPI1, 1007)

FIELD(LLIND_SDU1000_SAPI3)

```
0x40, 0x1F, /* sdu.l_buf = 8000 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x03, /* reserved */
0xC0, 0x00, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
```

```

0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLIND_SDU1000_SAPI3, 1007)

FIELD(LLIND_SDU1000_SAPI5)

```

0x40, 0x1F, /* sdu.l_buf = 8000 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x05, /* reserved */
0xC0, 0x00, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,

```



```

0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLIND_SDU1000_SAPI9, 1007)

FIELD(LLIND_SDU1000_SAPI11)

```

0x40, 0x1F, /* sdu.l_buf = 8000 */
0x18, 0x00, /* sdu.o_buf = 24 */
0x0B, /* reserved */
0xC0, 0x00, /* reserved */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,

```



```

0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N0_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0x10, 0x00, /* reserved, but with N(S) = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N1_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N2_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0x20, 0x00, /* reserved, but with N(S) = 2 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N2_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N3_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0x30, 0x00, /* reserved, but with N(S) = 3 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N3_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N4_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0x40, 0x00, /* reserved, but with N(S) = 4 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N4_SAPI3, 108)

```

FIELD(LLDATAIND_SDUI00_N4A1_SAPI3)

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 and A = 1 */
0x40, 0x00, /* reserved, but with N(S) = 4 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLDATAIND_SDUI00_N4A1_SAPI3, 108)

FIELD(LLDATAIND_SDUI00_N5_SAPI3)

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0x50, 0x00, /* reserved, but with N(S) = 5 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLDATAIND_SDUI00_N5_SAPI3, 108)

FIELD(LLDATAIND_SDUI00_N6_SAPI3)

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0x60, 0x00, /* reserved, but with N(S) = 6 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLDATAIND_SDUI00_N6_SAPI3, 108)

FIELD(LLDATAIND_SDUI00_N7_SAPI3)

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0x70, 0x00, /* reserved, but with N(S) = 7 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLDATAIND_SDUI00_N7_SAPI3, 108)

FIELD(LLDATAIND_SDUI00_N8_SAPI3)

```

0x20, 0x03,          /* sdu.l_buf = 800 */
0x20, 0x00,          /* sdu.o_buf = 32 */
0x03, 0x00,          /* reserved, but with SAPI 3 */
0x80, 0x00,          /* reserved, but with N(S) = 8 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLDATAIND_SDU100_N8_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N9_SAPI3)

```

0x20, 0x03,          /* sdu.l_buf = 800 */
0x20, 0x00,          /* sdu.o_buf = 32 */
0x03, 0x00,          /* reserved, but with SAPI 3 */
0x90, 0x00,          /* reserved, but with N(S) = 9 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLDATAIND_SDU100_N9_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N10_SAPI3)

```

0x20, 0x03,          /* sdu.l_buf = 800 */
0x20, 0x00,          /* sdu.o_buf = 32 */
0x03, 0x00,          /* reserved, but with SAPI 3 */
0xA0, 0x00,          /* reserved, but with N(S) = 10 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLDATAIND_SDU100_N10_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N11_SAPI3)

```

0x20, 0x03,          /* sdu.l_buf = 800 */
0x20, 0x00,          /* sdu.o_buf = 32 */
0x03, 0x00,          /* reserved, but with SAPI 3 */
0xB0, 0x00,          /* reserved, but with N(S) = 11 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

ENDFIELD(LLDATAIND_SDU100_N11_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N12_SAPI3)

```

0x20, 0x03,          /* sdu.l_buf = 800 */
0x20, 0x00,          /* sdu.o_buf = 32 */

```

```

0x03, 0x00, /* reserved, but with SAPI 3 */
0xC0, 0x00, /* reserved, but with N(S) = 12 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N12_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDU100_N13_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0xD0, 0x00, /* reserved, but with N(S) = 13 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N13_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDU100_N14_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0xE0, 0x00, /* reserved, but with N(S) = 14 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N14_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDU100_N15_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x00, /* reserved, but with SAPI 3 */
0xF0, 0x00, /* reserved, but with N(S) = 15 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N15_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDU100_N15A1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 and A=1 */
0xF0, 0x00, /* reserved, but with N(S) = 15 */

```

```

0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLDATAIND_SDU100_N15A1_SAPI3, 108)
```

```
FIELD(LLDATAIND_SDU100_N0A1_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf= 800 */
0x20, 0x00, /* sdu.o_buf= 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0x00, 0x00, /* reserved, but with N(S) = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLDATAIND_SDU100_N0A1_SAPI3, 108)
```

```
FIELD(LLDATAIND_SDU100_N1A1_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf= 800 */
0x20, 0x00, /* sdu.o_buf= 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0x10, 0x00, /* reserved, but with N(S) = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLDATAIND_SDU100_N1A1_SAPI3, 108)
```

```
FIELD(LLDATAIND_SDU100_N2A1_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf= 800 */
0x20, 0x00, /* sdu.o_buf= 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0x20, 0x00, /* reserved, but with N(S) = 2 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLDATAIND_SDU100_N2A1_SAPI3, 108)
```

```
FIELD(LLDATAIND_SDU100_N3A1_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf= 800 */
0x20, 0x00, /* sdu.o_buf= 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0x30, 0x00, /* reserved, but with N(S) = 3 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,

```

```

0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLDATAIND_SDU100_N3A1_SAPI3, 108)
```

```
FIELD(LLDATAIND_SDU100_N5A1_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0x50, 0x00, /* reserved, but with N(S) = 5 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLDATAIND_SDU100_N5A1_SAPI3, 108)
```

```
FIELD(LLDATAIND_SDU100_N6A1_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0x60, 0x00, /* reserved, but with N(S) = 6 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLDATAIND_SDU100_N6A1_SAPI3, 108)
```

```
FIELD(LLDATAIND_SDU100_N7A1_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0x70, 0x00, /* reserved, but with N(S) = 7 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64

```

```
ENDFIELD(LLDATAIND_SDU100_N7A1_SAPI3, 108)
```

```
FIELD(LLDATAIND_SDU100_N8A1_SAPI3)
```

```

0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0x80, 0x00, /* reserved, but with N(S) = 8 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,

```

```

0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N8A1_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDU100_N9A1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0x90, 0x00, /* reserved, but with N(S) = 9 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N9A1_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDU100_N10A1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0xA0, 0x00, /* reserved, but with N(S) = 10 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N10A1_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDU100_N11A1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0xB0, 0x00, /* reserved, but with N(S) = 11 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N11A1_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDU100_N12A1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0xC0, 0x00, /* reserved, but with N(S) = 12 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,

```

```

0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N12A1_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N13A1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0xD0, 0x00, /* reserved, but with N(S) = 13 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N13A1_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N14A1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x40, /* reserved, but with SAPI 3 */
0xE0, 0x00, /* reserved, but with N(S) = 14 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N14A1_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N16A1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x41, /* reserved, but with SAPI 3, N(S) = 16 */
0x00, 0x00, /* reserved, but with N(S) = 16 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N16A1_SAPI3, 108)

FIELD(LLDATAIND_SDU100_N17A1_SAPI3)
0x20, 0x03, /* sdu.l_buf = 800 */
0x20, 0x00, /* sdu.o_buf = 32 */
0x03, 0x41, /* reserved, but with SAPI 3, N(S) = 17 */
0x10, 0x00, /* reserved, but with N(S) = 17 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDU100_N17A1_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDUI00_N18A1_SAPI3)
    0x20, 0x03, /* sdu.l_buf = 800 */
    0x20, 0x00, /* sdu.o_buf = 32 */
    0x03, 0x41, /* reserved, but with SAPI 3 */
    0x20, 0x00, /* reserved, but with N(S) = 18 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDUI00_N18A1_SAPI3, 108)

```

```

FIELD(LLDATAIND_SDUI00_N19A1_SAPI3)
    0x20, 0x03, /* sdu.l_buf = 800 */
    0x20, 0x00, /* sdu.o_buf = 32 */
    0x03, 0x41, /* reserved, but with SAPI 3 */
    0x30, 0x00, /* reserved, but with N(S) = 19 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64
ENDFIELD(LLDATAIND_SDUI00_N19A1_SAPI3, 108)

```

```

FIELD(LLDATAIND_1OCT_N0A1_SAPI3)
    0x08, 0x00, /* sdu.l_buf = 1*8 */
    0x20, 0x00, /* sdu.o_buf = 32 */
    0x03, 0x40, /* reserved, but with SAPI 3 */
    0x00, 0x00, /* reserved, but with N(S) = 0 */
    0x01
ENDFIELD(LLDATAIND_1OCT_N0A1_SAPI3, 9)

```

/*

2.2.3 GRR SDU Fields / Descriptor Lists

*/

/*

2.2.3.1 GRR-IND Data Primitives

*/

```

FIELD(GRRIND_NS0_NR0_A1_SDUI1_SAPI3)
    0x40, 0x00, /* sdu.l_buf = 8*8 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0x40, 0x00, 0x00, /* Control field (I frame): N(S) = 0, N(R)=0, .A=1, RR */
    0x01,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS0_NR0_A1_SDUI1_SAPI3, 12)

```

```

FIELD(GRRIND_NS0_NR0_SDUI00_SAPI3)
    0x58, 0x03, /* sdu.l_buf = 856 */
    0x00, 0x00, /* sdu.o_buf = 0 */

```

```

0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0x00, 0x00, /* Control field (I frame): N(S) = 0, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS0_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS1_NR0_SDU100_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0x10, 0x00, /* Control field (I frame): N(S) = 1, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS1_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS2_NR0_SDU100_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0x20, 0x00, /* Control field (I frame): N(S) = 2, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS2_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS3_NR0_SDU100_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0x30, 0x00, /* Control field (I frame): N(S) = 3, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS3_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS4_NR0_SDU100_SAPI3)

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0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0x40, 0x00, /* Control field (I frame): N(S) = 4, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS4_NR0_SDU100_SAPI3, 111)

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FIELD(GRRIND_NS4_NR0_SDU100_A1_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x40, 0x00, /* Control field (I frame): N(S) = 4, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS4_NR0_SDU100_A1_SAPI3, 111)

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FIELD(GRRIND_NS4_NR2_SDU100_A1_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x40, 0x08, /* Control field (I frame): N(S) = 4, N(R)=8, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS4_NR2_SDU100_A1_SAPI3, 111)

```

```

FIELD(GRRIND_NS5_NR0_SDU100_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0x50, 0x00, /* Control field (I frame): N(S) = 5, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS5_NR0_SDU100_SAPI3, 111)

```

```

FIELD(GRRIND_NS6_NR0_SDU100_SAPI3)
    0x58, 0x03,                /* sdu.l_buf = 856 */
    0x00, 0x00,                /* sdu.o_buf = 0 */
    0x03,                       /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0x00, 0x60, 0x00,          /* Control field (I frame): N(S) = 6, N(R)=0, A=0, RR */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00          /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS6_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS7_NR0_SDU100_SAPI3)
    0x58, 0x03,                /* sdu.l_buf = 856 */
    0x00, 0x00,                /* sdu.o_buf = 0 */
    0x03,                       /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0x00, 0x70, 0x00,          /* Control field (I frame): N(S) = 7, N(R)=0, A=0, RR */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00          /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS7_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS8_NR0_SDU100_SAPI3)
    0x58, 0x03,                /* sdu.l_buf = 856 */
    0x00, 0x00,                /* sdu.o_buf = 0 */
    0x03,                       /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0x00, 0x80, 0x00,          /* Control field (I frame): N(S) = 8, N(R)=0, A=0, RR */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00          /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS8_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS9_NR0_SDU100_SAPI3)
    0x58, 0x03,                /* sdu.l_buf = 856 */
    0x00, 0x00,                /* sdu.o_buf = 0 */
    0x03,                       /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0x00, 0x90, 0x00,          /* Control field (I frame): N(S) = 9, N(R)=0, A=0, RR */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,

```

```

0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS9_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS10_NR0_SDU100_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0xA0, 0x00,                               /* Control field (I frame): N(S) = 10, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS10_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS11_NR0_SDU100_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0xB0, 0x00,                               /* Control field (I frame): N(S) = 11, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS11_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS12_NR0_SDU100_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0xC0, 0x00,                               /* Control field (I frame): N(S) = 12, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS12_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS13_NR0_SDU100_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0xD0, 0x00,                               /* Control field (I frame): N(S) = 13, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,

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0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS13_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS14_NR0_SDU100_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0xE0, 0x00,                               /* Control field (I frame): N(S) = 14, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS14_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS15_NR0_SDU100_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xF0, 0x00,                               /* Control field (I frame): N(S) = 15, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS15_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS16_NR0_SDU100_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x01, 0x00, 0x00,                               /* Control field (I frame): N(S) = 16, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS16_NR0_SDU100_SAPI3, 111)

FIELD(GRRIND_NS0_NR0_SAPI5)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x05,                                           /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x00, 0x00, 0x00,                               /* Control field (I frame): N(S) = 0, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,

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0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS0_NR0_SAPI5, 111)

FIELD(GRRIND_NS1_NR0_SAPI5)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x05,                                             /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x00, 0x01, 0x00,                                 /* Control field (I frame): N(S) = 1, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS1_NR0_SAPI5, 111)

FIELD(GRRIND_NS2_NR0_SAPI5)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x05,                                             /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x00, 0x02, 0x00,                                 /* Control field (I frame): N(S) = 2, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS2_NR0_SAPI5, 111)

FIELD(GRRIND_NS3_NR0_SAPI5)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x05,                                             /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x00, 0x03, 0x00,                                 /* Control field (I frame): N(S) = 3, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS3_NR0_SAPI5, 111)

FIELD(GRRIND_NS4_NR0_SAPI5)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x05,                                             /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x00, 0x04, 0x00,                                 /* Control field (I frame): N(S) = 4, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,

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0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS4_NR0_SAPI5, 111)

FIELD(GRRIND_NS5_NR0_SAPI5)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x05,                                             /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x00, 0x05, 0x00,                                 /* Control field (I frame): N(S) = 5, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS5_NR0_SAPI5, 111)

FIELD(GRRIND_NS6_NR0_SAPI5)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x05,                                             /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x00, 0x06, 0x00,                                 /* Control field (I frame): N(S) = 6, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS6_NR0_SAPI5, 111)

FIELD(GRRIND_NS7_NR0_SAPI5)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x05,                                             /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x40, 0x07, 0x00,                                 /* Control field (I frame): N(S) = 7, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS7_NR0_SAPI5, 111)

FIELD(GRRIND_NS8_NR0_SAPI5)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x05,                                             /* Address field: PD = 0, C/R = 0, SAPI = 5 */

```

```

0x00, 0x80, 0x00, /* Control field (I frame): N(S) = 8, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS8_NR0_SAPI5, 111)

FIELD(GRRIND_NS0_NR0_SAPI9)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x00, 0x00, 0x00, /* Control field (I frame): N(S) = 0, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS0_NR0_SAPI9, 111)

FIELD(GRRIND_NS1_NR0_SAPI9)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x00, 0x01, 0x00, /* Control field (I frame): N(S) = 1, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS1_NR0_SAPI9, 111)

FIELD(GRRIND_NS2_NR0_SAPI9)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x00, 0x02, 0x00, /* Control field (I frame): N(S) = 2, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS2_NR0_SAPI9, 111)

FIELD(GRRIND_NS3_NR0_SAPI9)
0x58, 0x03, /* sdu.l_buf = 856 */

```

```

0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9*/
0x40, 0x03, 0x00, /* Control field (I frame): N(S) = 3, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS3_NR0_SAPI9, 111)

```

```

FIELD(GRRIND_NS4_NR0_SAPI9)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9*/
0x00, 0x40, 0x00, /* Control field (I frame): N(S) = 4, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS4_NR0_SAPI9, 111)

```

```

FIELD(GRRIND_NS0_NR0_SAPI11)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11*/
0x00, 0x00, 0x00, /* Control field (I frame): N(S) = 0, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS0_NR0_SAPI11, 111)

```

```

FIELD(GRRIND_NS1_NR0_SAPI11)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11*/
0x40, 0x01, 0x00, /* Control field (I frame): N(S) = 1, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS1_NR0_SAPI11, 111)

```

FIELD(GRRIND_NS2_NR0_SAPI11)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
0x00, 0x20, 0x00, /* Control field (I frame): N(S) = 2, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_NS2_NR0_SAPI11, 111)

FIELD(GRRIND_NS0_NR0_SDU100_A1_SAPI3)

```

0x58, 0x03, /* sdu.l_buf=107*8= 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x00, 0x00, /* Control field (I frame): N(S) = 0, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_NS0_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS1_NR0_SDU100_A1_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x10, 0x00, /* Control field (I frame): N(S) = 1, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_NS1_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS2_NR0_SDU100_A1_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x20, 0x00, /* Control field (I frame): N(S) = 2, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_NS2_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS3_NR0_SDU100_A1_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x30, 0x00, /* Control field (I frame): N(S) = 3, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_NS3_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS5_NR0_SDU100_A1_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x50, 0x00, /* Control field (I frame): N(S) = 5, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_NS5_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS6_NR0_SDU100_A1_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x60, 0x00, /* Control field (I frame): N(S) = 6, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_NS6_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS7_NR0_SDU100_A1_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x70, 0x00, /* Control field (I frame): N(S) = 7, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,

```

```

0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS7_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS8_NR0_SDU100_A1_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x80, 0x00,                                 /* Control field (I frame): N(S) = 8, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS8_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS9_NR0_SDU100_A1_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x90, 0x00,                                 /* Control field (I frame): N(S) = 9, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS9_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS10_NR0_SDU100_A1_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xA0, 0x00,                                 /* Control field (I frame): N(S) = 10, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS10_NR0_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS11_NR0_SDU100_A1_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xB0, 0x00,                                 /* Control field (I frame): N(S) = 11, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,

```

```

0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS11_NRO_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS12_NRO_SDU100_A1_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xC0, 0x00,                                 /* Control field (I frame): N(S) = 12, N(R)=0, A=1, RR*/
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS12_NRO_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS13_NRO_SDU100_A1_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xD0, 0x00,                                 /* Control field (I frame): N(S) = 13, N(R)=0, A=1, RR*/
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS13_NRO_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS14_NRO_SDU100_A1_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xE0, 0x00,                                 /* Control field (I frame): N(S) = 14, N(R)=0, A=1, RR*/
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS14_NRO_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS15_NRO_SDU100_A1_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xF0, 0x00,                                 /* Control field (I frame): N(S) = 15, N(R)=0, A=1, RR*/
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,

```

```

0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS15_NRO_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS16_NRO_SDU100_A1_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x41, 0x00, 0x00,                               /* Control field (I frame): N(S) = 16, N(R)=0, A=1, RR*/
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F,
0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS16_NRO_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS17_NRO_SDU100_A1_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x41, 0x10, 0x00,                               /* Control field (I frame): N(S) = 17, N(R)=0, A=1, RR*/
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F,
0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS17_NRO_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS18_NRO_SDU100_A1_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x41, 0x20, 0x00,                               /* Control field (I frame): N(S) = 18, N(R)=0, A=1, RR*/
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F,
0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS18_NRO_SDU100_A1_SAPI3, 111)

FIELD(GRRIND_NS19_NRO_SDU100_A1_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */

```

```

0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x41, 0x30, 0x00, /* Control field (I frame): N(S) = 19, N(R)=0, A=1, RR*/
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F,
0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS19_NRO_SDU100_A1_SAPI3, 111)

```

```

FIELD(GRRIND_NS20_NRO_SDU100_A1_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x41, 0x40, 0x00, /* Control field (I frame): N(S) = 20, N(R)=0, A=1, RR*/
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F,
0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_NS20_NRO_SDU100_A1_SAPI3, 111)

```

/*

2.2.3.2 GRR-REQ Data Primitives, small

*/

/*

2.2.3.2.1 N(U) 0

*/

```

FIELD(GRRREQ_SDU100_SAPI1)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI1, 110)

```

```

FIELD(GRRREQ_SDU100_SAPI3)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */

```

```

0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI3, 110)

FIELD(GRRREQ_SDU100_SAPI3_NU1)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xC0, 0x04, /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI3_NU1, 110)

FIELD(GRRREQ_SDU100_SAPI5)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x05, /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI5, 110)

FIELD(GRRREQ_SDU100_SAPI7)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x07, /* Address field: PD = 0, C/R = 0, SAPI = 7 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI7, 110)

FIELD(GRRREQ_SDU100_SAPI9)
0x50, 0x03, /* sdu.l_buf = 848 */

```

```

0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI9, 110)

```

```

FIELD(GRRREQ_SDU100_SAPI11)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI11, 110)

```

```

FIELD(GRRREQ_SDU100_SAPI1_PM)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
0xC0, 0x01, /* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI1_PM, 110)

```

```

FIELD(GRRREQ_SDU100_SAPI3_PM)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xC0, 0x01, /* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI3_PM, 110)

```

```

FIELD(GRRREQ_SDU100_SAPI5_PM)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x05, /* Address field: PD = 0, C/R = 0, SAPI = 5 */
    0xC0, 0x01, /* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI5_PM, 110)

FIELD(GRRREQ_SDU100_SAPI7_PM)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x07, /* Address field: PD = 0, C/R = 0, SAPI = 7 */
    0xC0, 0x01, /* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI7_PM, 110)

FIELD(GRRREQ_SDU100_SAPI9_PM)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0xC0, 0x01, /* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI9_PM, 110)

FIELD(GRRREQ_SDU100_SAPI11_PM)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
    0xC0, 0x01, /* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRREQ_SDU100_SAPI11_PM, 110)

FIELD(GRRREQ_SDU0_SAPI1_PM0)

```

48, 0, /* sdu.l_buf = 48 */
0, 0, /* sdu.o_buf = 0 */
0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRREQ_SDU0_SAPI1_PM0, 10)

*/

2.2.3.2.2 N(U) 1

*/

FIELD(GRRREQ_SDU100_SAPI1_NU1_PM)

```

0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
0xC0, 0x05, /* Control field (UI frame): N(U) = 1, E = 0, PM = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRREQ_SDU100_SAPI1_NU1_PM, 110)

FIELD(GRRREQ_SDU0_SAPI1_NU1_PM)

```

48, 0, /* sdu.l_buf = 48 */
0, 0, /* sdu.o_buf = 0 */
0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
0xC0, 0x05, /* Control field (UI frame): N(U) = 1, E = 0, PM = 1 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRREQ_SDU0_SAPI1_NU1_PM, 10)

*/

2.2.3.2.3 N(U) 2

*/

FIELD(GRRREQ_SDU100_SAPI1_NU2_PM)

```

0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
0xC0, 0x09, /* Control field (UI frame): N(U) = 2, E = 0, PM = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRREQ_SDU100_SAPI1_NU2_PM, 110)

*/

2.2.3.2.4 N(U) 3

*/

```

FIELD(GRRREQ_SDU100_SAPI1_NU3_PM)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
    0xC0, 0x0D, /* Control field (UI frame): N(U) = 3, E = 0, PM = 1 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI1_NU3_PM, 110)
*/

```

2.2.3.2.5 N(U) 4

```

*/
FIELD(GRRREQ_SDU100_SAPI1_NU4_PM)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
    0xC0, 0x11, /* Control field (UI frame): N(U) = 4, E = 0, PM = 1 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI1_NU4_PM, 110)
*/

```

2.2.3.2.6 N(U) 5

```

*/
FIELD(GRRREQ_SDU100_SAPI1_NU5_PM)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
    0xC0, 0x15, /* Control field (UI frame): N(U) = 5, E = 0, PM = 1 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI1_NU5_PM, 110)
*/

```

2.2.3.2.7 N(U) 6

```

*/
FIELD(GRRREQ_SDU100_SAPI1_NU6_PM)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */

```

```

0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
0xC0, 0x19, /* Control field (UI frame): N(U) = 6, E = 0, PM = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU100_SAPI1_NU6_PM, 110)
*/

```

2.2.3.3 GRR-REQ Data Primitives, large

```

*/
FIELD(GRRREQ_SDU1000_SAPI1)
0x70, 0x1F, /* sdu.l_buf = 8048 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x01, /* Address field: PD = 0, C/R = 0, SAPI = 1 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,

```

```

0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRRREQ_SDU1000_SAPI1, 1010)
```

```
FIELD(GRRREQ_SDU1000_SAPI3)
```

```

0x70, 0x1F, /* sdu.l_buf = 8048 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,

```

```

0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

/* FCS field: all 0x00 in simulation */

ENDFIELD(GRRREQ_SDU1000_SAPI3, 1010)

FIELD(GRRREQ_SDU1000_SAPI5)

```

0x70, 0x1F,
0x00, 0x00,

```

/* sdu.l_buf = 8048 */

/* sdu.o_buf = 0 */


```

0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU1000_SAPI5, 1010)

FIELD(GRRREQ_SDU1000_SAPI7)
0x70, 0x1F,                                     /* sdu.l_buf = 8048 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x07,                                           /* Address field: PD = 0, C/R = 0, SAPI = 7 */
0xC0, 0x00,                                    /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,

```

```

0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRRREQ_SDU1000_SAPI7, 1010)
```

```
FIELD(GRRREQ_SDU1000_SAPI9)
```

```

0x70, 0x1F,
0x00, 0x00,
0x09,
0xC0, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,

```

```
/* sdu.l_buf = 8048 */
```

```
/* sdu.o_buf = 0 */
```

```
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
```

```
/* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
```



```

0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00
/* FCS field: all 0x00 in simulation */

```

```
ENDFIELD(GRRREQ_SDU1000_SAPI1_PM, 1010)
```

```
FIELD(GRRREQ_SDU1000_SAPI3_PM)
```

```

0x70, 0x1F,
0x00, 0x00,
0x03,
0xC0, 0x01,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,

```


FIELD(GRRREQ_SDU1000_SAPI5_PM)

```

0x70, 0x1F, /* sdu.l_buf = 8048 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x05, /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0xC0, 0x01, /* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,

```

```

0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU1000_SAPI5_PM, 1010)

FIELD(GRRREQ_SDU1000_SAPI7_PM)
0x70, 0x1F,                                       /* sdu.l_buf = 8048 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x07,                                             /* Address field: PD = 0, C/R = 0, SAPI = 7 */
0xC0, 0x01,                                       /* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,

```

```

0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRRREQ_SDU1000_SAPI7_PM, 1010)
```

```
FIELD(GRRREQ_SDU1000_SAPI9_PM)
```

```

0x70, 0x1F,
0x00, 0x00,
0x09,
0xC0, 0x01,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,

```

```
/* sdu.l_buf = 8048 */
```

```
/* sdu.o_buf = 0 */
```

```
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
```

```
/* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
```



```

0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRREQ_SDU1000_SAPI9_PM, 1010)

FIELD(GRRREQ_SDU1000_SAPI11_PM)
0x70, 0x1F,                                     /* sdu.l_buf = 8048 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x0B,                                           /* Address field: PD = 0, C/R = 0, SAPI = 11 */
0xC0, 0x01,                                     /* Control field (UI frame): N(U) = 0, E = 0, PM = 1 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,

```

```

0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRRREQ_SDU1000_SAPI11_PM, 1010)
```

```
/*
```

2.2.3.4 GRR-DATA-REQ primitives commands/responses

```
*/
```

```
FIELD(GRR_DATAREQ_1OCT_NS0_NR0_SAPI3)
```

```
0x40, 0x00,
```

```
/* sdu.l_buf = 64 */
```

```
0x00, 0x00,
```

```
/* sdu.o_buf = 0 */
```

```
0x03,
```

```
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
```

```
0x40, 0x00, 0x00,
```

```
/* Control field (I frame): N(S) = 0, N(R)=0, A=1, RR */
```

```
0x01,
```

```
0x00, 0x00, 0x00
```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRR_DATAREQ_1OCT_NS0_NR0_SAPI3, 12)
```

```
FIELD(GRR_DATAREQ_1OCT_NS0_NR1_SAPI3)
```

```
0x40, 0x00,
```

```
/* sdu.l_buf = 64 */
```

```
0x00, 0x00,
```

```
/* sdu.o_buf = 0 */
```

```
0x03,
```

```
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
```

```
0x40, 0x00, 0x04,
```

```
/* Control field (I frame): N(S) = 0, N(R)=1, A=1, RR */
```

```
0x01,
```

```
0x00, 0x00, 0x00
```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRR_DATAREQ_1OCT_NS0_NR1_SAPI3, 12)
```

```
FIELD(GRR_DATAREQ_NS0_NR0_SAPI3)
```

```
0x58, 0x03,
```

```
/* sdu.l_buf = 856 */
```

```
0x00, 0x00,
```

```
/* sdu.o_buf = 0 */
```

```
0x03,
```

```
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
```

```
0x40, 0x00, 0x00,
```

```
/* Control field (I frame): N(S) = 0, N(R)=0, A=1, RR */
```

```
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
```

```
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
```

```
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
```

```
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
```

```
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
```

```
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
```

```
0x61, 0x62, 0x63, 0x64,
```

```
0x00, 0x00, 0x00
```

```
/* FCS field: all 0x00 in simulation */
```

ENDFIELD(GRR_DATAREQ_NS0_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS0_NR2_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x00, 0x08, /* Control field (I frame): N(S) = 0, N(R)=2, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRR_DATAREQ_NS0_NR2_SAPI3, 111)

FIELD(GRR_DATAREQ_NS0_NR0_A0_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0x00, 0x00, /* Control field (I frame): N(S) = 0, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRR_DATAREQ_NS0_NR0_A0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS1_NR0_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x10, 0x00, /* Control field (I frame): N(S) = 1, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRR_DATAREQ_NS1_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS1_NR2_SAPI3)

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x10, 0x08, /* Control field (I frame): N(S) = 1, N(R)=2, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,

```

```

0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS1_NR2_SAPI3, 111)

FIELD(GRR_DATAREQ_NS2_NR0_A1_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x20, 0x00,                               /* Control field (I frame): N(S) = 2, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS2_NR0_A1_SAPI3, 111)

FIELD(GRR_DATAREQ_NS2_NR2_A1_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x20, 0x08,                               /* Control field (I frame): N(S) = 2, N(R)=2, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS2_NR2_A1_SAPI3, 111)

FIELD(GRR_DATAREQ_NS3_NR0_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x30, 0x00,                               /* Control field (I frame): N(S) = 3, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS3_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS3_NR5_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x30, 0x14,                               /* Control field (I frame): N(S) = 3, N(R)=5, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,

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0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS3_NR5_SAPI3, 111)

FIELD(GRR_DATAREQ_NS4_NR0_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x40, 0x00,                                 /* Control field (I frame): N(S) = 4, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS4_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS4_NR5_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x40, 0x14,                                 /* Control field (I frame): N(S) = 4, N(R)=5, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS4_NR5_SAPI3, 111)

FIELD(GRR_DATAREQ_NS4_NR0_A0_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0x40, 0x00,                                 /* Control field (I frame): N(S) = 4, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS4_NR0_A0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS5_NR0_SAPI3)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x50, 0x00,                                 /* Control field (I frame): N(S) = 5, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,

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0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS5_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS6_NR0_A1_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x60, 0x00,                               /* Control field (I frame): N(S) = 6, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS6_NR0_A1_SAPI3, 111)

FIELD(GRR_DATAREQ_NS7_NR0_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x70, 0x00,                               /* Control field (I frame): N(S) = 7, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS7_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS8_NR0_A1_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x80, 0x00,                               /* Control field (I frame): N(S) = 8, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS8_NR0_A1_SAPI3, 111)

FIELD(GRR_DATAREQ_NS8_NR0_A0_SAPI3)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x00, 0x80, 0x00,                               /* Control field (I frame): N(S) = 8, N(R)=0, A=1, RR */

```

```

0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRR_DATAREQ_NS8_NR0_A0_SAPI3, 111)
```

```
FIELD(GRR_DATAREQ_NS9_NR0_SAPI3)
```

```

0x58, 0x03,
0x00, 0x00,
0x03,
0x40, 0x90, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* sdu.l_buf = 856 */
```

```
/* sdu.o_buf = 0 */
```

```
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
```

```
/* Control field (I frame): N(S) = 9, N(R)=0, A=1, RR */
```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRR_DATAREQ_NS9_NR0_SAPI3, 111)
```

```
FIELD(GRR_DATAREQ_NS10_NR0_SAPI3)
```

```

0x58, 0x03,
0x00, 0x00,
0x03,
0x40, 0xA0, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* sdu.l_buf = 856 */
```

```
/* sdu.o_buf = 0 */
```

```
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
```

```
/* Control field (I frame): N(S) = 10, N(R)=0, A=1, RR */
```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRR_DATAREQ_NS10_NR0_SAPI3, 111)
```

```
FIELD(GRR_DATAREQ_NS11_NR0_SAPI3)
```

```

0x58, 0x03,
0x00, 0x00,
0x03,
0x40, 0xB0, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* sdu.l_buf = 856 */
```

```
/* sdu.o_buf = 0 */
```

```
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
```

```
/* Control field (I frame): N(S) = 11, N(R)=0, A=1, RR */
```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRR_DATAREQ_NS11_NR0_SAPI3, 111)
```

```
FIELD(GRR_DATAREQ_NS12_NR0_SAPI3)
```

```

0x58, 0x03,
0x00, 0x00,

```

```
/* sdu.l_buf = 856 */
```

```
/* sdu.o_buf = 0 */
```

```

0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xC0, 0x00, /* Control field (I frame): N(S) = 12, N(R)=0,.A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS12_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS13_NR0_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xD0, 0x00, /* Control field (I frame): N(S) = 13, N(R)=0,.A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS13_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS14_NR0_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xE0, 0x00, /* Control field (I frame): N(S) = 14, N(R)=0,.A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS14_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS15_NR0_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0xF0, 0x00, /* Control field (I frame): N(S) = 15, N(R)=0,.A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS15_NR0_SAPI3, 111)

FIELD(GRR_DATAREQ_NS16_NR0_A0_SAPI3)

```

```

0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x01, 0x00, 0x00, /* Control field (I frame): N(S) = 16, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS16_NR0_A0_SAPI3, 111)

```

```

FIELD(GRR_DATAREQ_NS16_NR0_A1_SAPI3)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x41, 0x00, 0x00, /* Control field (I frame): N(S) = 16, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS16_NR0_A1_SAPI3, 111)

```

```

FIELD(GRR_DATAREQ_NS0_NR0_SAPI5)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x05, /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x40, 0x00, 0x00, /* Control field (I frame): N(S) = 0, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS0_NR0_SAPI5, 111)

```

```

FIELD(GRR_DATAREQ_NS0_NR0_A0_SAPI5)
0x58, 0x03, /* sdu.l_buf = 856 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x05, /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x00, 0x00, 0x00, /* Control field (I frame): N(S) = 0, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS0_NR0_A0_SAPI5, 111)

```

```

FIELD(GRR_DATAREQ_NS1_NR0_SAPI5)
    0x58, 0x03,                /* sdu.l_buf = 856 */
    0x00, 0x00,                /* sdu.o_buf = 0 */
    0x05,                       /* Address field: PD = 0, C/R = 0, SAPI = 5 */
    0x40, 0x10, 0x00,          /* Control field (I frame): N(S) = 1, N(R)=0, A=1, RR */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00          /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS1_NR0_SAPI5, 111)

```

```

FIELD(GRR_DATAREQ_NS2_NR0_A1_SAPI5)
    0x58, 0x03,                /* sdu.l_buf = 856 */
    0x00, 0x00,                /* sdu.o_buf = 0 */
    0x05,                       /* Address field: PD = 0, C/R = 0, SAPI = 5 */
    0x40, 0x20, 0x00,          /* Control field (I frame): N(S) = 2, N(R)=0, A=1, RR */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00          /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS2_NR0_A1_SAPI5, 111)

```

```

FIELD(GRR_DATAREQ_NS2_NR0_A0_SAPI5)
    0x58, 0x03,                /* sdu.l_buf = 856 */
    0x00, 0x00,                /* sdu.o_buf = 0 */
    0x05,                       /* Address field: PD = 0, C/R = 0, SAPI = 5 */
    0x00, 0x20, 0x00,          /* Control field (I frame): N(S) = 2, N(R)=0, A=0, RR */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00          /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS2_NR0_A0_SAPI5, 111)

```

```

FIELD(GRR_DATAREQ_NS3_NR0_SAPI5)
    0x58, 0x03,                /* sdu.l_buf = 856 */
    0x00, 0x00,                /* sdu.o_buf = 0 */
    0x05,                       /* Address field: PD = 0, C/R = 0, SAPI = 5 */
    0x40, 0x30, 0x00,          /* Control field (I frame): N(S) = 3, N(R)=0, A=1, RR */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,

```

```

0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS3_NR0_SAPI5, 111)

FIELD(GRR_DATAREQ_NS4_NR0_SAPI5)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x05,                                           /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x40, 0x40, 0x00,                               /* Control field (I frame): N(S) = 4, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS4_NR0_SAPI5, 111)

FIELD(GRR_DATAREQ_NS5_NR0_SAPI5)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x05,                                           /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x40, 0x50, 0x00,                               /* Control field (I frame): N(S) = 5, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS5_NR0_SAPI5, 111)

FIELD(GRR_DATAREQ_NS6_NR0_A0_SAPI5)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x05,                                           /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x00, 0x60, 0x00,                               /* Control field (I frame): N(S) = 6, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS6_NR0_A0_SAPI5, 111)

FIELD(GRR_DATAREQ_NS6_NR0_A1_SAPI5)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x05,                                           /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x40, 0x60, 0x00,                               /* Control field (I frame): N(S) = 6, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,

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0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS6_NR0_A1_SAPI5, 111)

FIELD(GRR_DATAREQ_NS7_NR0_SAPI5)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x05,                                           /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x40, 0x70, 0x00,                               /* Control field (I frame): N(S) = 7, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS7_NR0_SAPI5, 111)

FIELD(GRR_DATAREQ_NS8_NR0_A1_SAPI5)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x05,                                           /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x40, 0x80, 0x00,                               /* Control field (I frame): N(S) = 8, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS8_NR0_A1_SAPI5, 111)

FIELD(GRR_DATAREQ_NS0_NR0_A0_SAPI9)
0x58, 0x03,                                     /* sdu.l_buf = 856 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x09,                                           /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x00, 0x00, 0x00,                               /* Control field (I frame): N(S) = 0, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS0_NR0_A0_SAPI9, 111)

FIELD(GRR_DATAREQ_NS0_NR0_500_SAPI9)
0xD8, 0x0F,                                     /* sdu.l_buf = 4056 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x09,                                           /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x00, 0x00, 0x00,                               /* Control field (I frame): N(S) = 0, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,

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0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRR_DATAREQ_NS0_NR0_500_SAPI9, 511)
```

```
FIELD(GRR_DATAREQ_NS0_NR0_A1_500_SAPI9)
```

```

0xD8, 0x0F, /* sdu.l_buf = 4056 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x40, 0x00, 0x00, /* Control field (I frame): N(S) = 0, N(R) = 0, A = 0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,

```



```

0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS0_NR0_A1_500_SAPI3, 511)

FIELD(GRR_DATAREQ_NS1_NR0_A1_500_SAPI9)
0xD8, 0x0F,                                     /* sdu.l_buf = 4056 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x09,                                           /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x40, 0x10, 0x00,                               /* Control field (I frame): N(S) = 1, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                               /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS1_NR0_A1_500_SAPI9, 511)

FIELD(GRR_DATAREQ_NS1_NR0_A1_500_SAPI3)
0xD8, 0x0F,                                     /* sdu.l_buf = 4056 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x40, 0x10, 0x00,                               /* Control field (I frame): N(S) = 1, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,

```



```

0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00
/* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS2_NR0_A1_500_SAPI9, 511)

FIELD(GRR_DATAREQ_NS0_NR0_SAPI9)
0x58, 0x03,
0x00, 0x00,
0x09,
0x40, 0x00, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00
/* sdu.l_buf = 856 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
/* Control field (I frame): N(S) = 0, N(R)=0, A=1, RR */
/* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS0_NR0_SAPI9, 111)

FIELD(GRR_DATAREQ_NS1_NR0_SAPI9)
0x58, 0x03,
0x00, 0x00,
0x09,
0x40, 0x10, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00
/* sdu.l_buf = 856 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
/* Control field (I frame): N(S) = 1, N(R)=0, A=1, RR */
/* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS1_NR0_SAPI9, 111)

FIELD(GRR_DATAREQ_NS1_NR0_A0_SAPI9)
0x58, 0x03,
0x00, 0x00,
0x09,
0x00, 0x10, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,

```

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0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS1_NR0_A0_SAPI9, 111)

FIELD(GRR_DATAREQ_NS2_NR0_A0_SAPI9)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x09,                                             /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x00, 0x20, 0x00,                                 /* Control field (I frame): N(S) = 2, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS2_NR0_A0_SAPI9, 111)

FIELD(GRR_DATAREQ_NS2_NR0_A1_SAPI9)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x09,                                             /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x40, 0x20, 0x00,                                 /* Control field (I frame): N(S) = 2, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS2_NR0_A1_SAPI9, 111)

FIELD(GRR_DATAREQ_NS3_NR0_SAPI9)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x09,                                             /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x40, 0x30, 0x00,                                 /* Control field (I frame): N(S) = 3, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS3_NR0_SAPI9, 111)

FIELD(GRR_DATAREQ_NS4_NR0_A0_SAPI9)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x09,                                             /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x00, 0x40, 0x00,                                 /* Control field (I frame): N(S) = 4, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,

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0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS4_NR0_A0_SAPI9, 111)

FIELD(GRR_DATAREQ_NS4_NR0_A1_SAPI9)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x09,                                             /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0x40, 0x40, 0x00,                                 /* Control field (I frame): N(S) = 4, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS4_NR0_A1_SAPI9, 111)

FIELD(GRR_DATAREQ_NS0_NR0_SAPI11)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x0B,                                             /* Address field: PD = 0, C/R = 0, SAPI = 11 */
0x40, 0x00, 0x00,                                 /* Control field (I frame): N(S) = 0, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS0_NR0_SAPI11, 111)

FIELD(GRR_DATAREQ_NS0_NR0_A0_SAPI11)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x0B,                                             /* Address field: PD = 0, C/R = 0, SAPI = 11 */
0x00, 0x00, 0x00,                                 /* Control field (I frame): N(S) = 0, N(R)=0, A=0, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS0_NR0_A0_SAPI11, 111)

FIELD(GRR_DATAREQ_NS1_NR0_SAPI11)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x0B,                                             /* Address field: PD = 0, C/R = 0, SAPI = 11 */
0x40, 0x10, 0x00,                                 /* Control field (I frame): N(S) = 1, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,

```

```

0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS1_NR0_SAPI11, 111)

FIELD(GRR_DATAREQ_NS2_NR0_A1_SAPI11)
0x58, 0x03,                                       /* sdu.l_buf = 856 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x0B,                                             /* Address field: PD = 0, C/R = 0, SAPI = 11 */
0x40, 0x20, 0x00,                                 /* Control field (I frame): N(S) = 2, N(R)=0, A=1, RR */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRR_DATAREQ_NS2_NR0_A1_SAPI11, 111)

```

/*

2.2.3.5 GRR-IND ACK commands/responses

*/

```

FIELD(GRRIND_ACK_NR9_SAPI3)
0x30, 0x00,                                       /* sdu.l_buf = 48 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x80, 0x25,                                       /* N(R)=9, S1=0, S2=1, A=0 */
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_ACK_NR9_SAPI3, 10)

FIELD(GRRIND_ACK_NR2_SAPI3)
0x30, 0x00,                                       /* sdu.l_buf = 48 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x80, 0x09,                                       /* N(R)=2, S1=0, S2=1, A=0 */
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_ACK_NR2_SAPI3, 10)

FIELD(GRRIND_ACK_NR19_SAPI3)
0x30, 0x00,                                       /* sdu.l_buf = 48 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x03,                                             /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0x80, 0x4D,                                       /* N(R)=19, S1=0, S2=1, A=0 */
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_ACK_NR19_SAPI3, 10)

FIELD(GRRIND_ACK_NR2_SAPI5)
0x30, 0x00,                                       /* sdu.l_buf = 48 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x05,                                             /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0x80, 0x09,                                       /* N(R)=2, S1=0, S2=1, A=0 */
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_ACK_NR2_SAPI5, 10)

FIELD(GRRIND_ACK_NR3_SAPI5)

0x30, 0x00,
0x00, 0x00,
0x05,
0x80, 0x0D,
0x00, 0x00, 0x00

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 5 */*
/ N(R)=3, S1=0, S2=1, A=0 */*
/ FCS field: all 0x00 in simulation */*

ENDFIELD(GRRIND_ACK_NR3_SAPI5, 10)

FIELD(GRRIND_ACK_NR4_SAPI5)

0x30, 0x00,
0x00, 0x00,
0x05,
0x80, 0x11,
0x00, 0x00, 0x00

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 5 */*
/ N(R)=4, S1=0, S2=1, A=0 */*
/ FCS field: all 0x00 in simulation */*

ENDFIELD(GRRIND_ACK_NR4_SAPI5, 10)

FIELD(GRRIND_ACK_NR1_SAPI9)

0x30, 0x00,
0x00, 0x00,
0x09,
0x80, 0x05,
0x00, 0x00, 0x00

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 9 */*
/ N(R)=1, S1=0, S2=1, A=0 */*
/ FCS field: all 0x00 in simulation */*

ENDFIELD(GRRIND_ACK_NR1_SAPI9, 10)

FIELD(GRRIND_ACK_NR1_SAPI11)

0x30, 0x00,
0x00, 0x00,
0x0B,
0x80, 0x05,
0x00, 0x00, 0x00

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 9 */*
/ N(R)=1, S1=0, S2=1, A=0 */*
/ FCS field: all 0x00 in simulation */*

ENDFIELD(GRRIND_ACK_NR1_SAPI11, 10)

*/**

2.2.3.6 GRR-IND RNR commands/responses

**/*

FIELD(GRRIND_RNR_NR16_SAPI3)

0x30, 0x00,
0x00, 0x00,
0x03,
0x80, 0x42,
0x00, 0x00, 0x00

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3 */*
/ N(R)=16, S1=1, S2=0, A=0 */*
/ FCS field: all 0x00 in simulation */*

ENDFIELD(GRRIND_RNR_NR16_SAPI3, 10)

FIELD(GRRIND_RNR_NR8_SAPI5)

0x30, 0x00,
0x00, 0x00,
0x05,
0x80, 0x22,
0x00, 0x00, 0x00

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 5 */*
/ N(R)=8, S1=1, S2=0, A=0 */*
/ FCS field: all 0x00 in simulation */*

ENDFIELD(GRRIND_RNR_NR8_SAPI5, 10)

FIELD(GRRIND_RNR_NR4_SAPI9)

0x30, 0x00,
0x00, 0x00,
0x09,

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3 */*

```

        0x80, 0x12,
        0x00, 0x00, 0x00
ENDFIELD(GRRIND_RNR_NR4_SAPI9, 10)

FIELD(GRRIND_RNR_NR2_SAPI11)
    0x30, 0x00,
    0x00, 0x00,
    0x0B,
    0x80, 0x0A,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_RNR_NR2_SAPI11, 10)

FIELD(GRRIND_RNR_NR0_SAPI3)
    0x30, 0x00,
    0x00, 0x00,
    0x03,
    0x80, 0x02,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_RNR_NR0_SAPI3, 10)

/*

2.2.3.7 GRR-IND SACK commands/responses
*/
FIELD(GRRIND_SACK_NR18_R2_SAPI3)
    0x38, 0x00,
    0x00, 0x00,
    0x03,
    0x80, 0x4B,
    0x40,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_SACK_NR18_R2_SAPI3, 11)

FIELD(GRRIND_SACK_NR2_R1_SAPI3)
    0x38, 0x00,
    0x00, 0x00,
    0x03,
    0x80, 0x0B,
    0x40,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_SACK_NR2_R1_SAPI3, 11)

FIELD(GRRIND_SACK_R8_SAPI3)
    0x40, 0x00,
    0x00, 0x00,
    0x03,
    0x80, 0x03,
    0xFE,
    0xFF,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_SACK_R8_SAPI3, 12)

FIELD(GRRIND_SACK_R4_R8_SAPI3)
    0x40, 0x00,
    0x00, 0x00,
    0x03,
    0x80, 0x03,
    0xEE,

```

```

/*N(R)=4, S1=1, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/*N(R)=2, S1=1, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/*N(R)=0, S1=1, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 56 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* N(R)=2, S1=1, S2=1, A=0 */
/* R(1)=0, R(2)=1, other 0 */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 56 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* N(R)=2, S1=1, S2=1, A=0 */
/* R(1)=0, R(2)=1, other 0 */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 64 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* N(R)=0, S1=1, S2=1, A=0 */
/* R(8)=0 */
/* rest of the bits are all 1 */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 64 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* N(R)=0, S1=1, S2=1, A=0 */
/* R(4)=0, R(8)=0 */

```

```

        0xFE,
        0x00, 0x00, 0x00
ENDFIELD(GRRIND_SACK_R4_R8_SAPI3, 12)

FIELD(GRRIND_SACK_R6_SAPI5)
    0x38, 0x00,
    0x00, 0x00,
    0x05,
    0x80, 0x03,
    0xFB,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_SACK_R6_SAPI5, 11)

FIELD(GRRIND_SACK_R2_R6_SAPI5)
    0x38, 0x00,
    0x00, 0x00,
    0x05,
    0x80, 0x03,
    0xBA,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_SACK_R2_R6_SAPI5, 11)

FIELD(GRRIND_SACK_R2_SAPI9)
    0x38, 0x00,
    0x00, 0x00,
    0x09,
    0x80, 0x03,
    0xB0,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_SACK_R2_SAPI9, 11)

FIELD(GRRIND_SACK_R1_R2_SAPI9)
    0x38, 0x00,
    0x00, 0x00,
    0x09,
    0x80, 0x03,
    0x20,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_SACK_R1_R2_SAPI9, 11)

FIELD(GRRIND_SACK_R1_SAPI11)
    0x38, 0x00,
    0x00, 0x00,
    0x0B,
    0x80, 0x03,
    0x40,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_SACK_R1_SAPI11, 11)

```

```

/* rest of the bits except R(16) are all 1 */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 56 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 5 */
/* N(R)=0, S1=1, S2=1, A=0 */
/* R(6)=0, rest is all 1s */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 56 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 5 */
/* N(R)=0, S1=1, S2=1, A=0 */
/* R(2)=0, R(6)=0, rest is all 1s except R(8) */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 56 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
/* N(R)=0, S1=1, S2=1, A=0 */
/* R(2)=0, rest is all 1s */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 56 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
/* N(R)=0, S1=1, S2=1, A=0 */
/* R(1)=0, R(2)=0, R(3)=1, other 0 */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 56 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 11 */
/* N(R)=0, S1=1, S2=1, A=0 */
/* R(2)=0, rest all 1s */
/* FCS field: all 0x00 in simulation */

```

```

/*

```

2.2.3.8 GRR-IND RR commands/responses

```

*/

```

```

FIELD(GRRIND_RR_NR2_A0_SAPI3)
    0x30, 0x00,
    0x00, 0x00,
    0x03,
    0x80, 0x08,

```

```

/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* N(R)=2, S1=0, S2=0, A=0 */

```

```

        0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR2_A0_SAPI3, 10)
/* FCS field: all 0x00 in simulation */

FIELD(GRRIND_RR_NR5_A0_SAPI3)
    0x30, 0x00,
    0x00, 0x00,
    0x03,
    0x80, 0x14,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR5_A0_SAPI3, 10)
/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* N(R)=5, S1=0, S2=0, A=0 */
/* FCS field: all 0x00 in simulation */

FIELD(GRRIND_RR_NR16_SAPI3)
    0x30, 0x00,
    0x00, 0x00,
    0x03,
    0x80, 0x40,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR16_SAPI3, 10)
/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* N(R)=16, S1=0, S2=0, A=0 */
/* FCS field: all 0x00 in simulation */

FIELD(GRRIND_RR_NR16_A1_SAPI3)
    0x30, 0x00,
    0x00, 0x00,
    0x03,
    0xA0, 0x40,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR16_A1_SAPI3, 10)
/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* N(R)=16, S1=0, S2=0, A=1 */
/* FCS field: all 0x00 in simulation */

FIELD(GRRIND_RR_NR2_A0_SAPI5)
    0x30, 0x00,
    0x00, 0x00,
    0x05,
    0x80, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR2_A0_SAPI5, 10)
/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 5 */
/* N(R)=2, S1=0, S2=0, A=0 */
/* FCS field: all 0x00 in simulation */

FIELD(GRRIND_RR_NR8_SAPI5)
    0x30, 0x00,
    0x00, 0x00,
    0x05,
    0x80, 0x20,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR8_SAPI5, 10)
/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 5 */
/* N(R)=8, S1=0, S2=0, A=0 */
/* FCS field: all 0x00 in simulation */

FIELD(GRRIND_RR_NR8_A1_SAPI5)
    0x30, 0x00,
    0x00, 0x00,
    0x05,
    0xA0, 0x20,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR8_A1_SAPI5, 10)
/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 5 */
/* N(R)=8, S1=0, S2=0, A=0 */
/* FCS field: all 0x00 in simulation */

FIELD(GRRIND_RR_NR1_A0_SAPI9)
    0x30, 0x00,
    0x00, 0x00,
    0x09,
    0x80, 0x04,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR1_A0_SAPI9, 10)
/* sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
/* N(R)=1, S1=0, S2=0, A=0 */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(GRRIND_RR_NR2_A0_SAPI9)
    0x30, 0x00, /* sdu.l_buf = 48 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0x80, 0x08, /* N(R)=2, S1=0, S2=0, A=0 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR2_A0_SAPI9, 10)

```

```

FIELD(GRRIND_RR_NR3_A0_SAPI9)
    0x30, 0x00, /* sdu.l_buf = 48 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0x80, 0x0C, /* N(R)=3, S1=0, S2=0, A=0 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR3_A0_SAPI9, 10)

```

```

FIELD(GRRIND_RR_NR4_SAPI9)
    0x30, 0x00, /* sdu.l_buf = 48 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0x80, 0x10, /* N(R)=4, S1=0, S2=0, A=0 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR4_SAPI9, 10)

```

```

FIELD(GRRIND_RR_NR4_A1_SAPI9)
    0x30, 0x00, /* sdu.l_buf = 48 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0xA0, 0x10, /* N(R)=4, S1=0, S2=0, A=1 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR4_A1_SAPI9, 10)

```

```

FIELD(GRRIND_RR_NR2_SAPI11)
    0x30, 0x00, /* sdu.l_buf = 48 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
    0x80, 0x08, /* N(R)=2, S1=0, S2=0, A=0 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR2_SAPI11, 10)

```

```

FIELD(GRRIND_RR_NR2_A1_SAPI11)
    0x30, 0x00, /* sdu.l_buf = 48 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
    0xA0, 0x08, /* N(R)=2, S1=0, S2=0, A=1 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR2_A1_SAPI11, 10)

```

```

FIELD(GRRIND_RR_NR0_A1_SAPI11)
    0x30, 0x00, /* sdu.l_buf = 48 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
    0xA0, 0x00, /* N(R)=0, S1=0, S2=0, A=1 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR0_A1_SAPI11, 10)

```

```

FIELD(GRRIND_RR_NR0_A0_SAPI3)

```

```

        0x30, 0x00,
        0x00, 0x00,
        0x03,
        0x80, 0x00,
        0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR0_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR1_A0_SAPI3)
        0x30, 0x00,
        0x00, 0x00,
        0x03,
        0x80, 0x04,
        0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR1_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR1_A1_SAPI3)
        0x30, 0x00,
        0x00, 0x00,
        0x03,
        0xA0, 0x04,
        0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR1_A1_SAPI3, 10)

FIELD(GRRIND_RR_NR3_A0_SAPI3)
        0x30, 0x00,
        0x00, 0x00,
        0x03,
        0x80, 0x0C,
        0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR3_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR4_A0_SAPI3)
        0x30, 0x00,
        0x00, 0x00,
        0x03,
        0x80, 0x10,
        0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR4_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR6_A0_SAPI3)
        0x30, 0x00,
        0x00, 0x00,
        0x03,
        0x80, 0x18,
        0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR6_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR7_A0_SAPI3)
        0x30, 0x00,
        0x00, 0x00,
        0x03,
        0x80, 0x1C,
        0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR7_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR8_A0_SAPI3)
        0x30, 0x00,
        0x00, 0x00,

```

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3*/*
/ N(R)=0, S1=0, S2=0, A=0*/*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3*/*
/ N(R)=1, S1=0, S2=0, A=0*/*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3*/*
/ N(R)=1, S1=0, S2=0, A=0*/*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3*/*
/ N(R)=2, S1=0, S2=0, A=0*/*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3*/*
/ N(R)=2, S1=0, S2=0, A=0*/*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3*/*
/ N(R)=2, S1=0, S2=0, A=0*/*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */*
/ sdu.o_buf = 0 */*

```

0x03,
0x80, 0x20,
0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR8_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR9_A0_SAPI3)
0x30, 0x00,
0x00, 0x00,
0x03,
0x80, 0x24,
0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR9_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR10_A0_SAPI3)
0x30, 0x00,
0x00, 0x00,
0x03,
0x80, 0x28,
0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR10_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR11_A0_SAPI3)
0x30, 0x00,
0x00, 0x00,
0x03,
0x80, 0x2C,
0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR11_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR12_A0_SAPI3)
0x30, 0x00,
0x00, 0x00,
0x03,
0x80, 0x30,
0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR12_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR13_A0_SAPI3)
0x30, 0x00,
0x00, 0x00,
0x03,
0x80, 0x34,
0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR13_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR14_A0_SAPI3)
0x30, 0x00,
0x00, 0x00,
0x03,
0x80, 0x38,
0x00, 0x00, 0x00
ENDFIELD(GRRIND_RR_NR14_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR15_A0_SAPI3)
0x30, 0x00,
0x00, 0x00,
0x03,
0x80, 0x3C,

```

/ Address field: PD = 0, C/R = 0, SAPI = 3*/
/* N(R)=2, S1=0, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3*/
/* N(R)=2, S1=0, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3*/
/* N(R)=2, S1=0, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3*/
/* N(R)=2, S1=0, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3*/
/* N(R)=2, S1=0, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3*/
/* N(R)=2, S1=0, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3*/
/* N(R)=2, S1=0, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 48 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3*/
/* N(R)=2, S1=0, S2=0, A=0*/
/* FCS field: all 0x00 in simulation */*

```

        0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR15_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR16_A0_SAPI3)
    0x30, 0x00,                                        /* sdu.l_buf = 48 */
    0x00, 0x00,                                        /* sdu.o_buf = 0 */
    0x03,                                              /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0x80, 0x40,                                        /* N(R)=2, S1=0, S2=0, A=0 */
    0x00, 0x00, 0x00                                  /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR16_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR17_A0_SAPI3)
    0x30, 0x00,                                        /* sdu.l_buf = 48 */
    0x00, 0x00,                                        /* sdu.o_buf = 0 */
    0x03,                                              /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0x80, 0x44,                                        /* N(R)=2, S1=0, S2=0, A=0 */
    0x00, 0x00, 0x00                                  /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR17_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR18_A0_SAPI3)
    0x30, 0x00,                                        /* sdu.l_buf = 48 */
    0x00, 0x00,                                        /* sdu.o_buf = 0 */
    0x03,                                              /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0x80, 0x48,                                        /* N(R)=2, S1=0, S2=0, A=0 */
    0x00, 0x00, 0x00                                  /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR18_A0_SAPI3, 10)

FIELD(GRRIND_RR_NR21_A0_SAPI3)
    0x30, 0x00,                                        /* sdu.l_buf = 48 */
    0x00, 0x00,                                        /* sdu.o_buf = 0 */
    0x03,                                              /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0x80, 0x54,                                        /* N(R)=21, S1=0, S2=0, A=0 */
    0x00, 0x00, 0x00                                  /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_RR_NR21_A0_SAPI3, 10)

/*

2.2.3.9 GRR-IND Data Primitives, small
*/
/*

2.2.3.9.1 N(U) 0
*/
FIELD(GRRIND_DESCLIST100_SAPI1)
    0x50, 0x03,                                        /* sdu.l_buf = 848 */
    0x00, 0x00,                                        /* sdu.o_buf = 0 */
    0x01,                                              /* Address field: PD = 0, C/R = 0, SAPI = 1 */
    0xC0, 0x00,                                        /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00                                  /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_DESCLIST100_SAPI1, 110)

FIELD(GRRIND_DESCLIST100_SAPI3)

```

0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_DESCLIST100_SAPI3, 110)

FIELD(GRRIND_DESCLIST100_SAPI5)

```

0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x05, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_DESCLIST100_SAPI5, 110)

FIELD(GRRIND_DESCLIST100_SAPI7)

```

0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x07, /* Address field: PD = 0, C/R = 1, SAPI = 7 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(GRRIND_DESCLIST100_SAPI7, 110)

FIELD(GRRIND_DESCLIST100_SAPI9)

```

0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,

```

```

    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_DESCLIST100_SAPI9, 110)

FIELD(GRRIND_DESCLIST100_SAPI11)
    0x50, 0x03,
    0x00, 0x00,
    0x0B,
    0xC0, 0x00,
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_DESCLIST100_SAPI11, 110)
*/

```

2.2.3.9.2 N(U) 1

```

*/
FIELD(GRRIND_DESCLIST100_NU1_SAPI1)
    0x50, 0x03,
    0x00, 0x00,
    0x01,
    0xC0, 0x04,
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_DESCLIST100_NU1_SAPI1, 110)

FIELD(GRRIND_DESCLIST100_NU1_SAPI3)
    0x50, 0x03,
    0x00, 0x00,
    0x03,
    0xC0, 0x04,
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00
ENDFIELD(GRRIND_DESCLIST100_NU1_SAPI3, 110)

FIELD(GRRIND_DESCLIST100_NU1_SAPI5)
    0x50, 0x03,
    0x00, 0x00,
    0x05,
    0xC0, 0x04,
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,

```

```

0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU1_SAPI5, 110)

FIELD(GRRIND_DESCLIST100_NU1_SAPI7)
0x50, 0x03,                                       /* sdu.l_buf = 848 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x07,                                             /* Address field: PD = 0, C/R = 1, SAPI = 7 */
0xC0, 0x04,                                       /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU1_SAPI7, 110)

FIELD(GRRIND_DESCLIST100_NU1_SAPI9)
0x50, 0x03,                                       /* sdu.l_buf = 848 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x09,                                             /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xC0, 0x04,                                       /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU1_SAPI9, 110)

FIELD(GRRIND_DESCLIST100_NU1_SAPI11)
0x50, 0x03,                                       /* sdu.l_buf = 848 */
0x00, 0x00,                                       /* sdu.o_buf = 0 */
0x0B,                                             /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xC0, 0x04,                                       /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU1_SAPI11, 110)
/*

```

2.2.3.9.3 N(U) 2

```
*/
```

```
FIELD(GRRIND_DESCLIST100_NU2_SAPI3)
```

```

0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xC0, 0x08, /* Control field (UI frame): N(U) = 2, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU2_SAPI3, 110)

```

```

FIELD(GRRIND_DESCLIST100_NU2_SAPI5)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x05, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
0xC0, 0x08, /* Control field (UI frame): N(U) = 2, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU2_SAPI5, 110)

```

```

FIELD(GRRIND_DESCLIST100_NU2_SAPI7)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x07, /* Address field: PD = 0, C/R = 1, SAPI = 7 */
0xC0, 0x08, /* Control field (UI frame): N(U) = 2, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU2_SAPI7, 110)

```

```

FIELD(GRRIND_DESCLIST100_NU2_SAPI9)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xC0, 0x08, /* Control field (UI frame): N(U) = 2, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU2_SAPI9, 110)

```

```

FIELD(GRRIND_DESCLIST100_NU2_SAPI11)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
    0xC0, 0x08, /* Control field (UI frame): N(U) = 2, E = 0, PM = 0 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU2_SAPI11, 110)
*/

```

2.2.3.9.4 N(U) 3

```

*/
FIELD(GRRIND_DESCLIST100_NU3_SAPI3)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
    0xC0, 0x0C, /* Control field (UI frame): N(U) = 3, E = 0, PM = 0 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU3_SAPI3, 110)

```

```

FIELD(GRRIND_DESCLIST100_NU3_SAPI5)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x05, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
    0xC0, 0x0C, /* Control field (UI frame): N(U) = 3, E = 0, PM = 0 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
    0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
    0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
    0x61, 0x62, 0x63, 0x64,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU3_SAPI5, 110)

```

```

FIELD(GRRIND_DESCLIST100_NU3_SAPI7)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x07, /* Address field: PD = 0, C/R = 1, SAPI = 7 */
    0xC0, 0x0C, /* Control field (UI frame): N(U) = 3, E = 0, PM = 0 */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
    0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
    0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
    0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,

```

```

0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU3_SAPI7, 110)

FIELD(GRRIND_DESCLIST100_NU3_SAPI9)
0x50, 0x03,                                     /* sdu.l_buf = 848 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x09,                                           /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xC0, 0x0C,                                     /* Control field (UI frame): N(U) = 3, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU3_SAPI9, 110)

FIELD(GRRIND_DESCLIST100_NU3_SAPI11)
0x50, 0x03,                                     /* sdu.l_buf = 848 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x0B,                                           /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xC0, 0x0C,                                     /* Control field (UI frame): N(U) = 3, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU3_SAPI11, 110)
/*

```

2.2.3.9.5 N(U) 4

```

*/
FIELD(GRRIND_DESCLIST100_NU4_SAPI3)
0x50, 0x03,                                     /* sdu.l_buf = 848 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x03,                                           /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xC0, 0x10,                                     /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00                                /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU4_SAPI3, 110)

FIELD(GRRIND_DESCLIST100_NU4_SAPI5)
0x50, 0x03,                                     /* sdu.l_buf = 848 */
0x00, 0x00,                                     /* sdu.o_buf = 0 */
0x05,                                           /* Address field: PD = 0, C/R = 1, SAPI = 5 */

```

```

0xC0, 0x10, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU4_SAPI5, 110)

FIELD(GRRIND_DESCLIST100_NU4_SAPI7)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x07, /* Address field: PD = 0, C/R = 1, SAPI = 7 */
0xC0, 0x10, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU4_SAPI7, 110)

FIELD(GRRIND_DESCLIST100_NU4_SAPI9)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xC0, 0x10, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU4_SAPI9, 110)

FIELD(GRRIND_DESCLIST100_NU4_SAPI11)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x0B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xC0, 0x10, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100_NU4_SAPI11, 110)
/*

```

2.2.3.9.6 Reversed content, N(U) 1

*/

```

FIELD(GRRIND_DESCLIST100A_NU1_SAPI1)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x01, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
    0xC0, 0x04, /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
    0x64, 0x63, 0x62, 0x61,
    0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
    0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
    0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
    0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
    0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
    0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100A_NU1_SAPI1, 110)

```

```

FIELD(GRRIND_DESCLIST100A_NU1_SAPI3)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
    0xC0, 0x04, /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
    0x64, 0x63, 0x62, 0x61,
    0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
    0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
    0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
    0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
    0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
    0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100A_NU1_SAPI3, 110)

```

```

FIELD(GRRIND_DESCLIST100A_NU1_SAPI5)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x05, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
    0xC0, 0x04, /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
    0x64, 0x63, 0x62, 0x61,
    0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
    0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
    0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
    0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
    0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
    0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01,
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100A_NU1_SAPI5, 110)

```

```

FIELD(GRRIND_DESCLIST100A_NU1_SAPI7)
    0x50, 0x03, /* sdu.l_buf = 848 */
    0x00, 0x00, /* sdu.o_buf = 0 */
    0x07, /* Address field: PD = 0, C/R = 1, SAPI = 7 */
    0xC0, 0x04, /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
    0x64, 0x63, 0x62, 0x61,
    0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
    0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
    0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
    0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
    0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,

```

```

0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100A_NU1_SAPI7, 110)

FIELD(GRRIND_DESCLIST100A_NU1_SAPI9)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xC0, 0x04, /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
0x64, 0x63, 0x62, 0x61,
0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100A_NU1_SAPI9, 110)

FIELD(GRRIND_DESCLIST100A_NU1_SAPI11)
0x50, 0x03, /* sdu.l_buf = 848 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x0B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xC0, 0x04, /* Control field (UI frame): N(U) = 1, E = 0, PM = 0 */
0x64, 0x63, 0x62, 0x61,
0x60, 0x5F, 0x5E, 0x5D, 0x5C, 0x5B, 0x5A, 0x59, 0x58, 0x57, 0x56, 0x55, 0x54, 0x53, 0x52, 0x51,
0x50, 0x4F, 0x4E, 0x4D, 0x4C, 0x4B, 0x4A, 0x49, 0x48, 0x47, 0x46, 0x45, 0x44, 0x43, 0x42, 0x41,
0x40, 0x3F, 0x3E, 0x3D, 0x3C, 0x3B, 0x3A, 0x39, 0x38, 0x37, 0x36, 0x35, 0x34, 0x33, 0x32, 0x31,
0x30, 0x2F, 0x2E, 0x2D, 0x2C, 0x2B, 0x2A, 0x29, 0x28, 0x27, 0x26, 0x25, 0x24, 0x23, 0x22, 0x21,
0x20, 0x1F, 0x1E, 0x1D, 0x1C, 0x1B, 0x1A, 0x19, 0x18, 0x17, 0x16, 0x15, 0x14, 0x13, 0x12, 0x11,
0x10, 0x0F, 0x0E, 0x0D, 0x0C, 0x0B, 0x0A, 0x09, 0x08, 0x07, 0x06, 0x05, 0x04, 0x03, 0x02, 0x01,
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(GRRIND_DESCLIST100A_NU1_SAPI11, 110)
*/

```

2.2.3.10 GRR-IND Data Primitives, large

```

*/
FIELD(GRRIND_DESCLIST1000_SAPI1)
0x70, 0x1F, /* sdu.l_buf = 8048 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x01, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,

```



```

0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00
/* FCS field: all 0x00 in simulation */

```

```
ENDFIELD(GRRIND_DESCLIST1000_SAPI5, 1010)
```

```
FIELD(GRRIND_DESCLIST1000_SAPI7)
```

```

0x70, 0x1F,
0x00, 0x00,
0x07,
0xC0, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,

```


FIELD(GRRIND_DESCLIST1000_SAPI9)

```

0x70, 0x1F, /* sdu.l_buf = 8048 */
0x00, 0x00, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xC0, 0x00, /* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,

```

```

0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRRIND_DESCLIST1000_SAPI9, 1010)
```

```
FIELD(GRRIND_DESCLIST1000_SAPI11)
```

```

0x70, 0x1F,
0x00, 0x00,
0x0B,
0xC0, 0x00,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,

```

```
/* sdu.l_buf = 8048 */
```

```
/* sdu.o_buf = 0 */
```

```
/* Address field: PD = 0, C/R = 1, SAPI = 11 */
```

```
/* Control field (UI frame): N(U) = 0, E = 0, PM = 0 */
```

```

0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, 0x08, 0x09, 0x0A, 0x0B, 0x0C, 0x0D, 0x0E, 0x0F, 0x10,
0x11, 0x12, 0x13, 0x14, 0x15, 0x16, 0x17, 0x18, 0x19, 0x1A, 0x1B, 0x1C, 0x1D, 0x1E, 0x1F, 0x20,
0x21, 0x22, 0x23, 0x24, 0x25, 0x26, 0x27, 0x28, 0x29, 0x2A, 0x2B, 0x2C, 0x2D, 0x2E, 0x2F, 0x30,
0x31, 0x32, 0x33, 0x34, 0x35, 0x36, 0x37, 0x38, 0x39, 0x3A, 0x3B, 0x3C, 0x3D, 0x3E, 0x3F, 0x40,
0x41, 0x42, 0x43, 0x44, 0x45, 0x46, 0x47, 0x48, 0x49, 0x4A, 0x4B, 0x4C, 0x4D, 0x4E, 0x4F, 0x50,
0x51, 0x52, 0x53, 0x54, 0x55, 0x56, 0x57, 0x58, 0x59, 0x5A, 0x5B, 0x5C, 0x5D, 0x5E, 0x5F, 0x60,
0x61, 0x62, 0x63, 0x64,
0x00, 0x00, 0x00

```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(GRRIND_DESCLIST1000_SAPI11, 1010)
```

```
/*
```

2.2.3.11 MS Signalling Primitives (U frames)

```
*/
```

```
/*
```

2.2.3.11.1 SABM command

```
*/
```

```
FIELD(MS_SABM_NOXID_SAPI3)
```

```
40, 0,
```

```
0, 0,
```

```
0x03,
```

```
0xF7,
```

```
0x00, 0x00, 0x00
```

```
/* sdu.l_buf = 5*8 */
```

```
/* sdu.o_buf = 0 */
```

```
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
```

```
/* Control field (U frame): P = 1, Mx = 0111 (SABM) */
```

```
/* FCS field: all 0x00 in simulation */
```

```
ENDFIELD(MS_SABM_NOXID_SAPI3, 9)
```

```

FIELD(MS_SABM_NOXID_SAPI5)
    40, 0, /* sdu.l_buf = 5*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x05, /* Address field: PD = 0, C/R = 0, SAPI = 5 */
    0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_NOXID_SAPI5, 9)

FIELD(MS_SABM_NOXID_SAPI9)
    40, 0, /* sdu.l_buf = 5*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_NOXID_SAPI9, 9)

FIELD(MS_SABM_NOXID_SAPI11)
    40, 0, /* sdu.l_buf = 5*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
    0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_NOXID_SAPI11, 9)

FIELD(MS_SABM_EMPTY_L3_XID_SAPI3)
    56, 0, /* sdu.l_buf = 7*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_EMPTY_L3_XID_SAPI3, 11)

FIELD(MS_SABM_N201I_520_SAPI3)
    80, 0, /* sdu.l_buf = 10*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
    0x1A, 0x02, 0x08, /* N201-I = 520 */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_N201I_520_SAPI3, 14)

FIELD(MS_SABM_N201I_520_T200_200_SAPI3)
    104, 0, /* sdu.l_buf = 13*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
    0x0E, 0x00, 0xC8, /* T200 = 200 */
    0x1A, 0x02, 0x08, /* N201-I = 520 */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_N201I_520_T200_200_SAPI3, 17)

FIELD(MS_SABM_N201I_520_MD0_MU0_SAPI3)
    128, 0, /* sdu.l_buf = 16*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */

```

```

0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x1A, 0x02, 0x08, /* N201-l = 520 */
0x1E, 0x00, 0x00, /* mD = 0 */
0x22, 0x00, 0x00, /* mU = 0 */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_N201I_520_MD0_MU0_SAPI3, 20)

FIELD(MS_SABM_N201I_520_SAPI5)
80, 0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x05, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x1A, 0x02, 0x08, /* N201-l = 520 */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_N201I_520_SAPI5, 14)

FIELD(MS_SABM_N201I_520_SAPI11)
80, 0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x1A, 0x02, 0x08, /* N201-l = 520 */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_N201I_520_SAPI11, 14)

FIELD(MS_SABM_NOL3_N201I_520_SAPI3)
64, 0, /* sdu.l_buf = 8*8 */
0, 0, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x1A, 0x02, 0x08, /* N201-l = 520 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_NOL3_N201I_520_SAPI3, 12)

FIELD(MS_SABM_NOL3_N201I_520_SAPI9)
64, 0, /* sdu.l_buf = 8*8 */
0, 0, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x1A, 0x02, 0x08, /* N201-l = 520 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_NOL3_N201I_520_SAPI9, 12)

FIELD(MS_SABM_N201I_520_SAPI9)
80, 0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x1A, 0x02, 0x08, /* N201-l = 520 */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_N201I_520_SAPI9, 14)

FIELD(MS_SABM_EMPTY_L3_XID_NU400_SAPI3)

```

```

80, 0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x16, 0x01, 0x90, /* N201-U = 400 */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_EMPTY_L3_XID_NU400_SAPI3, 14)

```

```

FIELD(MS_SABM_EMPTY_L3_XID_SAPI5)
56, 0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */
0x05, /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_EMPTY_L3_XID_SAPI5, 11)

```

```

FIELD(MS_SABM_EMPTY_L3_XID_SAPI9)
56, 0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_EMPTY_L3_XID_SAPI9, 11)

```

```

FIELD(MS_SABM_EMPTY_L3_XID_SAPI11)
56, 0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */
0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_SABM_EMPTY_L3_XID_SAPI11, 11)

```

/*

2.2.3.11.2 DISC command

```

*/
FIELD(MS_DISC_SAPI3)
40, 0, /* sdu.l_buf = 5*8 */
0, 0, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xF4, /* Control field (U frame): P = 1, Mx = 0100 (DISC) */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_DISC_SAPI3, 9)
/*

```

2.2.3.11.3 UA response

```

*/
FIELD(MS_UA1_NOL3_N201I_520_SAPI9)
64, 0, /* sdu.l_buf = 8*8 */
0, 0, /* sdu.o_buf = 0 */
0x49, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
0x1A, 0x02, 0x08, /* N201-I = 520 */

```

```

        0x00, 0x00, 0x00
ENDFIELD(MS_UA1_NOL3_N2011_520_SAPI9, 12)
/* FCS field: all 0x00 in simulation */

FIELD(MS_UA1_NOXID_SAPI3)
    40, 0,
    0, 0,
    0x43,
    0xF6,
    0x00, 0x00, 0x00
ENDFIELD(MS_UA1_NOXID_SAPI3, 9)
/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */
/* Control field (U frame): F = 1, Mx = 0110 (UA) */
/* FCS field: all 0x00 in simulation */

FIELD(MS_UA1_NOXID_SAPI5)
    40, 0,
    0, 0,
    0x45,
    0xF6,
    0x00, 0x00, 0x00
ENDFIELD(MS_UA1_NOXID_SAPI5, 9)
/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 5 */
/* Control field (U frame): F = 1, Mx = 0110 (UA) */
/* FCS field: all 0x00 in simulation */

FIELD(MS_UA1_NOXID_SAPI9)
    40, 0,
    0, 0,
    0x49,
    0xF6,
    0x00, 0x00, 0x00
ENDFIELD(MS_UA1_NOXID_SAPI9, 9)
/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 9 */
/* Control field (U frame): F = 1, Mx = 0110 (UA) */
/* FCS field: all 0x00 in simulation */

FIELD(MS_UA1_NOXID_SAPI11)
    40, 0,
    0, 0,
    0x4B,
    0xF6,
    0x00, 0x00, 0x00
ENDFIELD(MS_UA1_NOXID_SAPI11, 9)
/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 11 */
/* Control field (U frame): F = 1, Mx = 0110 (UA) */
/* FCS field: all 0x00 in simulation */

FIELD(MS_UA1_EMPTY_L3_XID_SAPI3)
    56, 0,
    0, 0,
    0x43,
    0xF6,
    0xAC, 0x00,
    0x00, 0x00, 0x00
ENDFIELD(MS_UA1_EMPTY_L3_XID_SAPI3, 11)
/* sdu.l_buf = 7*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */
/* Control field (U frame): F = 1, Mx = 0110 (UA) */
/* Empty L3-Para, 2 byte header */
/* FCS field: all 0x00 in simulation */

FIELD(MS_UA1_EMPTY_L3_XID_SAPI5)
    56, 0,
    0, 0,
    0x45,
    0xF6,
    0xAC, 0x00,
    0x00, 0x00, 0x00
ENDFIELD(MS_UA1_EMPTY_L3_XID_SAPI5, 11)
/* sdu.l_buf = 7*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 5 */
/* Control field (U frame): F = 1, Mx = 0110 (UA) */
/* Empty L3-Para, 2 byte header */
/* FCS field: all 0x00 in simulation */

FIELD(MS_UA1_EMPTY_L3_XID_SAPI9)
    56, 0,
    0, 0,
    0x49,
    0xF6,
ENDFIELD(MS_UA1_EMPTY_L3_XID_SAPI9, 11)
/* sdu.l_buf = 7*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 9 */
/* Control field (U frame): F = 1, Mx = 0110 (UA) */

```

```

0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_UA1_EMPTY_L3_XID_SAPI9, 11)

FIELD(MS_UA1_EMPTY_L3_XID_SAPI11)
56, 0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */
0x4B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_UA1_EMPTY_L3_XID_SAPI11, 11)
FIELD(MS_UA1_N201U_400_SAPI3)
80, 0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
0x16, 0x01, 0x90, /* N201-U = 400 */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_UA1_N201U_400_SAPI3, 14)

FIELD(MS_UA1_N201U_300_SAPI5)
80, 0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x45, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
0x16, 0x01, 0x2C, /* N201-U = 300 */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_UA1_N201U_300_SAPI5, 14)

FIELD(MS_UA1_N201U_600_SAPI5)
80, 0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x45, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
0x16, 0x02, 0x58, /* N201-U = 600 */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_UA1_N201U_600_SAPI5, 14)

FIELD(MS_UA1_N201U_300_SAPI9)
80, 0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x49, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
0x16, 0x01, 0x2C, /* N201-U = 300 */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_UA1_N201U_300_SAPI9, 14)

FIELD(MS_UA1_N201U_400_SAPI11)
80, 0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x4B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */

```

```

        0x16, 0x01, 0x90,
        0xAC, 0x00,
        0x00, 0x00, 0x00
ENDFIELD(MS_UA1_N201U_400_SAPI11, 14)
/*

```

2.2.3.11.4 DM response

```

/*
FIELD(MS_DM1_SAPI1)
    40, 0,
    0, 0,
    0x41,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(MS_DM1_SAPI1, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 1 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(MS_DM1_SAPI3)
    40, 0,
    0, 0,
    0x43,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(MS_DM1_SAPI3, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(MS_DM1_SAPI5)
    40, 0,
    0, 0,
    0x45,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(MS_DM1_SAPI5, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 5 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(MS_DM1_SAPI7)
    40, 0,
    0, 0,
    0x47,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(MS_DM1_SAPI7, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 7 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(MS_DM1_SAPI9)
    40, 0,
    0, 0,
    0x49,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(MS_DM1_SAPI9, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 9 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(MS_DM1_SAPI11)
    40, 0,
    0, 0,
    0x4B,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(MS_DM1_SAPI11, 9)
/*

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 11 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

2.2.3.11.5 FRMR response (undefined command control field)

*/

```

FIELD(MS_FRMR_UNDEFCTRL0_SAPI1)
    120, 0, /* sdu.l_buf = 15*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x41, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
    0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
    0xF0, 0, 0, 0, 0, 0, /* Information field: undefined control field */
    0x00, /* eight bits: X, X, X, X, V(S):4 */
    0x00, /* eight bits: V(S):5, X, V(R):2 */
    0x00, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.) */
    0x04, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_UNDEFCTRL0_SAPI1, 19)

```

```

FIELD(MS_FRMR_UNDEFCTRL0_SAPI3)
    120, 0, /* sdu.l_buf = 15*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
    0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
    0xF0, 0, 0, 0, 0, 0, /* Information field: undefined control field */
    0x00, /* eight bits: X, X, X, X, V(S):4 */
    0x00, /* eight bits: V(S):5, X, V(R):2 */
    0x00, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.) */
    0x04, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_UNDEFCTRL0_SAPI3, 19)

```

```

FIELD(MS_FRMR_UNDEFCTRL0_SAPI5)
    120, 0, /* sdu.l_buf = 15*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x45, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
    0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
    0xF0, 0, 0, 0, 0, 0, /* Information field: undefined control field */
    0x00, /* eight bits: X, X, X, X, V(S):4 */
    0x00, /* eight bits: V(S):5, X, V(R):2 */
    0x00, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.) */
    0x04, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_UNDEFCTRL0_SAPI5, 19)

```

```

FIELD(MS_FRMR_UNDEFCTRL0_SAPI7)
    120, 0, /* sdu.l_buf = 15*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x47, /* Address field: PD = 0, C/R = 1, SAPI = 7 */
    0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
    0xF0, 0, 0, 0, 0, 0, /* Information field: undefined control field */
    0x00, /* eight bits: X, X, X, X, V(S):4 */
    0x00, /* eight bits: V(S):5, X, V(R):2 */
    0x00, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.) */
    0x04, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_UNDEFCTRL0_SAPI7, 19)

```

```

FIELD(MS_FRMR_UNDEFCTRL0_SAPI9)
    120, 0, /* sdu.l_buf = 15*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x49, /* Address field: PD = 0, C/R = 1, SAPI = 9 */

```

```

0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
0xF0, 0, 0, 0, 0, 0, /* Information field: undefined control field */
0x00, /* eight bits: X, X, X, X, V(S):4 */
0x00, /* eight bits: V(S):5, X, V(R):2 */
0x00, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.)* */
0x04, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_UNDEFCTRL0_SAPI9, 19)

```

```

FIELD(MS_FRMR_UNDEFCTRL0_SAPI11)
120, 0, /* sdu.l_buf = 15*8 */
0, 0, /* sdu.o_buf = 0 */
0x4B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
0xF0, 0, 0, 0, 0, 0, /* Information field: undefined control field */
0x00, /* eight bits: X, X, X, X, V(S):4 */
0x00, /* eight bits: V(S):5, X, V(R):2 */
0x00, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.)* */
0x04, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_UNDEFCTRL0_SAPI11, 19)
/*

```

2.2.3.11.6 FRMR response (undefined command control field in ABM)

```

*/
FIELD(MS_FRMR_UNDEFCTRL0_REEST_SAPI3)
120, 0, /* sdu.l_buf = 15*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
0xF0, 0, 0, 0, 0, 0, /* Information field: undefined control field */
0x00, /* eight bits: X, X, X, X, V(S):4 */
0x00, /* eight bits: V(S):5, X, V(R):2 */
0x00, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.)* */
0x0C, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_UNDEFCTRL0_REEST_SAPI3, 19)

```

```

FIELD(MS_FRMR_UNDEFCTRL0_VS1_REEST_SAPI3)
120, 0, /* sdu.l_buf = 15*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
0xF0, 0, 0, 0, 0, 0, /* Information field: undefined control field */
0x00, /* eight bits: X, X, X, X, V(S):4 */
0x08, /* eight bits: V(S):5, X, V(R):2 -> VS=1 ->0x08*/
0x00, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.)* */
0x0C, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_UNDEFCTRL0_VS1_REEST_SAPI3, 19)
/*

```

2.2.3.11.7 FRMR response (DM response with information field)

```

*/
FIELD(MS_FRMR_DM1_SAPI3)
120, 0, /* sdu.l_buf = 15*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */

```

```

0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
0xF1, 0, 0, 0, 0, 0, /* Information field: DM response (F bit 1) */
0x00, /* eight bits: X, X, X, X, V(S):4 */
0x00, /* eight bits: V(S):5, X, V(R):2 */
0x01, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.)* */
0x05, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_DM1_SAPI3, 19)

```

```

FIELD(MS_FRMR_DM1_SAPI5)
120, 0, /* sdu.l_buf = 15*8 */
0, 0, /* sdu.o_buf = 0 */
0x45, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
0xF1, 0, 0, 0, 0, 0, /* Information field: DM response (F bit 1) */
0x00, /* eight bits: X, X, X, X, V(S):4 */
0x00, /* eight bits: V(S):5, X, V(R):2 */
0x01, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.)* */
0x05, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_DM1_SAPI5, 19)

```

```

FIELD(MS_FRMR_DM1_SAPI9)
120, 0, /* sdu.l_buf = 15*8 */
0, 0, /* sdu.o_buf = 0 */
0x49, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
0xF1, 0, 0, 0, 0, 0, /* Information field: DM response (F bit 1) */
0x00, /* eight bits: X, X, X, X, V(S):4 */
0x00, /* eight bits: V(S):5, X, V(R):2 */
0x01, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.)* */
0x05, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_DM1_SAPI9, 19)

```

```

FIELD(MS_FRMR_DM1_SAPI11)
120, 0, /* sdu.l_buf = 15*8 */
0, 0, /* sdu.o_buf = 0 */
0x4B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xF8, /* Control field (U frame): F = 1, Mx = 1000 (FRMR) */
0xF1, 0, 0, 0, 0, 0, /* Information field: DM response (F bit 1) */
0x00, /* eight bits: X, X, X, X, V(S):4 */
0x00, /* eight bits: V(S):5, X, V(R):2 */
0x01, /* eight bits: V(R):7, C/R (0 = comm., 1 = resp.)* */
0x05, /* eight bits: X, X, X, X, W4, W3, W2, W1 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(MS_FRMR_DM1_SAPI11, 19)

```

/*

2.2.3.12 SGSN Signalling Primitives (U frames)

/*

/*

2.2.3.12.1 SABM command

/*

```

FIELD(SGSN_SABM_NOXID_SAPI1)
40, 0, /* sdu.l_buf = 5*8 */

```

```

0, 0, /* sdu.o_buf = 0 */
0x41, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_NOXID_SAPI1, 9)

FIELD(SGSN_SABM_NOXID_SAPI3)
40, 0, /* sdu.l_buf = 5*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_NOXID_SAPI3, 9)

FIELD(SGSN_SABM_NOXID_SAPI5)
40, 0, /* sdu.l_buf = 5*8 */
0, 0, /* sdu.o_buf = 0 */
0x45, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_NOXID_SAPI5, 9)

FIELD(SGSN_SABM_NOXID_SAPI7)
40, 0, /* sdu.l_buf = 5*8 */
0, 0, /* sdu.o_buf = 0 */
0x47, /* Address field: PD = 0, C/R = 1, SAPI = 7 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_NOXID_SAPI7, 9)

FIELD(SGSN_SABM_NOXID_SAPI9)
40, 0, /* sdu.l_buf = 5*8 */
0, 0, /* sdu.o_buf = 0 */
0x49, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_NOXID_SAPI9, 9)

FIELD(SGSN_SABM_NOXID_SAPI11)
40, 0, /* sdu.l_buf = 5*8 */
0, 0, /* sdu.o_buf = 0 */
0x4B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_NOXID_SAPI11, 9)

FIELD(SGSN_SABM_EMPTY_L3_XID_SAPI3)
56, 0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_EMPTY_L3_XID_SAPI3, 11)

FIELD(SGSN_SABM_EMPTY_L3_XID_SAPI5)
56, 0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */

```

```

0x45, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_EMPTY_L3_XID_SAPI5, 11)

FIELD(SGSN_SABM_EMPTY_L3_XID_SAPI9)
56,0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */
0x49, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_EMPTY_L3_XID_SAPI9, 11)

FIELD(SGSN_SABM_EMPTY_L3_XID_SAPI11)
56, 0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */
0x4B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0xAC, 0x00, /* Empty L3-Para, 2 byte header */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_EMPTY_L3_XID_SAPI11, 11)
FIELD(SGSN_SABM_N201U_400_SAPI3)
72,0, /* sdu.l_buf = 9*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x16, 0x01, 0x90, /* N201-U = 400 */
0x2C, /* Empty L3-Para */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_N201U_400_SAPI3, 13)

FIELD(SGSN_SABM_N201U_600_SAPI5)
72,0, /* sdu.l_buf = 9*8 */
0, 0, /* sdu.o_buf = 0 */
0x45, /* Address field: PD = 0, C/R = 1, SAPI = 5 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x16, 0x02, 0x58, /* N201-U = 600 */
0x2C, /* Empty L3-Para */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_N201U_600_SAPI5, 13)

FIELD(SGSN_SABM_N201U_300_SAPI9)
72,0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */
0x49, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */
0x16, 0x01, 0x2C, /* N201-U = 300 */
0x2C, /* Empty L3-Para */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_SABM_N201U_300_SAPI9, 13)

FIELD(SGSN_SABM_L30_N201I_520_XID_SAPI3)
80,0, /* sdu.l_buf = 10*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xF7, /* Control field (U frame): P = 1, Mx = 0111 (SABM) */

```

```

        0x1A, 0x02, 0x08,
        0xAC, 0x00,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_SABM_L30_N2011_520_XID_SAPI3, 14)

```

```

/* N201-l = 520 */
/* Empty L3-Para, 2 byte header */
/* FCS field: all 0x00 in simulation */

```

/*

2.2.3.12.2 DISC command

*/

```

FIELD(SGSN_DISC_SAPI3)
    40, 0,
    0, 0,
    0x43,
    0xF4,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DISC_SAPI3, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */
/* Control field (U frame): P = 1, Mx = 0100 (DISC) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_DISC_SAPI5)
    40, 0,
    0, 0,
    0x45,
    0xF4,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DISC_SAPI5, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 5 */
/* Control field (U frame): P = 1, Mx = 0100 (DISC) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_DISC_SAPI9)
    40, 0,
    0, 0,
    0x49,
    0xF4,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DISC_SAPI9, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 9 */
/* Control field (U frame): P = 1, Mx = 0100 (DISC) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_DISC_SAPI11)
    40, 0,
    0, 0,
    0x4B,
    0xF4,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DISC_SAPI11, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 11 */
/* Control field (U frame): P = 1, Mx = 0100 (DISC) */
/* FCS field: all 0x00 in simulation */

```

/*

2.2.3.12.3 Undefined command

*/

```

FIELD(SGSN_UNDEFCOMMCTRL0_SAPI1)
    40, 0,
    0, 0,
    0x41,
    0xF0,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UNDEFCOMMCTRL0_SAPI1, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 1 */
/* Control field (U frame): P = 1, Mx = 0000 (undef) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_UNDEFCOMMCTRL0_SAPI3)
    40, 0,
    0, 0,
    0x43,

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */

```

```

        0xF0,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_UNDEFCOMMCTRL0_SAPI3, 9)

FIELD(SGSN_UNDEFCOMMCTRL0_SAPI5)
    40, 0,
    0, 0,
    0x45,
    0xF0,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UNDEFCOMMCTRL0_SAPI5, 9)

FIELD(SGSN_UNDEFCOMMCTRL0_SAPI7)
    40, 0,
    0, 0,
    0x47,
    0xF0,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UNDEFCOMMCTRL0_SAPI7, 9)

FIELD(SGSN_UNDEFCOMMCTRL0_SAPI9)
    40, 0,
    0, 0,
    0x49,
    0xF0,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UNDEFCOMMCTRL0_SAPI9, 9)

FIELD(SGSN_UNDEFCOMMCTRL0_SAPI11)
    40, 0,
    0, 0,
    0x4B,
    0xF0,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UNDEFCOMMCTRL0_SAPI11, 9)
/*

```

2.2.3.12.4 UA response

```

*/
FIELD(SGSN_UA1_NOXID_SAPI3)
    40, 0,
    0, 0,
    0x03,
    0xF6,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_NOXID_SAPI3, 9)

FIELD(SGSN_UA1_NOXID_SAPI5)
    40, 0,
    0, 0,
    0x05,
    0xF6,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_NOXID_SAPI5, 9)

FIELD(SGSN_UA1_NOXID_SAPI9)
    40, 0,
    0, 0,

```

```

        0x09,
        0xF6,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_NOXID_SAPI9, 9)

FIELD(SGSN_UA1_NOXID_SAPI11)
        40, 0,
        0, 0,
        0x0B,
        0xF6,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_NOXID_SAPI11, 9)

FIELD(SGSN_UA0_NOXID_SAPI3)
        40, 0,
        0, 0,
        0x03,
        0xE6,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_UA0_NOXID_SAPI3, 9)

FIELD(SGSN_UA1_EMPTY_L3_XID_SAPI3)
        56, 0,
        0, 0,
        0x03,
        0xF6,
        0xAC, 0x00,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_EMPTY_L3_XID_SAPI3, 11)

FIELD(SGSN_UA_T200_200_XID_SAPI3)
        80, 0,
        0, 0,
        0x03,
        0xF6,
        0x0E, 0x00, 0xC8,
        0xAC, 0x00,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_UA_T200_200_XID_SAPI3, 14)

FIELD(SGSN_UA1_EMPTY_L3_XID_SAPI5)
        56, 0,
        0, 0,
        0x05,
        0xF6,
        0xAC, 0x00,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_EMPTY_L3_XID_SAPI5, 11)

FIELD(SGSN_UA1_EMPTY_L3_XID_SAPI9)
        56, 0,
        0, 0,
        0x09,
        0xF6,
        0xAC, 0x00,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_EMPTY_L3_XID_SAPI9, 11)

```

/ Address field: PD = 0, C/R = 0, SAPI = 9 */*
/ Control field (U frame): F = 1, Mx = 0110 (UA) */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 5*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 11 */*
/ Control field (U frame): F = 1, Mx = 0110 (UA) */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 5*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3 */*
/ Control field (U frame): F = 0, Mx = 0110 (UA) */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 7*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3 */*
/ Control field (U frame): F = 1, Mx = 0110 (UA) */*
/ Empty L3-Para, 2 byte header */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 10*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3 */*
/ Control field (U frame): F = 1, Mx = 0110 (UA) */*
/ T200 = 200 */*
/ Empty L3-Para, 2 byte header */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 7*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 5 */*
/ Control field (U frame): F = 1, Mx = 0110 (UA) */*
/ Empty L3-Para, 2 byte header */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 7*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 9 */*
/ Control field (U frame): F = 1, Mx = 0110 (UA) */*
/ Empty L3-Para, 2 byte header */*
/ FCS field: all 0x00 in simulation */*

```

FIELD(SGSN_UA1_EMPTY_L3_XID_SAPI11)
    56, 0, /* sdu.l_buf = 7*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
    0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_UA1_EMPTY_L3_XID_SAPI11, 11)

```

```

FIELD(SGSN_UA1_N201_I520_U400_SAPI9)
    104, 0, /* sdu.l_buf = 13*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
    0x16, 0x01, 0x90, /* N201-U = 400 */
    0x1A, 0x02, 0x08, /* N201-I = 520 */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_UA1_N201_I520_U400_SAPI9, 17)

```

```

FIELD(SGSN_UA1_N201_I520_KU1_SAPI9)
    96, 0, /* sdu.l_buf = 12*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
    0x1A, 0x02, 0x08, /* N201-I = 520 */
    0x29, 1, /* kU = 1 */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_UA1_N201_I520_KU1_SAPI9, 16)

```

```

FIELD(SGSN_UA1_N201_I520_SAPI3)
    80, 0, /* sdu.l_buf = 10*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
    0x1A, 0x02, 0x08, /* N201-I = 520 */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_UA1_N201_I520_SAPI3, 14)

```

```

FIELD(SGSN_UA1_N201_I520_MD0_MU0_KD4_KU4_SAPI3)
    160, 0, /* sdu.l_buf = 20*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0xF6, /* Control field (U frame): F = 1, Mx = 0110 (UA) */
    0x1A, 0x02, 0x08, /* N201-I = 520 */
    0x1E, 0x00, 0x00, /* mD = 0 */
    0x22, 0x00, 0x00, /* mU = 0 */
    0x25, 4, /* kD = 4 */
    0x29, 4, /* kU = 4 */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_UA1_N201_I520_MD0_MU0_KD4_KU4_SAPI3, 24)

```

```

FIELD(SGSN_UA1_NOL3_N201_I520_SAPI3)
    64, 0, /* sdu.l_buf = 8*8 */

```

```

    0, 0,
    0x03,
    0xF6,
    0x1A, 0x02, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_NOL3_N201_I520_SAPI3, 12)

```

```

/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
/* Control field (U frame): F = 1, Mx = 0110 (UA) */
/* N201-I = 520 */
/* FCS field: all 0x00 in simulation */

```

/*

2.2.3.12.5 DM response

*/

```

FIELD(SGSN_DM1_SAPI3)
    40, 0,
    0, 0,
    0x03,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DM1_SAPI3, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_DM1_SAPI5)
    40, 0,
    0, 0,
    0x05,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DM1_SAPI5, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 5 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_DM1_SAPI9)
    40, 0,
    0, 0,
    0x09,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DM1_SAPI9, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_DM1_SAPI11)
    40, 0,
    0, 0,
    0x0B,
    0xF1,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DM1_SAPI11, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 11 */
/* Control field (U frame): F = 1, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_DM0_SAPI3)
    40, 0,
    0, 0,
    0x03,
    0xE1,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DM0_SAPI3, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* Control field (U frame): F = 0, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_DM0_SAPI5)
    40, 0,
    0, 0,
    0x05,
    0xE1,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_DM0_SAPI5, 9)

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 5 */
/* Control field (U frame): F = 0, Mx = 0001 (DM) */
/* FCS field: all 0x00 in simulation */

```

```

FIELD(SGSN_DM0_SAPI9)
    40, 0, /* sdu.l_buf = 5*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0xE1, /* Control field (U frame): F = 0, Mx = 0001 (DM) */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_DM0_SAPI9, 9)

FIELD(SGSN_DM0_SAPI11)
    40, 0, /* sdu.l_buf = 5*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = B */
    0xE1, /* Control field (U frame): F = 0, Mx = 0001 (DM) */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_DM0_SAPI11, 9)

FIELD(SGSN_DM1_WITHINFO_SAPI3)
    96, 0, /* sdu.l_buf = 12*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0xF1, /* Control field (U frame): F = 1, Mx = 0001 (DM) */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, /* Information field: not permitted! */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_DM1_WITHINFO_SAPI3, 16)

FIELD(SGSN_DM1_WITHINFO_SAPI5)
    96, 0, /* sdu.l_buf = 12*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x05, /* Address field: PD = 0, C/R = 0, SAPI = 5 */
    0xF1, /* Control field (U frame): F = 1, Mx = 0001 (DM) */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, /* Information field: not permitted! */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_DM1_WITHINFO_SAPI5, 16)

FIELD(SGSN_DM1_WITHINFO_SAPI9)
    96, 0, /* sdu.l_buf = 12*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
    0xF1, /* Control field (U frame): F = 1, Mx = 0001 (DM) */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, /* Information field: not permitted! */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_DM1_WITHINFO_SAPI9, 16)

FIELD(SGSN_DM1_WITHINFO_SAPI11)
    96, 0, /* sdu.l_buf = 12*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
    0xF1, /* Control field (U frame): F = 1, Mx = 0001 (DM) */
    0x01, 0x02, 0x03, 0x04, 0x05, 0x06, 0x07, /* Information field: not permitted! */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(SGSN_DM1_WITHINFO_SAPI11, 16)
/*

```

2.2.4 XID Parameter Fields

```

*/
/*
INFO

```



```

        0x00, 0x00, 0x00
ENDFIELD(XID1_VE0_NU500_SAPI1, 14)

FIELD(XID1_VE0_NU300_SAPI1)
    80, 0,
    0, 0,
    0x41,
    0xFB,
    0x01, 0,
    0x16, 0x01, 0x2C,
    0x00, 0x00, 0x00
ENDFIELD(XID1_VE0_NU300_SAPI1, 14)

FIELD(XID1_NU300_SAPI1)
    64, 0,
    0, 0,
    0x41,
    0xFB,
    0x16, 0x01, 0x2C,
    0x00, 0x00, 0x00
ENDFIELD(XID1_NU300_SAPI1, 12)

FIELD(XID1_VE0_NU400_SAPI3)
    80, 0,
    0, 0,
    0x43,
    0xFB,
    0x01, 0,
    0x16, 0x01, 0x90,
    0x00, 0x00, 0x00
ENDFIELD(XID1_VE0_NU400_SAPI3, 14)

FIELD(XID1_VE0_NU400_L3_SAPI3)
    96, 0,
    0, 0,
    0x43,
    0xFB,
    0x01, 0,
    0x16, 0x01, 0x90,
    0xAC, 0x00,
    0x00, 0x00, 0x00
ENDFIELD(XID1_VE0_NU400_L3_SAPI3, 16)

FIELD(XID1_VE0_NU500_SAPI3)
    80, 0,
    0, 0,
    0x43,
    0xFB,
    0x01, 0,
    0x16, 0x01, 0xF4,
    0x00, 0x00, 0x00
ENDFIELD(XID1_VE0_NU500_SAPI3, 14)

FIELD(XID1_VE0_XA31_NU500_SAPI1)
    112, 0,
    0, 0,
    0x41,
    0xFB,

```

/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 10*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 1 */
/* Control field (U frame): P/F = 1, Function = XID */
/* Version = 0 */
/* N201-U = 300 */
/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 9*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 1 */
/* Control field (U frame): P/F = 1, Function = XID */
/* N201-U = 300 */
/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 10*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 1 */
/* Control field (U frame): P/F = 1, Function = XID */
/* Version = 0 */
/* N201-U = 400 */
/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 12*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 1 */
/* Control field (U frame): P/F = 1, Function = XID */
/* Version = 0 */
/* N201-U = 400 */
/* Empty L3-Para, 2 byte header */
/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 10*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 1 */
/* Control field (U frame): P/F = 1, Function = XID */
/* Version = 0 */
/* N201-U = 500 */
/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 14*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 1 */
/* Control field (U frame): P/F = 1, Function = XID */

```

0x01, 0, /* Version = 0 */
0x7F, 0xAB, 0xCD, 0xEF, /* unknown parameter 31, length = 3 */
0x16, 0x01, 0xF4, /* N201-U = 500 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_VE0_XA31_NU500_SAPI1, 18)

FIELD(XID1_VE0_NU500_NU750_NU1000_SAPI1)
128, 0, /* sdu.l_buf = 16*8 */
0, 0, /* sdu.o_buf = 0 */
0x41, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x01, 0, /* Version = 0 */
0x16, 0x01, 0xF4, /* N201-U = 500 */
0x16, 0x02, 0xEE, /* N201-U = 750 */
0x16, 0x03, 0xE8, /* N201-U = 1000 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_VE0_NU500_NU750_NU1000_SAPI1, 20)

FIELD(XID1_IOV_UI_SAPI1)
96, 0, /* sdu.l_buf = 12*8 */
0, 0, /* sdu.o_buf = 0 */
0x41, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x30, /* XID Reset */
0x84, 0x10, /* XID IOV-UI */
0x65, 0x69, 0xD0, 0x37, /* 936405349 */
0xA1, 0x5E, 0xE4 /* FCS field: real values */
ENDFIELD(XID1_IOV_UI_SAPI1, 20)

FIELD(XID1_VE0_NU500_RE_SAPI1)
88, 0, /* sdu.l_buf = 11*8 */
0, 0, /* sdu.o_buf = 0 */
0x41, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x01, 0, /* Version = 0 */
0x16, 0x01, 0xF4, /* N201-U = 500 */
0x30, /* Reset */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_VE0_NU500_RE_SAPI1, 15)

FIELD(XID1_RE_VE0_NU500_SAPI1)
88, 0, /* sdu.l_buf = 11*8 */
0, 0, /* sdu.o_buf = 0 */
0x41, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x30, /* Reset */
0x01, 0, /* Version = 0 */
0x16, 0x01, 0xF4, /* N201-U = 500 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_RE_VE0_NU500_SAPI1, 15)

FIELD(XID_RESTART_NEG_SAPI1)
56, 0, /* sdu.l_buf = 7*8 */
0, 0, /* sdu.o_buf = 0 */
0x41, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x11, 5, /* N200 = 5 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(XID_RESTART_NEG_SAPI1, 11)

FIELD(XID1_RE_VE0_NU500_SAPI3)

```

88, 0, /* sdu.l_buf = 11*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x30, /* Reset */
0x01, 0, /* Version = 0 */
0x16, 0x01, 0xF4, /* N201-U = 500 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(XID1_RE_VE0_NU500_SAPI3, 15)

FIELD(XID1_RE_VE0_NU400_SAPI3)

```

88, 0, /* sdu.l_buf = 11*8 */
0, 0, /* sdu.o_buf = 0 */
0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x30, /* Reset */
0x01, 0, /* Version = 0 */
0x16, 0x01, 0x90, /* N201-U = 400 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(XID1_RE_VE0_NU400_SAPI3, 15)

FIELD(XID1_SAPI1)

```

40, 0, /* sdu.l_buf = 5*8 */
0, 0, /* sdu.o_buf = 0 */
0x41, /* Address field: PD = 0, C/R = 1, SAPI = 1 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(XID1_SAPI1, 9)

FIELD(XID_CMD_SAPI3)

```

88, 0, /* sdu.l_buf = 11*8 */
0, 0, /* sdu.o_buf = 0 */
0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x16, 0x02, 0x08, /* N201-U = 520 */
0x1A, 0x02, 0x08, /* N201-I = 520 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(XID_CMD_SAPI3, 15)

FIELD(XID_CMD_SAPI5)

```

88, 0, /* sdu.l_buf = 11*8 */
0, 0, /* sdu.o_buf = 0 */
0x05, /* Address field: PD = 0, C/R = 0, SAPI = 5 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x16, 0x02, 0x08, /* N201-U = 520 */
0x1A, 0x02, 0x08, /* N201-I = 520 */
0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */

```

ENDFIELD(XID_CMD_SAPI5, 15)

FIELD(XID_CMD_SAPI9)

```

88, 0, /* sdu.l_buf = 11*8 */
0, 0, /* sdu.o_buf = 0 */
0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
0xFB, /* Control field (U frame): P/F = 1, Function = XID */
0x16, 0x02, 0x08, /* N201-U = 520 */

```

```

        0x1A, 0x02, 0x08,
        0x00, 0x00, 0x00
ENDFIELD(XID_CMD_SAPI9, 15)

FIELD(XID_CMD_SAPI11)
    88, 0,
    0, 0,
    0x0B,
    0xFB,
    0x16, 0x02, 0x08,
    0x1A, 0x02, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(XID_CMD_SAPI11, 15)

FIELD(XID_RSP_SAPI3)
    88, 0,
    0, 0,
    0x03,
    0xFB,
    0x16, 0x02, 0x08,
    0x1A, 0x02, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(XID_RSP_SAPI3, 15)

FIELD(XID_RSP_SAPI5)
    88, 0,
    0, 0,
    0x05,
    0xFB,
    0x16, 0x02, 0x08,
    0x1A, 0x02, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(XID_RSP_SAPI5, 15)

FIELD(XID_RSP_SAPI9)
    88, 0,
    0, 0,
    0x09,
    0xFB,
    0x16, 0x02, 0x08,
    0x1A, 0x02, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(XID_RSP_SAPI9, 12)

FIELD(XID_RSP_SAPI11)
    88, 0,
    0, 0,
    0x0B,
    0xFB,
    0x16, 0x02, 0x08,
    0x1A, 0x02, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(XID_RSP_SAPI11, 15)

FIELD(XID_CMD_SAPI3_N201U_1520)
    64, 0,
    0, 0,
    0x03,

```

/ N201-I = 520 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 11*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 11 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 520 */*
/ N201-I = 520 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 11*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 520 */*
/ N201-I = 520 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 11*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 5 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 520 */*
/ N201-I = 520 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 11*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 9 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 520 */*
/ N201-I = 520 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 11*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 11 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 520 */*
/ N201-I = 520 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 8*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3 */*

```

        0xFB,
        0x16, 0x05, 0xF0,
        0x00, 0x00, 0x00
ENDFIELD(XID_CMD_SAPI3_N201U_1520, 12)

FIELD(XID_CMD_SAPI5_N201U_1519)
        64, 0,
        0, 0,
        0x05,
        0xFB,
        0x16, 0x05, 0xEF,
        0x00, 0x00, 0x00
ENDFIELD(XID_CMD_SAPI5_N201U_1519, 12)

FIELD(XID_CMD_SAPI9_N201U_1518)
        64, 0,
        0, 0,
        0x09,
        0xFB,
        0x16, 0x05, 0xEE,
        0x00, 0x00, 0x00
ENDFIELD(XID_CMD_SAPI9_N201U_1518, 12)
FIELD(XID_CMD_SAPI11_N201_U_I_1517_1510)
        88, 0,
        0, 0,
        0x0B,
        0xFB,
        0x16, 0x05, 0xED,
        0x1A, 0x05, 0xE6,
        0x00, 0x00, 0x00
ENDFIELD(XID_CMD_SAPI11_N201_U_I_1517_1510, 15)
FIELD(XID_SAPI3)
        40, 0,
        0, 0,
        0x03,
        0xFB,
        0x00, 0x00, 0x00
ENDFIELD(XID_SAPI3, 9)

FIELD(XID_RSP_SAPI5_N201U_1519)
        64, 0,
        0, 0,
        0x05,
        0xFB,
        0x16, 0x05, 0xEF,
        0x00, 0x00, 0x00
ENDFIELD(XID_RSP_SAPI5_N201U_1519, 12)

FIELD(XID_RSP_SAPI9_N201U_1510)
        64, 0,
        0, 0,
        0x09,
        0xFB,
        0x16, 0x05, 0xE6,
        0x00, 0x00, 0x00
ENDFIELD(XID_RSP_SAPI9_N201U_1510, 12)

FIELD(XID_RSP_EMPTY)

```

/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 1520 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 8*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 5 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 1519 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 8*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 9 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 1510 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 11*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 9 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 1517 */*
/ N201-I = 1510 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 5*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 3 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 8*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 5 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 1519 */*
/ FCS field: all 0x00 in simulation */*

/ sdu.l_buf = 8*8 */*
/ sdu.o_buf = 0 */*
/ Address field: PD = 0, C/R = 0, SAPI = 9 */*
/ Control field (U frame): P/F = 1, Function = XID */*
/ N201-U = 1510 */*
/ FCS field: all 0x00 in simulation */*

```

        40, 0, /* sdu.l_buf = 5*8 */
        0, 0, /* sdu.o_buf = 0 */
        0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
        0xFB, /* Control field (U frame): P/F = 1, Function = XID */
        0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID_RSP_EMPTY, 9)

FIELD(XID_CMD_SAPI3_N201U_300)
        64, 0, /* sdu.l_buf = 8*8 */
        0, 0, /* sdu.o_buf = 0 */
        0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
        0xFB, /* Control field (U frame): P/F = 1, Function = XID */
        0x16, 0x01, 0x2c, /* N201-U = 300 */
        0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID_CMD_SAPI3_N201U_300, 12)
FIELD(XID_CMD_SAPI9_N201U_400)
        64, 0, /* sdu.l_buf = 8*8 */
        0, 0, /* sdu.o_buf = 0 */
        0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
        0xFB, /* Control field (U frame): P/F = 1, Function = XID */
        0x16, 0x01, 0x90, /* N201-U = 400 */
        0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID_CMD_SAPI9_N201U_400, 12)

FIELD(XID_SAPI9)
        64, 0, /* sdu.l_buf = 8*8 */
        0, 0, /* sdu.o_buf = 0 */
        0x09, /* Address field: PD = 0, C/R = 0, SAPI = 9 */
        0xFB, /* Control field (U frame): P/F = 1, Function = XID */
        0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID_SAPI9, 12)

FIELD(XID1_NU300_SAPI3)
        64, 0, /* sdu.l_buf = 8*8 */
        0, 0, /* sdu.o_buf = 0 */
        0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
        0xFB, /* Control field (U frame): P/F = 1, Function = XID */
        0x16, 0x01, 0x2C, /* N201-U = 300 */
        0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_NU300_SAPI3, 12)
FIELD(XID1_NU300_L3_SAPI3)
        96, 0, /* sdu.l_buf = 12*8 */
        0, 0, /* sdu.o_buf = 0 */
        0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
        0xFB, /* Control field (U frame): P/F = 1, Function = XID */
        0x01, 0, /* Version = 0 */
        0x16, 0x01, 0x2C, /* N201-U = 300 */
        0xAC, 0x00, /* Empty L3-Para, 2 byte header */
        0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_NU300_L3_SAPI3, 16)

FIELD(XID_SAPI3_NI1520)
        64, 0, /* sdu.l_buf = 8*8 */
        0, 0, /* sdu.o_buf = 0 */
        0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
        0xFB, /* Control field (U frame): P/F = 1, Function = XID */

```

```

        0x1A, 0x05, 0xF0,
        0x00, 0x00, 0x00
ENDFIELD(XID_SAPI3_NI1520, 12)

FIELD(XID_SAPI3_NI1520_NU300)
    88, 0,
    0, 0,
    0x43,
    0xFB,
    0x16, 0x01, 0x2C,
    0x1A, 0x05, 0xF0,
    0x00, 0x00, 0x00
ENDFIELD(XID_SAPI3_NI1520_NU300, 15)

FIELD(XID_NUI1520_NU200_SAPI3)
    88, 0,
    0, 0,
    0x43,
    0xFB,
    0x16, 0x00, 0xC8,
    0x1A, 0x05, 0xF0,
    0x00, 0x00, 0x00
ENDFIELD(XID_NUI1520_NU200_SAPI3, 15)

FIELD(XID_N5_SAPI3)
    56, 0,
    0, 0,
    0x43,
    0xFB,
    0x11, 15,
    0x00, 0x00, 0x00
ENDFIELD(XID_N5_SAPI3, 11)

FIELD(XID_IOV_UI_SAPI3)
    88, 0,
    0, 0,
    0x43,
    0xFB,
    0x84, 0x10,
    0x65, 0x69, 0xD0, 0x37,
    0xA1, 0x5E, 0xE4
ENDFIELD(XID_IOV_UI_SAPI3, 15)

FIELD(XID1_SAPI3)
    40, 0,
    0, 0,
    0x43,
    0xFB,
    0x00, 0x00, 0x00
ENDFIELD(XID1_SAPI3, 9)

FIELD(XID1_RE_SAPI1)
    48, 0,
    0, 0,
    0x41,
    0xFB,
    0x30,

```

```

/* N201-I = 1520 */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 11*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */
/* Control field (U frame): P/F = 1, Function = XID */
/* N201-U = 300 */
/* N201-I = 1520 */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 11*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */
/* Control field (U frame): P/F = 1, Function = XID */
/* N201-U = 200 */
/* N201-I = 1520 */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 7*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */
/* Control field (U frame): P/F = 1, Function = XID */
/* N200 = 15 */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 11*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */
/* Control field (U frame): P/F = 1, Function = XID */
/* XID IOV-UI */
/* 936405349 */
/* FCS field: real values */

```

```

/* sdu.l_buf = 5*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 3 */
/* Control field (U frame): P/F = 1, Function = XID */
/* FCS field: all 0x00 in simulation */

```

```

/* sdu.l_buf = 6*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 1, SAPI = 1 */
/* Control field (U frame): P/F = 1, Function = XID */
/* Reset */

```

```

0x00, 0x00, 0x00          /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_RE_SAPI1, 10)

FIELD(XID1_RE_SAPI3)
    48, 0,                /* sdu.l_buf = 6*8 */
    0, 0,                /* sdu.o_buf = 0 */
    0x43,                /* Address field: PD = 0, C/R = 1, SAPI = 3 */
    0xFB,                /* Control field (U frame): P/F = 1, Function = XID */
    0x30,                /* Reset */
    0x00, 0x00, 0x00    /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_RE_SAPI3, 10)

FIELD(XID1_SGSN_ALLMAX_SAPI1)
    120, 0,              /* sdu.l_buf = 15*8 */
    0, 0,                /* sdu.o_buf = 0 */
    0x41,                /* Address field: PD = 0, C/R = 1, SAPI = 1 */
    0xFB,                /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0,            /* Version = 0 */
    0x0E, 0x0F, 0xFF,   /* T200 = 4095 */
    0x11, 15,           /* N200 = 15 */
    0x16, 0x05, 0xF0,   /* N201-U = 1520 */
    0x00, 0x00, 0x00    /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_SGSN_ALLMAX_SAPI1, 19)

FIELD(XID1_SGSN_ALLMAX_SAPI3)
    224, 0,              /* sdu.l_buf = 28*8 */
    0, 0,                /* sdu.o_buf = 0 */
    0x43,                /* Address field: PD = 0, C/R = 1, SAPI = 3 */
    0xFB,                /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0,            /* Version = 0 */
    0x0E, 0x0F, 0xFF,   /* T200 = 4095 */
    0x11, 15,           /* N200 = 15 */
    0x16, 0x05, 0xF0,   /* N201-U = 1520 */
    0x1A, 0x05, 0xF0,   /* N201-l = 1520 */
    0x1E, 0x5F, 0x00,   /* mD = 24320 */
    0x22, 0x5F, 0x00,   /* mU = 24320 */
    0x25, 255,          /* kD = 255 */
    0x29, 255,          /* kU = 255 */
    0x00, 0x00, 0x00    /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_SGSN_ALLMAX_SAPI3, 32)

FIELD(XID1_SGSN_ALLMAX_SAPI5)
    224, 0,              /* sdu.l_buf = 28*8 */
    0, 0,                /* sdu.o_buf = 0 */
    0x45,                /* Address field: PD = 0, C/R = 1, SAPI = 5 */
    0xFB,                /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0,            /* Version = 0 */
    0x0E, 0x0F, 0xFF,   /* T200 = 4095 */
    0x11, 15,           /* N200 = 15 */
    0x16, 0x05, 0xF0,   /* N201-U = 1520 */
    0x1A, 0x05, 0xF0,   /* N201-l = 1520 */
    0x1E, 0x5F, 0x00,   /* mD = 24320 */
    0x22, 0x5F, 0x00,   /* mU = 24320 */
    0x25, 255,          /* kD = 255 */
    0x29, 255,          /* kU = 255 */
    0x00, 0x00, 0x00    /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_SGSN_ALLMAX_SAPI5, 32)

```

```

FIELD(XID1_SGSN_ALLMAX_SAPI7)
    120, 0, /* sdu.l_buf = 15*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x47, /* Address field: PD = 0, C/R = 1, SAPI = 7 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0, /* Version = 0 */
    0x0E, 0x0F, 0xFF, /* T200 = 4095 */
    0x11, 15, /* N200 = 15 */
    0x16, 0x05, 0xF0, /* N201-U = 1520 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_SGSN_ALLMAX_SAPI7, 19)

```

```

FIELD(XID1_SGSN_ALLMAX_SAPI9)
    224, 0, /* sdu.l_buf = 28*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x49, /* Address field: PD = 0, C/R = 1, SAPI = 9 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0, /* Version = 0 */
    0x0E, 0x0F, 0xFF, /* T200 = 4095 */
    0x11, 15, /* N200 = 15 */
    0x16, 0x05, 0xF0, /* N201-U = 1520 */
    0x1A, 0x05, 0xF0, /* N201-l = 1520 */
    0x1E, 0x5F, 0x00, /* mD = 24320 */
    0x22, 0x5F, 0x00, /* mU = 24320 */
    0x25, 255, /* kD = 255 */
    0x29, 255, /* kU = 255 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_SGSN_ALLMAX_SAPI9, 32)

```

```

FIELD(XID1_SGSN_ALLMAX_SAPI11)
    224, 0, /* sdu.l_buf = 28*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x4B, /* Address field: PD = 0, C/R = 1, SAPI = 11 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0, /* Version = 0 */
    0x0E, 0x0F, 0xFF, /* T200 = 4095 */
    0x11, 15, /* N200 = 15 */
    0x16, 0x05, 0xF0, /* N201-U = 1520 */
    0x1A, 0x05, 0xF0, /* N201-l = 1520 */
    0x1E, 0x5F, 0x00, /* mD = 24320 */
    0x22, 0x5F, 0x00, /* mU = 24320 */
    0x25, 255, /* kD = 255 */
    0x29, 255, /* kU = 255 */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_SGSN_ALLMAX_SAPI11, 32)

```

```

FIELD(XID1_SGSN_ALLMORETHANMAX_SAPI3)
    224, 0, /* sdu.l_buf = 28*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x43, /* Address field: PD = 0, C/R = 1, SAPI = 3 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0, /* Version = 0 */
    0x0E, 0x10, 0x00, /* T200 = 4096 */
    0x11, 16, /* N200 = 16 */
    0x16, 0x05, 0xF1, /* N201-U = 1521 */
    0x1A, 0x05, 0xF1, /* N201-l = 1521 */
    0x1E, 0x5F, 0x01, /* mD = 24321 */

```

```

        0x22, 0x5F, 0x01,          /* mU = 24321 */
        0x25, 255,                /* kD = 255 */
        0x29, 255,                /* kU = 255 */
        0x00, 0x00, 0x00          /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_SGSN_ALLMORETHANMAX_SAPI3, 32)

FIELD(XID1_SGSN_ALLMIN_SAPI3)
    224, 0,                       /* sdu.l_buf = 28*8 */
    0, 0,                          /* sdu.o_buf = 0 */
    0x43,                           /* Address field: PD = 0, C/R = 1, SAPI = 3 */
    0xFB,                            /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0,                         /* Version = 0 */
    0x0E, 0, 1,                      /* T200 = 1 */
    0x11, 1,                          /* N200 = 1 */
    0x16, 0, 140,                     /* N201-U = 140 */
    0x1A, 0, 140,                     /* N201-I = 140 */
    0x1E, 0, 0,                       /* mD = 0 */
    0x22, 0, 0,                       /* mU = 0 */
    0x25, 1,                          /* kD = 1 */
    0x29, 1,                          /* kU = 1 */
    0x00, 0x00, 0x00                 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_SGSN_ALLMIN_SAPI3, 32)

FIELD(XID1_SGSN_ALLLESSTHANMIN_SAPI3)
    224, 0,                       /* sdu.l_buf = 28*8 */
    0, 0,                          /* sdu.o_buf = 0 */
    0x43,                           /* Address field: PD = 0, C/R = 1, SAPI = 3 */
    0xFB,                            /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0,                         /* Version = 0 */
    0x0E, 0, 0,                      /* T200 = 0 */
    0x11, 0,                          /* N200 = 0 */
    0x16, 0, 139,                     /* N201-U = 139 */
    0x1A, 0, 139,                     /* N201-I = 139 */
    0x1E, 0, 0,                       /* mD = 0 */
    0x22, 0, 0,                       /* mU = 0 */
    0x25, 0,                          /* kD = 0 */
    0x29, 0,                          /* kU = 0 */
    0x00, 0x00, 0x00                 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_SGSN_ALLLESSTHANMIN_SAPI3, 32)

```

/*

The XID parameter N201-U in the following XID command has to contain some value which results in the last octet being 0x00. Otherwise the XID command is rejected due to a wrong FCS field (last three octets, according to l_buf!).

*/

```

FIELD(XID1_EXCEEDSSDULENGTH_SAPI1)
    72, 0,                          /* sdu.l_buf = 9*8 */
    0, 0,                          /* sdu.o_buf = 0 */
    0x41,                           /* Address field: PD = 0, C/R = 1, SAPI = 1 */
    0xFB,                            /* Control field (U frame): P/F = 1, Function = XID */
    0x01, 0,                         /* Version = 0 */
    0x16, 0x02, 0x00,                 /* N201-U = 512 */
    0x00, 0x00, 0x00                 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID1_EXCEEDSSDULENGTH_SAPI1, 14)

```

/*

2.2.4.2 MS Commands / SGSN Responses (C/R bit = 0)

*/

```

FIELD(XID_CR0_EMPTY_L3_SAPI3)
    56, 0, /* sdu.l_buf = 7*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID_CR0_EMPTY_L3_SAPI3, 11)
FIELD(XID_EL3_N201_I_U_520_400_SAPI3)
    104, 0, /* sdu.l_buf = 13*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0x16, 0x01, 0x90, /* N201-U = 400 */
    0x1A, 0x02, 0x08, /* N201-I = 520 */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID_EL3_N201_I_U_520_400_SAPI3, 17)

FIELD(XID_N201_I_U_520_400_EL3_SAPI3)
    104, 0, /* sdu.l_buf = 13*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0x16, 0x01, 0x90, /* N201-U = 400 */
    0x1A, 0x02, 0x08, /* N201_I = 520 */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID_N201_I_U_520_400_EL3_SAPI3, 17)

FIELD(XID_N201I_520_EMPTY_L3_SAPI3)
    80, 0, /* sdu.l_buf = 10*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x03, /* Address field: PD = 0, C/R = 0, SAPI = 3 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0x1A, 0x02, 0x08, /* N201_I = 520 */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID_N201I_520_EMPTY_L3_SAPI3, 14)

FIELD(XID_CR0_EMPTY_L3_SAPI11)
    56, 0, /* sdu.l_buf = 7*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0xAC, 0x00, /* Empty L3-Para, 2 byte header */
    0x00, 0x00, 0x00 /* FCS field: all 0x00 in simulation */
ENDFIELD(XID_CR0_EMPTY_L3_SAPI11, 11)

FIELD(XID_CR0_ALL_DEF_SAPI11)
    232, 0, /* sdu.l_buf = 29*8 */
    0, 0, /* sdu.o_buf = 0 */
    0x0B, /* Address field: PD = 0, C/R = 0, SAPI = 11 */
    0xFB, /* Control field (U frame): P/F = 1, Function = XID */
    0x29, 0x02, 0x25, 0x02, 0x22,
    0x00, 0xBE, 0x1E, 0x00, 0xBE,

```

```

        0x1A, 0x05, 0xDF, 0x16, 0x01,
        0xF4, 0x11, 0x03, 0x0E, 0x01,
        0x90, 0x01, 0x00, 0x2C,
        0x00, 0x00, 0x00
ENDFIELD(XID_CR0_ALL_DEF_SAPI11, 33)
FIELD(XID_CR0_ALL_DEF_EXCPT_N201_I_SAPI3)
    232, 0,
    0, 0,
    0x03,
    0xFB,
    0x29, 0x02, 0x25, 0x02, 0x22,
    0x00, 0xBE, 0x1E, 0x00, 0xBE,
    0x1A, 0x02, 0x08, 0x16, 0x01,
    0xF4, 0x11, 0x03, 0x0E, 0x01,
    0x90, 0x01, 0x00, 0x2C,
    0x00, 0x00, 0x00
ENDFIELD(XID_CR0_ALL_DEF_EXCPT_N201_I_SAPI3, 33)
FIELD(XID_CR1_ALL_DEF_SAPI3)
    224, 0,
    0, 0,
    0x43,
    0xFB,
    0x29, 0x02, 0x25, 0x02, 0x22,
    0x00, 0xBE, 0x1E, 0x00, 0xBE,
    0x1A, 0x05, 0xDF, 0x16, 0x01,
    0xF4, 0x11, 0x03, 0x0E, 0x01,
    0x90, 0x01, 0x00,
    0x00, 0x00, 0x00
ENDFIELD(XID_CR1_ALL_DEF_SAPI3, 32)

FIELD(SGSN_UA1_ALL_DEF_SAPI11)
    232, 0,
    0, 0,
    0x0B,
    0xF6,
    0x29, 0x02, 0x25, 0x02, 0x22,
    0x00, 0xBE, 0x1E, 0x00, 0xBE,
    0x1A, 0x05, 0xDF, 0x16, 0x01,
    0xF4, 0x11, 0x03, 0x0E, 0x01,
    0x90, 0x01, 0x00, 0x2C,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_ALL_DEF_SAPI11, 33)

FIELD(SGSN_UA1_ALL_MIN_EXCPT_T200_N200_SAPI9)
    232, 0,
    0, 0,
    0x09,
    0xF6,
    0x01, 0,
    0x0E, 0x0F, 0xFF,
    0x11, 15,
    0x16, 0, 140,
    0x1A, 0, 140,
    0x1E, 0, 0,
    0x22, 0, 0,
    0x25, 1,
    0x29, 1,

```

/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 29*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* Control field (U frame): P/F = 1, Function = XID */
/* 29 ku, 25 kd, 22 mu */
/* 1e md */
/* 1a N201_I, 16 N201_U */
/* 11 N200, 0e T200 */
/* 01 Version, 2C Empty Layer 3 */
/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 28*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 3 */
/* Control field (U frame): P/F = 1, Function = XID */
/* 29 ku, 25 kd, 22 mu */
/* 1e md */
/* 1a N201_I, 16 N201_U */
/* 11 N200, 0e T200 */
/* 01 Version, */
/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 29*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 11 */
/* Control field (U frame): P/F = 1, Mx = 0110 (UA) */

/* FCS field: all 0x00 in simulation */

/* sdu.l_buf = 29*8 */
/* sdu.o_buf = 0 */
/* Address field: PD = 0, C/R = 0, SAPI = 9 */
/* Control field (U frame): P/F = 1, Mx = 0110 (UA) */
/* Version = 0 */
/* T200 = 4095 */
/* N200 = 15 */
/* N201-U = 140 */
/* N201-I = 140 */
/* mD = 0 */
/* mU = 0 */
/* kD = 1 */
/* kU = 1 */

```

        0x2C,
        0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_ALL_MIN_EXCPT_T200_N200_SAPI9, 33)

FIELD(SGSN_UA1_N201U_400_SAPI3)
    72, 0,
    0, 0,
    0x03,
    0xF6,
    0x16, 0x01, 0x90,
    0x2C,
    0x00, 0x00, 0x00
ENDFIELD(SGSN_UA1_N201U_400_SAPI3, 13)

FIELD(XID_CR0_ALL_DIF_SAPI11)
    232, 0,
    0, 0,
    0x0B,
    0xFB,
    0x29, 0x02, 0x25, 0x02, 0x22,
    0x00, 0xBE, 0x1E, 0x00, 0xBE,
    0x1A, 0x05, 0xDF, 0x16, 0x01,
    0xF4, 0x11, 0x05, 0x0E, 0x01,
    0x90, 0x01, 0x00, 0x2C,
    0x00, 0x00, 0x00
ENDFIELD(XID_CR0_ALL_DIF_SAPI11, 33)

```

/*

2.2.5 Primitive Mass Test

*/

/*

*/

2.2.5.1 GRR-S-Frames , RR, SAPI 3

*/

```

FIELD(RR_NR0_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x00,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR0_A0_S3, 10)

FIELD(RR_NR1_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x04,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR1_A0_S3, 10)

FIELD(RR_NR2_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR2_A0_S3, 10)

```

```
FIELD(RR_NR3_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x0c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR3_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=3 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR4_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x10,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR4_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=4 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR5_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x14,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR5_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=5 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR6_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x18,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR6_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=6 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR7_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x1c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR7_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=7 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR8_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x20,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR8_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=8 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR9_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x24,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR9_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=9 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR10_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x28,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR10_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=10 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR11_A0_S3)
    0x30, 0x00, 0x00, 0x00,
```

```

        0x03,                               /* PD = 0, C/R = 0, SAPI = 3 */
        0x80, 0x2c,                         /* S frame: N(R)=11 A=0 RR */
        0x00, 0x00, 0x00                   /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR11_A0_S3, 10)

FIELD(RR_NR12_A0_S3)
        0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03,                               /* PD = 0, C/R = 0, SAPI = 3 */
        0x80, 0x30,                         /* S frame: N(R)=12 A=0 RR */
        0x00, 0x00, 0x00                   /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR12_A0_S3, 10)

FIELD(RR_NR13_A0_S3)
        0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03,                               /* PD = 0, C/R = 0, SAPI = 3 */
        0x80, 0x34,                         /* S frame: N(R)=13 A=0 RR */
        0x00, 0x00, 0x00                   /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR13_A0_S3, 10)

FIELD(RR_NR14_A0_S3)
        0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03,                               /* PD = 0, C/R = 0, SAPI = 3 */
        0x80, 0x38,                         /* S frame: N(R)=14 A=0 RR */
        0x00, 0x00, 0x00                   /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR14_A0_S3, 10)

FIELD(RR_NR15_A0_S3)
        0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03,                               /* PD = 0, C/R = 0, SAPI = 3 */
        0x80, 0x3c,                         /* S frame: N(R)=15 A=0 RR */
        0x00, 0x00, 0x00                   /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR15_A0_S3, 10)

FIELD(RR_NR16_A0_S3)
        0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03,                               /* PD = 0, C/R = 0, SAPI = 3 */
        0x80, 0x40,                         /* S frame: N(R)=16 A=0 RR */
        0x00, 0x00, 0x00                   /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR16_A0_S3, 10)

FIELD(RR_NR17_A0_S3)
        0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03,                               /* PD = 0, C/R = 0, SAPI = 3 */
        0x80, 0x44,                         /* S frame: N(R)=17 A=0 RR */
        0x00, 0x00, 0x00                   /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR17_A0_S3, 10)

FIELD(RR_NR18_A0_S3)
        0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03,                               /* PD = 0, C/R = 0, SAPI = 3 */
        0x80, 0x48,                         /* S frame: N(R)=18 A=0 RR */
        0x00, 0x00, 0x00                   /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR18_A0_S3, 10)

FIELD(RR_NR19_A0_S3)
        0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03,                               /* PD = 0, C/R = 0, SAPI = 3 */
        0x80, 0x4c,                         /* S frame: N(R)=19 A=0 RR */

```

```

        0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR19_A0_S3, 10)

FIELD(RR_NR20_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x50,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR20_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=20 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR21_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x54,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR21_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=21 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR22_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x58,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR22_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=22 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR23_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x5c,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR23_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=23 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR24_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x60,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR24_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=24 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR25_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x64,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR25_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=25 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR26_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x68,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR26_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=26 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR27_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x6c,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR27_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=27 A=0 RR */
                                        /* FCS: 0x00 for sim. */

```

```
FIELD(RR_NR28_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x70,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR28_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=28 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR29_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x74,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR29_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=29 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR30_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x78,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR30_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=30 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR31_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x7c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR31_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=31 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR32_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x80,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR32_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=32 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR33_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x84,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR33_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=33 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR34_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x88,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR34_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=34 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR35_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0x8c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR35_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=35 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR36_A0_S3)
```

```

        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x80, 0x90,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR36_A0_S3, 10)

FIELD(RR_NR37_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x80, 0x94,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR37_A0_S3, 10)

FIELD(RR_NR38_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x80, 0x98,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR38_A0_S3, 10)

FIELD(RR_NR39_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x80, 0x9c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR39_A0_S3, 10)

FIELD(RR_NR40_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x80, 0xa0,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR40_A0_S3, 10)

FIELD(RR_NR41_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x80, 0xa4,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR41_A0_S3, 10)

FIELD(RR_NR42_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x80, 0xa8,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR42_A0_S3, 10)

FIELD(RR_NR43_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x80, 0xac,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR43_A0_S3, 10)

FIELD(RR_NR44_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,

```

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=36 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=37 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=38 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=39 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=40 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=41 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=42 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=43 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

```

        0x80, 0xb0,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR44_A0_S3, 10)

FIELD(RR_NR45_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xb4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR45_A0_S3, 10)

FIELD(RR_NR46_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xb8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR46_A0_S3, 10)

FIELD(RR_NR47_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xbc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR47_A0_S3, 10)

FIELD(RR_NR48_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xc0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR48_A0_S3, 10)

FIELD(RR_NR49_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xc4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR49_A0_S3, 10)

FIELD(RR_NR50_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xc8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR50_A0_S3, 10)

FIELD(RR_NR51_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xcc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR51_A0_S3, 10)

FIELD(RR_NR52_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xd0,
    0x00, 0x00, 0x00

```

/ S frame: N(R)=44 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=45 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=46 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=47 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=48 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=49 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=50 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=51 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=52 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR52_A0_S3, 10)

FIELD(RR_NR53_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x80, 0xd4,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=53 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR53_A0_S3, 10)

FIELD(RR_NR54_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x80, 0xd8,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=54 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR54_A0_S3, 10)

FIELD(RR_NR55_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x80, 0xdc,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=55 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR55_A0_S3, 10)

FIELD(RR_NR56_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x80, 0xe0,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=56 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR56_A0_S3, 10)

FIELD(RR_NR57_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x80, 0xe4,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=57 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR57_A0_S3, 10)

FIELD(RR_NR58_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x80, 0xe8,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=58 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR58_A0_S3, 10)

FIELD(RR_NR59_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x80, 0xec,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=59 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR59_A0_S3, 10)

FIELD(RR_NR60_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x80, 0xf0,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=60 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR60_A0_S3, 10)

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FIELD(RR_NR61_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xf4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR61_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=61 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR62_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xf8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR62_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=62 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR63_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x80, 0xfc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR63_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=63 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR64_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x00,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR64_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=64 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR65_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x04,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR65_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=65 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR66_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR66_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=66 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR67_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x0c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR67_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=67 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR68_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x10,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR68_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=68 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR69_A0_S3)
    0x30, 0x00, 0x00, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x81, 0x14, /* S frame: N(R)=69 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR69_A0_S3, 10)

FIELD(RR_NR70_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x81, 0x18, /* S frame: N(R)=70 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR70_A0_S3, 10)

FIELD(RR_NR71_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x81, 0x1c, /* S frame: N(R)=71 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR71_A0_S3, 10)

FIELD(RR_NR72_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x81, 0x20, /* S frame: N(R)=72 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR72_A0_S3, 10)

FIELD(RR_NR73_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x81, 0x24, /* S frame: N(R)=73 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR73_A0_S3, 10)

FIELD(RR_NR74_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x81, 0x28, /* S frame: N(R)=74 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR74_A0_S3, 10)

FIELD(RR_NR75_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x81, 0x2c, /* S frame: N(R)=75 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR75_A0_S3, 10)

FIELD(RR_NR76_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x81, 0x30, /* S frame: N(R)=76 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR76_A0_S3, 10)

FIELD(RR_NR77_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x81, 0x34, /* S frame: N(R)=77 A=0 RR */

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        0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR77_A0_S3, 10)

FIELD(RR_NR78_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x81, 0x38,                       /* S frame: N(R)=78 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR78_A0_S3, 10)

FIELD(RR_NR79_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x81, 0x3c,                       /* S frame: N(R)=79 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR79_A0_S3, 10)

FIELD(RR_NR80_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x81, 0x40,                       /* S frame: N(R)=80 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR80_A0_S3, 10)

FIELD(RR_NR81_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x81, 0x44,                       /* S frame: N(R)=81 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR81_A0_S3, 10)

FIELD(RR_NR82_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x81, 0x48,                       /* S frame: N(R)=82 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR82_A0_S3, 10)

FIELD(RR_NR83_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x81, 0x4c,                       /* S frame: N(R)=83 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR83_A0_S3, 10)

FIELD(RR_NR84_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x81, 0x50,                       /* S frame: N(R)=84 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR84_A0_S3, 10)

FIELD(RR_NR85_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x81, 0x54,                       /* S frame: N(R)=85 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR85_A0_S3, 10)

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FIELD(RR_NR86_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x58,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR86_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=86 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR87_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x5c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR87_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=87 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR88_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x60,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR88_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=88 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR89_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x64,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR89_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=89 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR90_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x68,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR90_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=90 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR91_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x6c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR91_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=91 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR92_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x70,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR92_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=92 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR93_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0x74,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR93_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=93 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR94_A0_S3)

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        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x81, 0x78,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR94_A0_S3, 10)

FIELD(RR_NR95_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x81, 0x7c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR95_A0_S3, 10)

FIELD(RR_NR96_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x81, 0x80,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR96_A0_S3, 10)

FIELD(RR_NR97_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x81, 0x84,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR97_A0_S3, 10)

FIELD(RR_NR98_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x81, 0x88,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR98_A0_S3, 10)

FIELD(RR_NR99_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x81, 0x8c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR99_A0_S3, 10)

FIELD(RR_NR100_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x81, 0x90,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR100_A0_S3, 10)

FIELD(RR_NR101_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x81, 0x94,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR101_A0_S3, 10)

FIELD(RR_NR102_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,

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/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=94 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=95 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=96 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=97 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=98 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=99 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=100 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=101 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

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        0x81, 0x98,                /* S frame: N(R)=102 A=0 RR */
        0x00, 0x00, 0x00         /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR102_A0_S3, 10)

FIELD(RR_NR103_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                     /* PD = 0, C/R = 0, SAPI = 3 */
        0x81, 0x9c,               /* S frame: N(R)=103 A=0 RR */
        0x00, 0x00, 0x00         /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR103_A0_S3, 10)

FIELD(RR_NR104_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                     /* PD = 0, C/R = 0, SAPI = 3 */
        0x81, 0xa0,               /* S frame: N(R)=104 A=0 RR */
        0x00, 0x00, 0x00         /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR104_A0_S3, 10)

FIELD(RR_NR105_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                     /* PD = 0, C/R = 0, SAPI = 3 */
        0x81, 0xa4,               /* S frame: N(R)=105 A=0 RR */
        0x00, 0x00, 0x00         /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR105_A0_S3, 10)

FIELD(RR_NR106_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                     /* PD = 0, C/R = 0, SAPI = 3 */
        0x81, 0xa8,               /* S frame: N(R)=106 A=0 RR */
        0x00, 0x00, 0x00         /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR106_A0_S3, 10)

FIELD(RR_NR107_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                     /* PD = 0, C/R = 0, SAPI = 3 */
        0x81, 0xac,               /* S frame: N(R)=107 A=0 RR */
        0x00, 0x00, 0x00         /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR107_A0_S3, 10)

FIELD(RR_NR108_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                     /* PD = 0, C/R = 0, SAPI = 3 */
        0x81, 0xb0,               /* S frame: N(R)=108 A=0 RR */
        0x00, 0x00, 0x00         /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR108_A0_S3, 10)

FIELD(RR_NR109_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                     /* PD = 0, C/R = 0, SAPI = 3 */
        0x81, 0xb4,               /* S frame: N(R)=109 A=0 RR */
        0x00, 0x00, 0x00         /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR109_A0_S3, 10)

FIELD(RR_NR110_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                     /* PD = 0, C/R = 0, SAPI = 3 */
        0x81, 0xb8,               /* S frame: N(R)=110 A=0 RR */
        0x00, 0x00, 0x00         /* FCS: 0x00 for sim. */

```

ENDFIELD(RR_NR110_A0_S3, 10)

FIELD(RR_NR111_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x81, 0xbc,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=111 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR111_A0_S3, 10)

FIELD(RR_NR112_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x81, 0xc0,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=112 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR112_A0_S3, 10)

FIELD(RR_NR113_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x81, 0xc4,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=113 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR113_A0_S3, 10)

FIELD(RR_NR114_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x81, 0xc8,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=114 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR114_A0_S3, 10)

FIELD(RR_NR115_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x81, 0xcc,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=115 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR115_A0_S3, 10)

FIELD(RR_NR116_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x81, 0xd0,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=116 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR116_A0_S3, 10)

FIELD(RR_NR117_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x81, 0xd4,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=117 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR117_A0_S3, 10)

FIELD(RR_NR118_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x81, 0xd8,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=118 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR118_A0_S3, 10)

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FIELD(RR_NR119_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0xdc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR119_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=119 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR120_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0xe0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR120_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=120 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR121_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0xe4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR121_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=121 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR122_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0xe8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR122_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=122 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR123_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0xec,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR123_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=123 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR124_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0xf0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR124_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=124 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR125_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0xf4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR125_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=125 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR126_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x81, 0xf8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR126_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=126 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR127_A0_S3)
    0x30, 0x00, 0x00, 0x00,

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        0x03, /* PD = 0, C/R = 0, SAPI = 3 */
        0x81, 0xfc, /* S frame: N(R)=127 A=0 RR */
        0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR127_A0_S3, 10)

FIELD(RR_NR128_A0_S3)
        0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03, /* PD = 0, C/R = 0, SAPI = 3 */
        0x82, 0x00, /* S frame: N(R)=128 A=0 RR */
        0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR128_A0_S3, 10)

FIELD(RR_NR129_A0_S3)
        0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03, /* PD = 0, C/R = 0, SAPI = 3 */
        0x82, 0x04, /* S frame: N(R)=129 A=0 RR */
        0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR129_A0_S3, 10)

FIELD(RR_NR130_A0_S3)
        0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03, /* PD = 0, C/R = 0, SAPI = 3 */
        0x82, 0x08, /* S frame: N(R)=130 A=0 RR */
        0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR130_A0_S3, 10)

FIELD(RR_NR131_A0_S3)
        0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03, /* PD = 0, C/R = 0, SAPI = 3 */
        0x82, 0x0c, /* S frame: N(R)=131 A=0 RR */
        0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR131_A0_S3, 10)

FIELD(RR_NR132_A0_S3)
        0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03, /* PD = 0, C/R = 0, SAPI = 3 */
        0x82, 0x10, /* S frame: N(R)=132 A=0 RR */
        0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR132_A0_S3, 10)

FIELD(RR_NR133_A0_S3)
        0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03, /* PD = 0, C/R = 0, SAPI = 3 */
        0x82, 0x14, /* S frame: N(R)=133 A=0 RR */
        0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR133_A0_S3, 10)

FIELD(RR_NR134_A0_S3)
        0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03, /* PD = 0, C/R = 0, SAPI = 3 */
        0x82, 0x18, /* S frame: N(R)=134 A=0 RR */
        0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR134_A0_S3, 10)

FIELD(RR_NR135_A0_S3)
        0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
        0x03, /* PD = 0, C/R = 0, SAPI = 3 */
        0x82, 0x1c, /* S frame: N(R)=135 A=0 RR */

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        0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR135_A0_S3, 10)

FIELD(RR_NR136_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x82, 0x20,                       /* S frame: N(R)=136 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR136_A0_S3, 10)

FIELD(RR_NR137_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x82, 0x24,                       /* S frame: N(R)=137 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR137_A0_S3, 10)

FIELD(RR_NR138_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x82, 0x28,                       /* S frame: N(R)=138 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR138_A0_S3, 10)

FIELD(RR_NR139_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x82, 0x2c,                       /* S frame: N(R)=139 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR139_A0_S3, 10)

FIELD(RR_NR140_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x82, 0x30,                       /* S frame: N(R)=140 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR140_A0_S3, 10)

FIELD(RR_NR141_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x82, 0x34,                       /* S frame: N(R)=141 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR141_A0_S3, 10)

FIELD(RR_NR142_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x82, 0x38,                       /* S frame: N(R)=142 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR142_A0_S3, 10)

FIELD(RR_NR143_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x82, 0x3c,                       /* S frame: N(R)=143 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR143_A0_S3, 10)

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FIELD(RR_NR144_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x40,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR144_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=144 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR145_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x44,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR145_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=145 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR146_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x48,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR146_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=146 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR147_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x4c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR147_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=147 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR148_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x50,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR148_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=148 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR149_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x54,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR149_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=149 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR150_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x58,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR150_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=150 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR151_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x5c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR151_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=151 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR152_A0_S3)
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        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x82, 0x60,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR152_A0_S3, 10)

FIELD(RR_NR153_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x82, 0x64,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR153_A0_S3, 10)

FIELD(RR_NR154_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x82, 0x68,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR154_A0_S3, 10)

FIELD(RR_NR155_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x82, 0x6c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR155_A0_S3, 10)

FIELD(RR_NR156_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x82, 0x70,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR156_A0_S3, 10)

FIELD(RR_NR157_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x82, 0x74,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR157_A0_S3, 10)

FIELD(RR_NR158_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x82, 0x78,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR158_A0_S3, 10)

FIELD(RR_NR159_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x82, 0x7c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR159_A0_S3, 10)

FIELD(RR_NR160_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,

```

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=152 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=153 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=154 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=155 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=156 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=157 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=158 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=159 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

```

        0x82, 0x80,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR160_A0_S3, 10)

FIELD(RR_NR161_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x84,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR161_A0_S3, 10)

FIELD(RR_NR162_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x88,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR162_A0_S3, 10)

FIELD(RR_NR163_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x8c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR163_A0_S3, 10)

FIELD(RR_NR164_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x90,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR164_A0_S3, 10)

FIELD(RR_NR165_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x94,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR165_A0_S3, 10)

FIELD(RR_NR166_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x98,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR166_A0_S3, 10)

FIELD(RR_NR167_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0x9c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR167_A0_S3, 10)

FIELD(RR_NR168_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0xa0,
    0x00, 0x00, 0x00

```

/* S frame: N(R)=160 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=161 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=162 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=163 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=164 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=165 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=166 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=167 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=168 A=0 RR */
/* FCS: 0x00 for sim. */

ENDFIELD(RR_NR168_A0_S3, 10)

FIELD(RR_NR169_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x82, 0xa4,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=169 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR169_A0_S3, 10)

FIELD(RR_NR170_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x82, 0xa8,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=170 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR170_A0_S3, 10)

FIELD(RR_NR171_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x82, 0xac,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=171 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR171_A0_S3, 10)

FIELD(RR_NR172_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x82, 0xb0,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=172 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR172_A0_S3, 10)

FIELD(RR_NR173_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x82, 0xb4,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=173 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR173_A0_S3, 10)

FIELD(RR_NR174_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x82, 0xb8,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=174 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR174_A0_S3, 10)

FIELD(RR_NR175_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x82, 0xbc,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=175 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR175_A0_S3, 10)

FIELD(RR_NR176_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x82, 0xc0,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=176 A=0 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_NR176_A0_S3, 10)

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FIELD(RR_NR177_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0xc4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR177_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=177 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR178_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0xc8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR178_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=178 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR179_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0xcc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR179_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=179 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR180_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0xd0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR180_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=180 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR181_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0xd4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR181_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=181 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR182_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0xd8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR182_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=182 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR183_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0xdc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR183_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=183 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR184_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x82, 0xe0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR184_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=184 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR185_A0_S3)
    0x30, 0x00, 0x00, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x82, 0xe4, /* S frame: N(R)=185 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR185_A0_S3, 10)

FIELD(RR_NR186_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x82, 0xe8, /* S frame: N(R)=186 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR186_A0_S3, 10)

FIELD(RR_NR187_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x82, 0xec, /* S frame: N(R)=187 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR187_A0_S3, 10)

FIELD(RR_NR188_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x82, 0xf0, /* S frame: N(R)=188 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR188_A0_S3, 10)

FIELD(RR_NR189_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x82, 0xf4, /* S frame: N(R)=189 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR189_A0_S3, 10)

FIELD(RR_NR190_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x82, 0xf8, /* S frame: N(R)=190 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR190_A0_S3, 10)

FIELD(RR_NR191_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x82, 0xfc, /* S frame: N(R)=191 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR191_A0_S3, 10)

FIELD(RR_NR192_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0x00, /* S frame: N(R)=192 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR192_A0_S3, 10)

FIELD(RR_NR193_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0x04, /* S frame: N(R)=193 A=0 RR */

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        0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR193_A0_S3, 10)

FIELD(RR_NR194_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x83, 0x08,                       /* S frame: N(R)=194 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR194_A0_S3, 10)

FIELD(RR_NR195_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x83, 0x0c,                       /* S frame: N(R)=195 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR195_A0_S3, 10)

FIELD(RR_NR196_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x83, 0x10,                       /* S frame: N(R)=196 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR196_A0_S3, 10)

FIELD(RR_NR197_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x83, 0x14,                       /* S frame: N(R)=197 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR197_A0_S3, 10)

FIELD(RR_NR198_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x83, 0x18,                       /* S frame: N(R)=198 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR198_A0_S3, 10)

FIELD(RR_NR199_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x83, 0x1c,                       /* S frame: N(R)=199 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR199_A0_S3, 10)

FIELD(RR_NR200_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x83, 0x20,                       /* S frame: N(R)=200 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR200_A0_S3, 10)

FIELD(RR_NR201_A0_S3)
    0x30, 0x00, 0x00, 0x00,           /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x83, 0x24,                       /* S frame: N(R)=201 A=0 RR */
    0x00, 0x00, 0x00                 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR201_A0_S3, 10)

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FIELD(RR_NR202_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0x28,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR202_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=202 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR203_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0x2c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR203_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=203 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR204_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0x30,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR204_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=204 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR205_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0x34,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR205_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=205 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR206_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0x38,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR206_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=206 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR207_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0x3c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR207_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=207 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR208_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0x40,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR208_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=208 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR209_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0x44,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR209_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=209 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR210_A0_S3)

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0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x48,
0x00, 0x00, 0x00
ENDFIELD(RR_NR210_A0_S3, 10)

FIELD(RR_NR211_A0_S3)
0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x4c,
0x00, 0x00, 0x00
ENDFIELD(RR_NR211_A0_S3, 10)

FIELD(RR_NR212_A0_S3)
0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x50,
0x00, 0x00, 0x00
ENDFIELD(RR_NR212_A0_S3, 10)

FIELD(RR_NR213_A0_S3)
0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x54,
0x00, 0x00, 0x00
ENDFIELD(RR_NR213_A0_S3, 10)

FIELD(RR_NR214_A0_S3)
0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x58,
0x00, 0x00, 0x00
ENDFIELD(RR_NR214_A0_S3, 10)

FIELD(RR_NR215_A0_S3)
0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x5c,
0x00, 0x00, 0x00
ENDFIELD(RR_NR215_A0_S3, 10)

FIELD(RR_NR216_A0_S3)
0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x60,
0x00, 0x00, 0x00
ENDFIELD(RR_NR216_A0_S3, 10)

FIELD(RR_NR217_A0_S3)
0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x64,
0x00, 0x00, 0x00
ENDFIELD(RR_NR217_A0_S3, 10)

FIELD(RR_NR218_A0_S3)
0x30, 0x00, 0x00, 0x00,
0x03,

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/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=210 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=211 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=212 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=213 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=214 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=215 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=216 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=217 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*

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        0x83, 0x68,                /* S frame: N(R)=218 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR218_A0_S3, 10)

FIELD(RR_NR219_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x83, 0x6c,                /* S frame: N(R)=219 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR219_A0_S3, 10)

FIELD(RR_NR220_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x83, 0x70,                /* S frame: N(R)=220 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR220_A0_S3, 10)

FIELD(RR_NR221_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x83, 0x74,                /* S frame: N(R)=221 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR221_A0_S3, 10)

FIELD(RR_NR222_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x83, 0x78,                /* S frame: N(R)=222 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR222_A0_S3, 10)

FIELD(RR_NR223_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x83, 0x7c,                /* S frame: N(R)=223 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR223_A0_S3, 10)

FIELD(RR_NR224_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x83, 0x80,                /* S frame: N(R)=224 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR224_A0_S3, 10)

FIELD(RR_NR225_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x83, 0x84,                /* S frame: N(R)=225 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR225_A0_S3, 10)

FIELD(RR_NR226_A0_S3)
        0x30, 0x00, 0x00, 0x00,   /* sdu.l_buf= 48, sdu.o_buf= 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x83, 0x88,                /* S frame: N(R)=226 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */

```

ENDFIELD(RR_NR226_A0_S3, 10)

FIELD(RR_NR227_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x8c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=227 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR227_A0_S3, 10)

FIELD(RR_NR228_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x90,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=228 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR228_A0_S3, 10)

FIELD(RR_NR229_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x94,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=229 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR229_A0_S3, 10)

FIELD(RR_NR230_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x98,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=230 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR230_A0_S3, 10)

FIELD(RR_NR231_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0x9c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=231 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR231_A0_S3, 10)

FIELD(RR_NR232_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0xa0,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=232 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR232_A0_S3, 10)

FIELD(RR_NR233_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0xa4,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=233 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR233_A0_S3, 10)

FIELD(RR_NR234_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x83, 0xa8,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=234 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR234_A0_S3, 10)

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FIELD(RR_NR235_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0xac,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR235_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=235 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR236_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0xb0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR236_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=236 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR237_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0xb4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR237_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=237 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR238_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0xb8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR238_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=238 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR239_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0xbc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR239_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=239 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR240_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0xc0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR240_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=240 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR241_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0xc4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR241_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=241 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR242_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x83, 0xc8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR242_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=242 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR243_A0_S3)
    0x30, 0x00, 0x00, 0x00,

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```

0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0xcc, /* S frame: N(R)=243 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR243_A0_S3, 10)

FIELD(RR_NR244_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0xd0, /* S frame: N(R)=244 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR244_A0_S3, 10)

FIELD(RR_NR245_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0xd4, /* S frame: N(R)=245 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR245_A0_S3, 10)

FIELD(RR_NR246_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0xd8, /* S frame: N(R)=246 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR246_A0_S3, 10)

FIELD(RR_NR247_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0xdc, /* S frame: N(R)=247 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR247_A0_S3, 10)

FIELD(RR_NR248_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0xe0, /* S frame: N(R)=248 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR248_A0_S3, 10)

FIELD(RR_NR249_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0xe4, /* S frame: N(R)=249 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR249_A0_S3, 10)

FIELD(RR_NR250_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0xe8, /* S frame: N(R)=250 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR250_A0_S3, 10)

FIELD(RR_NR251_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x83, 0xec, /* S frame: N(R)=251 A=0 RR */

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        0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR251_A0_S3, 10)

FIELD(RR_NR252_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x83, 0xf0,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR252_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=252 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR253_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x83, 0xf4,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR253_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=253 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR254_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x83, 0xf8,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR254_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=254 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR255_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x83, 0xfc,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR255_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=255 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR256_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x00,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR256_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=256 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR257_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x04,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR257_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=257 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR258_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x08,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR258_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=258 A=0 RR */
                                        /* FCS: 0x00 for sim. */

FIELD(RR_NR259_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x0c,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR259_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                        /* S frame: N(R)=259 A=0 RR */
                                        /* FCS: 0x00 for sim. */

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FIELD(RR_NR260_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x10,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR260_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=260 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR261_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x14,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR261_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=261 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR262_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x18,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR262_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=262 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR263_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x1c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR263_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=263 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR264_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x20,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR264_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=264 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR265_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x24,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR265_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=265 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR266_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x28,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR266_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=266 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR267_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x2c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR267_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=267 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR268_A0_S3)

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        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x30,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR268_A0_S3, 10)

FIELD(RR_NR269_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x34,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR269_A0_S3, 10)

FIELD(RR_NR270_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x38,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR270_A0_S3, 10)

FIELD(RR_NR271_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x3c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR271_A0_S3, 10)

FIELD(RR_NR272_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x40,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR272_A0_S3, 10)

FIELD(RR_NR273_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x44,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR273_A0_S3, 10)

FIELD(RR_NR274_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x48,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR274_A0_S3, 10)

FIELD(RR_NR275_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0x4c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR275_A0_S3, 10)

FIELD(RR_NR276_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,

```

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=268 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=269 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=270 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=271 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=272 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=273 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=274 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=275 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*

```

        0x84, 0x50,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR276_A0_S3, 10)

FIELD(RR_NR277_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x54,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR277_A0_S3, 10)

FIELD(RR_NR278_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x58,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR278_A0_S3, 10)

FIELD(RR_NR279_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x5c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR279_A0_S3, 10)

FIELD(RR_NR280_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x60,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR280_A0_S3, 10)

FIELD(RR_NR281_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x64,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR281_A0_S3, 10)

FIELD(RR_NR282_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x68,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR282_A0_S3, 10)

FIELD(RR_NR283_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x6c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR283_A0_S3, 10)

FIELD(RR_NR284_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x70,
    0x00, 0x00, 0x00

```

/* S frame: N(R)=276 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=277 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=278 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=279 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=280 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=281 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=282 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=283 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=284 A=0 RR */
/* FCS: 0x00 for sim. */

ENDFIELD(RR_NR284_A0_S3, 10)

FIELD(RR_NR285_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x84, 0x74,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=285 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR285_A0_S3, 10)

FIELD(RR_NR286_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x84, 0x78,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=286 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR286_A0_S3, 10)

FIELD(RR_NR287_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x84, 0x7c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=287 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR287_A0_S3, 10)

FIELD(RR_NR288_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x84, 0x80,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=288 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR288_A0_S3, 10)

FIELD(RR_NR289_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x84, 0x84,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=289 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR289_A0_S3, 10)

FIELD(RR_NR290_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x84, 0x88,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=290 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR290_A0_S3, 10)

FIELD(RR_NR291_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x84, 0x8c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=291 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR291_A0_S3, 10)

FIELD(RR_NR292_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x84, 0x90,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=292 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR292_A0_S3, 10)

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FIELD(RR_NR293_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x94,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR293_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=293 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR294_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x98,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR294_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=294 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR295_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0x9c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR295_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=295 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR296_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0xa0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR296_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=296 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR297_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0xa4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR297_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=297 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR298_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0xa8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR298_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=298 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR299_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0xac,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR299_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=299 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR300_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0xb0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR300_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=300 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR301_A0_S3)
    0x30, 0x00, 0x00, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x84, 0xb4, /* S frame: N(R)=301 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR301_A0_S3, 10)

FIELD(RR_NR302_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x84, 0xb8, /* S frame: N(R)=302 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR302_A0_S3, 10)

FIELD(RR_NR303_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x84, 0xbc, /* S frame: N(R)=303 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR303_A0_S3, 10)

FIELD(RR_NR304_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x84, 0xc0, /* S frame: N(R)=304 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR304_A0_S3, 10)

FIELD(RR_NR305_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x84, 0xc4, /* S frame: N(R)=305 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR305_A0_S3, 10)

FIELD(RR_NR306_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x84, 0xc8, /* S frame: N(R)=306 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR306_A0_S3, 10)

FIELD(RR_NR307_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x84, 0xcc, /* S frame: N(R)=307 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR307_A0_S3, 10)

FIELD(RR_NR308_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x84, 0xd0, /* S frame: N(R)=308 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR308_A0_S3, 10)

FIELD(RR_NR309_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x84, 0xd4, /* S frame: N(R)=309 A=0 RR */

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        0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR309_A0_S3, 10)

FIELD(RR_NR310_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0xd8,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR310_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                           /* S frame: N(R)=310 A=0 RR */
                                           /* FCS: 0x00 for sim. */

FIELD(RR_NR311_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0xdc,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR311_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                           /* S frame: N(R)=311 A=0 RR */
                                           /* FCS: 0x00 for sim. */

FIELD(RR_NR312_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0xe0,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR312_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                           /* S frame: N(R)=312 A=0 RR */
                                           /* FCS: 0x00 for sim. */

FIELD(RR_NR313_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0xe4,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR313_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                           /* S frame: N(R)=313 A=0 RR */
                                           /* FCS: 0x00 for sim. */

FIELD(RR_NR314_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0xe8,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR314_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                           /* S frame: N(R)=314 A=0 RR */
                                           /* FCS: 0x00 for sim. */

FIELD(RR_NR315_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0xec,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR315_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                           /* S frame: N(R)=315 A=0 RR */
                                           /* FCS: 0x00 for sim. */

FIELD(RR_NR316_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0xf0,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR316_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                           /* S frame: N(R)=316 A=0 RR */
                                           /* FCS: 0x00 for sim. */

FIELD(RR_NR317_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x84, 0xf4,
        0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
ENDFIELD(RR_NR317_A0_S3, 10)                /* PD = 0, C/R = 0, SAPI = 3 */
                                           /* S frame: N(R)=317 A=0 RR */
                                           /* FCS: 0x00 for sim. */

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FIELD(RR_NR318_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0xf8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR318_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=318 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR319_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x84, 0xfc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR319_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=319 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR320_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x00,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR320_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=320 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR321_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x04,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR321_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=321 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR322_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x08,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR322_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=322 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR323_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x0c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR323_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=323 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR324_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x10,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR324_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=324 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR325_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x14,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR325_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=325 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR326_A0_S3)

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        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x85, 0x18,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR326_A0_S3, 10)

FIELD(RR_NR327_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x85, 0x1c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR327_A0_S3, 10)

FIELD(RR_NR328_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x85, 0x20,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR328_A0_S3, 10)

FIELD(RR_NR329_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x85, 0x24,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR329_A0_S3, 10)

FIELD(RR_NR330_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x85, 0x28,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR330_A0_S3, 10)

FIELD(RR_NR331_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x85, 0x2c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR331_A0_S3, 10)

FIELD(RR_NR332_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x85, 0x30,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR332_A0_S3, 10)

FIELD(RR_NR333_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x85, 0x34,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR333_A0_S3, 10)

FIELD(RR_NR334_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,

```

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=326 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=327 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=328 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=329 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=330 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=331 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=332 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=333 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

```

        0x85, 0x38,                /* S frame: N(R)=334 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR334_A0_S3, 10)

FIELD(RR_NR335_A0_S3)
        0x30, 0x00, 0x00, 0x00,  /* sdu.l_buf= 48, sdu.o_buf = 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x85, 0x3c,                /* S frame: N(R)=335 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR335_A0_S3, 10)

FIELD(RR_NR336_A0_S3)
        0x30, 0x00, 0x00, 0x00,  /* sdu.l_buf= 48, sdu.o_buf = 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x85, 0x40,                /* S frame: N(R)=336 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR336_A0_S3, 10)

FIELD(RR_NR337_A0_S3)
        0x30, 0x00, 0x00, 0x00,  /* sdu.l_buf= 48, sdu.o_buf = 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x85, 0x44,                /* S frame: N(R)=337 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR337_A0_S3, 10)

FIELD(RR_NR338_A0_S3)
        0x30, 0x00, 0x00, 0x00,  /* sdu.l_buf= 48, sdu.o_buf = 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x85, 0x48,                /* S frame: N(R)=338 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR338_A0_S3, 10)

FIELD(RR_NR339_A0_S3)
        0x30, 0x00, 0x00, 0x00,  /* sdu.l_buf= 48, sdu.o_buf = 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x85, 0x4c,                /* S frame: N(R)=339 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR339_A0_S3, 10)

FIELD(RR_NR340_A0_S3)
        0x30, 0x00, 0x00, 0x00,  /* sdu.l_buf= 48, sdu.o_buf = 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x85, 0x50,                /* S frame: N(R)=340 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR340_A0_S3, 10)

FIELD(RR_NR341_A0_S3)
        0x30, 0x00, 0x00, 0x00,  /* sdu.l_buf= 48, sdu.o_buf = 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x85, 0x54,                /* S frame: N(R)=341 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR341_A0_S3, 10)

FIELD(RR_NR342_A0_S3)
        0x30, 0x00, 0x00, 0x00,  /* sdu.l_buf= 48, sdu.o_buf = 0 */
        0x03,                      /* PD = 0, C/R = 0, SAPI = 3 */
        0x85, 0x58,                /* S frame: N(R)=342 A=0 RR */
        0x00, 0x00, 0x00          /* FCS: 0x00 for sim. */

```

ENDFIELD(RR_NR342_A0_S3, 10)

FIELD(RR_NR343_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x85, 0x5c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=343 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR343_A0_S3, 10)

FIELD(RR_NR344_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x85, 0x60,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=344 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR344_A0_S3, 10)

FIELD(RR_NR345_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x85, 0x64,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=345 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR345_A0_S3, 10)

FIELD(RR_NR346_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x85, 0x68,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=346 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR346_A0_S3, 10)

FIELD(RR_NR347_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x85, 0x6c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=347 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR347_A0_S3, 10)

FIELD(RR_NR348_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x85, 0x70,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=348 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR348_A0_S3, 10)

FIELD(RR_NR349_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x85, 0x74,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=349 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR349_A0_S3, 10)

FIELD(RR_NR350_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x85, 0x78,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=350 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR350_A0_S3, 10)

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FIELD(RR_NR351_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x7c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR351_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=351 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR352_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x80,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR352_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=352 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR353_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x84,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR353_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=353 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR354_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x88,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR354_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=354 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR355_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x8c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR355_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=355 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR356_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x90,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR356_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=356 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR357_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x94,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR357_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=357 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR358_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0x98,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR358_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=358 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR359_A0_S3)
    0x30, 0x00, 0x00, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x85, 0x9c, /* S frame: N(R)=359 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR359_A0_S3, 10)

FIELD(RR_NR360_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf= 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x85, 0xa0, /* S frame: N(R)=360 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR360_A0_S3, 10)

FIELD(RR_NR361_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf= 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x85, 0xa4, /* S frame: N(R)=361 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR361_A0_S3, 10)

FIELD(RR_NR362_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf= 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x85, 0xa8, /* S frame: N(R)=362 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR362_A0_S3, 10)

FIELD(RR_NR363_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf= 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x85, 0xac, /* S frame: N(R)=363 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR363_A0_S3, 10)

FIELD(RR_NR364_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf= 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x85, 0xb0, /* S frame: N(R)=364 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR364_A0_S3, 10)

FIELD(RR_NR365_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf= 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x85, 0xb4, /* S frame: N(R)=365 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR365_A0_S3, 10)

FIELD(RR_NR366_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf= 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x85, 0xb8, /* S frame: N(R)=366 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR366_A0_S3, 10)

FIELD(RR_NR367_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf= 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x85, 0xbc, /* S frame: N(R)=367 A=0 RR */

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        0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR367_A0_S3, 10)

FIELD(RR_NR368_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xc0,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
                                    /* PD = 0, C/R = 0, SAPI = 3 */
                                    /* S frame: N(R)=368 A=0 RR */
ENDFIELD(RR_NR368_A0_S3, 10)                /* FCS: 0x00 for sim. */

FIELD(RR_NR369_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xc4,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
                                    /* PD = 0, C/R = 0, SAPI = 3 */
                                    /* S frame: N(R)=369 A=0 RR */
ENDFIELD(RR_NR369_A0_S3, 10)                /* FCS: 0x00 for sim. */

FIELD(RR_NR370_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xc8,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
                                    /* PD = 0, C/R = 0, SAPI = 3 */
                                    /* S frame: N(R)=370 A=0 RR */
ENDFIELD(RR_NR370_A0_S3, 10)                /* FCS: 0x00 for sim. */

FIELD(RR_NR371_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xcc,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
                                    /* PD = 0, C/R = 0, SAPI = 3 */
                                    /* S frame: N(R)=371 A=0 RR */
ENDFIELD(RR_NR371_A0_S3, 10)                /* FCS: 0x00 for sim. */

FIELD(RR_NR372_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xd0,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
                                    /* PD = 0, C/R = 0, SAPI = 3 */
                                    /* S frame: N(R)=372 A=0 RR */
ENDFIELD(RR_NR372_A0_S3, 10)                /* FCS: 0x00 for sim. */

FIELD(RR_NR373_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xd4,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
                                    /* PD = 0, C/R = 0, SAPI = 3 */
                                    /* S frame: N(R)=373 A=0 RR */
ENDFIELD(RR_NR373_A0_S3, 10)                /* FCS: 0x00 for sim. */

FIELD(RR_NR374_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xd8,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
                                    /* PD = 0, C/R = 0, SAPI = 3 */
                                    /* S frame: N(R)=374 A=0 RR */
ENDFIELD(RR_NR374_A0_S3, 10)                /* FCS: 0x00 for sim. */

FIELD(RR_NR375_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xdc,
    0x00, 0x00, 0x00                /* sdu.l_buf = 48, sdu.o_buf = 0 */
                                    /* PD = 0, C/R = 0, SAPI = 3 */
                                    /* S frame: N(R)=375 A=0 RR */
ENDFIELD(RR_NR375_A0_S3, 10)                /* FCS: 0x00 for sim. */

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FIELD(RR_NR376_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xe0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR376_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=376 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR377_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xe4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR377_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=377 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR378_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xe8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR378_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=378 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR379_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xec,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR379_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=379 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR380_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xf0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR380_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=380 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR381_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xf4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR381_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=381 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR382_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xf8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR382_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=382 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR383_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x85, 0xfc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR383_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=383 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR384_A0_S3)

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        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0x00,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR384_A0_S3, 10)

FIELD(RR_NR385_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0x04,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR385_A0_S3, 10)

FIELD(RR_NR386_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0x08,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR386_A0_S3, 10)

FIELD(RR_NR387_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0x0c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR387_A0_S3, 10)

FIELD(RR_NR388_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0x10,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR388_A0_S3, 10)

FIELD(RR_NR389_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0x14,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR389_A0_S3, 10)

FIELD(RR_NR390_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0x18,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR390_A0_S3, 10)

FIELD(RR_NR391_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0x1c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR391_A0_S3, 10)

FIELD(RR_NR392_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,

```

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=384 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=385 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=386 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=387 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=388 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=389 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=390 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=391 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*

```

        0x86, 0x20,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR392_A0_S3, 10)

FIELD(RR_NR393_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x24,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR393_A0_S3, 10)

FIELD(RR_NR394_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x28,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR394_A0_S3, 10)

FIELD(RR_NR395_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x2c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR395_A0_S3, 10)

FIELD(RR_NR396_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x30,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR396_A0_S3, 10)

FIELD(RR_NR397_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x34,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR397_A0_S3, 10)

FIELD(RR_NR398_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x38,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR398_A0_S3, 10)

FIELD(RR_NR399_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x3c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR399_A0_S3, 10)

FIELD(RR_NR400_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x40,
    0x00, 0x00, 0x00

```

/* S frame: N(R)=392 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=393 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=394 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=395 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=396 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=397 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=398 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=399 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=400 A=0 RR */
/* FCS: 0x00 for sim. */

ENDFIELD(RR_NR400_A0_S3, 10)

FIELD(RR_NR401_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x86, 0x44,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=401 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR401_A0_S3, 10)

FIELD(RR_NR402_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x86, 0x48,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=402 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR402_A0_S3, 10)

FIELD(RR_NR403_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x86, 0x4c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=403 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR403_A0_S3, 10)

FIELD(RR_NR404_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x86, 0x50,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=404 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR404_A0_S3, 10)

FIELD(RR_NR405_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x86, 0x54,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=405 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR405_A0_S3, 10)

FIELD(RR_NR406_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x86, 0x58,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=406 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR406_A0_S3, 10)

FIELD(RR_NR407_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x86, 0x5c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=407 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR407_A0_S3, 10)

FIELD(RR_NR408_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x86, 0x60,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=408 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR408_A0_S3, 10)

```

FIELD(RR_NR409_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x64,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR409_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=409 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR410_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x68,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR410_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=410 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR411_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x6c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR411_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=411 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR412_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x70,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR412_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=412 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR413_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x74,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR413_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=413 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR414_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x78,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR414_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=414 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR415_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x7c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR415_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=415 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR416_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0x80,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR416_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=416 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR417_A0_S3)
    0x30, 0x00, 0x00, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x86, 0x84, /* S frame: N(R)=417 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR417_A0_S3, 10)

FIELD(RR_NR418_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x86, 0x88, /* S frame: N(R)=418 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR418_A0_S3, 10)

FIELD(RR_NR419_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x86, 0x8c, /* S frame: N(R)=419 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR419_A0_S3, 10)

FIELD(RR_NR420_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x86, 0x90, /* S frame: N(R)=420 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR420_A0_S3, 10)

FIELD(RR_NR421_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x86, 0x94, /* S frame: N(R)=421 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR421_A0_S3, 10)

FIELD(RR_NR422_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x86, 0x98, /* S frame: N(R)=422 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR422_A0_S3, 10)

FIELD(RR_NR423_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x86, 0x9c, /* S frame: N(R)=423 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR423_A0_S3, 10)

FIELD(RR_NR424_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x86, 0xa0, /* S frame: N(R)=424 A=0 RR */
0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR424_A0_S3, 10)

FIELD(RR_NR425_A0_S3)
0x30, 0x00, 0x00, 0x00, /* sdu.l_buf = 48, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x86, 0xa4, /* S frame: N(R)=425 A=0 RR */

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        0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR425_A0_S3, 10)

FIELD(RR_NR426_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x86, 0xa8,                       /* S frame: N(R)=426 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR426_A0_S3, 10)

FIELD(RR_NR427_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x86, 0xac,                       /* S frame: N(R)=427 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR427_A0_S3, 10)

FIELD(RR_NR428_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x86, 0xb0,                       /* S frame: N(R)=428 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR428_A0_S3, 10)

FIELD(RR_NR429_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x86, 0xb4,                       /* S frame: N(R)=429 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR429_A0_S3, 10)

FIELD(RR_NR430_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x86, 0xb8,                       /* S frame: N(R)=430 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR430_A0_S3, 10)

FIELD(RR_NR431_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x86, 0xbc,                       /* S frame: N(R)=431 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR431_A0_S3, 10)

FIELD(RR_NR432_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x86, 0xc0,                       /* S frame: N(R)=432 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR432_A0_S3, 10)

FIELD(RR_NR433_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x86, 0xc4,                       /* S frame: N(R)=433 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR433_A0_S3, 10)

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FIELD(RR_NR434_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0xc8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR434_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=434 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR435_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0xcc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR435_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=435 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR436_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0xd0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR436_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=436 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR437_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0xd4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR437_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=437 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR438_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0xd8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR438_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=438 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR439_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0xdc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR439_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=439 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR440_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0xe0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR440_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=440 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR441_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x86, 0xe4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR441_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=441 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR442_A0_S3)
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        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0xe8,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR442_A0_S3, 10)

FIELD(RR_NR443_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0xec,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR443_A0_S3, 10)

FIELD(RR_NR444_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0xf0,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR444_A0_S3, 10)

FIELD(RR_NR445_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0xf4,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR445_A0_S3, 10)

FIELD(RR_NR446_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0xf8,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR446_A0_S3, 10)

FIELD(RR_NR447_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x86, 0xfc,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR447_A0_S3, 10)

FIELD(RR_NR448_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0x00,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR448_A0_S3, 10)

FIELD(RR_NR449_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0x04,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR449_A0_S3, 10)

FIELD(RR_NR450_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,

```

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=442 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=443 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=444 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=445 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=446 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=447 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=448 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=449 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*

```

        0x87, 0x08,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR450_A0_S3, 10)

FIELD(RR_NR451_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x0c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR451_A0_S3, 10)

FIELD(RR_NR452_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x10,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR452_A0_S3, 10)

FIELD(RR_NR453_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x14,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR453_A0_S3, 10)

FIELD(RR_NR454_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x18,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR454_A0_S3, 10)

FIELD(RR_NR455_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x1c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR455_A0_S3, 10)

FIELD(RR_NR456_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x20,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR456_A0_S3, 10)

FIELD(RR_NR457_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x24,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR457_A0_S3, 10)

FIELD(RR_NR458_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x28,
    0x00, 0x00, 0x00

```

/* S frame: N(R)=450 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=451 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=452 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=453 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=454 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=455 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=456 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=457 A=0 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=458 A=0 RR */
/* FCS: 0x00 for sim. */

ENDFIELD(RR_NR458_A0_S3, 10)

FIELD(RR_NR459_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x87, 0x2c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=459 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR459_A0_S3, 10)

FIELD(RR_NR460_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x87, 0x30,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=460 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR460_A0_S3, 10)

FIELD(RR_NR461_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x87, 0x34,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=461 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR461_A0_S3, 10)

FIELD(RR_NR462_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x87, 0x38,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=462 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR462_A0_S3, 10)

FIELD(RR_NR463_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x87, 0x3c,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=463 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR463_A0_S3, 10)

FIELD(RR_NR464_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x87, 0x40,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=464 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR464_A0_S3, 10)

FIELD(RR_NR465_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x87, 0x44,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=465 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR465_A0_S3, 10)

FIELD(RR_NR466_A0_S3)

0x30, 0x00, 0x00, 0x00,
0x03,
0x87, 0x48,
0x00, 0x00, 0x00

/ sdu.l_buf = 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=466 A=0 RR */
/* FCS: 0x00 for sim. */*

ENDFIELD(RR_NR466_A0_S3, 10)

```

FIELD(RR_NR467_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x4c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR467_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=467 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR468_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x50,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR468_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=468 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR469_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x54,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR469_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=469 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR470_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x58,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR470_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=470 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR471_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x5c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR471_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=471 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR472_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x60,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR472_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=472 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR473_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x64,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR473_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=473 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR474_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x68,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR474_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=474 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR475_A0_S3)
    0x30, 0x00, 0x00, 0x00,

```

```

        0x03,
        0x87, 0x6c,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR475_A0_S3, 10)

FIELD(RR_NR476_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x70,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR476_A0_S3, 10)

FIELD(RR_NR477_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x74,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR477_A0_S3, 10)

FIELD(RR_NR478_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x78,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR478_A0_S3, 10)

FIELD(RR_NR479_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x7c,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR479_A0_S3, 10)

FIELD(RR_NR480_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x80,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR480_A0_S3, 10)

FIELD(RR_NR481_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x84,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR481_A0_S3, 10)

FIELD(RR_NR482_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x88,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR482_A0_S3, 10)

FIELD(RR_NR483_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0x8c,

```

/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=475 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=476 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=477 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=478 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=479 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=480 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=481 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=482 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=483 A=0 RR */*

```

        0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR483_A0_S3, 10)

FIELD(RR_NR484_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x87, 0x90,                       /* S frame: N(R)=484 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR484_A0_S3, 10)

FIELD(RR_NR485_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x87, 0x94,                       /* S frame: N(R)=485 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR485_A0_S3, 10)

FIELD(RR_NR486_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x87, 0x98,                       /* S frame: N(R)=486 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR486_A0_S3, 10)

FIELD(RR_NR487_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x87, 0x9c,                       /* S frame: N(R)=487 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR487_A0_S3, 10)

FIELD(RR_NR488_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x87, 0xa0,                       /* S frame: N(R)=488 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR488_A0_S3, 10)

FIELD(RR_NR489_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x87, 0xa4,                       /* S frame: N(R)=489 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR489_A0_S3, 10)

FIELD(RR_NR490_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x87, 0xa8,                       /* S frame: N(R)=490 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR490_A0_S3, 10)

FIELD(RR_NR491_A0_S3)
    0x30, 0x00, 0x00, 0x00,          /* sdu.l_buf = 48, sdu.o_buf = 0 */
    0x03,                             /* PD = 0, C/R = 0, SAPI = 3 */
    0x87, 0xac,                       /* S frame: N(R)=491 A=0 RR */
    0x00, 0x00, 0x00                /* FCS: 0x00 for sim. */
ENDFIELD(RR_NR491_A0_S3, 10)

```

```
FIELD(RR_NR492_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0xb0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR492_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=492 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR493_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0xb4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR493_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=493 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR494_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0xb8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR494_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=494 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR495_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0xbc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR495_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=495 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR496_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0xc0,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR496_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=496 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR497_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0xc4,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR497_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=497 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR498_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0xc8,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR498_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=498 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR499_A0_S3)
    0x30, 0x00, 0x00, 0x00,
    0x03,
    0x87, 0xcc,
    0x00, 0x00, 0x00
ENDFIELD(RR_NR499_A0_S3, 10)
/* sdu.l_buf= 48, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* S frame: N(R)=499 A=0 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_NR500_A0_S3)
```

```

        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xd0,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR500_A0_S3, 10)

FIELD(RR_NR501_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xd4,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR501_A0_S3, 10)

FIELD(RR_NR502_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xd8,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR502_A0_S3, 10)

FIELD(RR_NR503_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xdc,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR503_A0_S3, 10)

FIELD(RR_NR504_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xe0,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR504_A0_S3, 10)

FIELD(RR_NR505_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xe4,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR505_A0_S3, 10)

FIELD(RR_NR506_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xe8,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR506_A0_S3, 10)

FIELD(RR_NR507_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xec,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR507_A0_S3, 10)

FIELD(RR_NR508_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,

```

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=500 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=501 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=502 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=503 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=504 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=505 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=506 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ S frame: N(R)=507 A=0 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 48, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*

```

        0x87, 0xf0,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR508_A0_S3, 10)

FIELD(RR_NR509_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xf4,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR509_A0_S3, 10)

FIELD(RR_NR510_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xf8,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR510_A0_S3, 10)

FIELD(RR_NR511_A0_S3)
        0x30, 0x00, 0x00, 0x00,
        0x03,
        0x87, 0xfc,
        0x00, 0x00, 0x00
ENDFIELD(RR_NR511_A0_S3, 10)
/*

```

2.2.5.2 GRR-I-Frames, RR, N(R)=0, 3 Byte L3 Data

*/

```

FIELD(RR_D3_NS0_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x40, 0x00, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS0_NR0_A1_S3, 14)

FIELD(RR_D3_NS1_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x40, 0x10, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS1_NR0_A1_S3, 14)

FIELD(RR_D3_NS2_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x40, 0x20, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS2_NR0_A1_S3, 14)

FIELD(RR_D3_NS3_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x40, 0x30, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS3_NR0_A1_S3, 14)

FIELD(RR_D3_NS4_NR0_A1_S3)

```

```

0x50, 0x00, 0x00, 0x00,
0x03,
0x40, 0x40, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS4_NR0_A1_S3, 14)

FIELD(RR_D3_NS5_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x40, 0x50, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS5_NR0_A1_S3, 14)

FIELD(RR_D3_NS6_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x40, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS6_NR0_A1_S3, 14)

FIELD(RR_D3_NS7_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x40, 0x70, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS7_NR0_A1_S3, 14)

FIELD(RR_D3_NS8_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x40, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS8_NR0_A1_S3, 14)

FIELD(RR_D3_NS9_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x40, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS9_NR0_A1_S3, 14)

FIELD(RR_D3_NS10_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x40, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS10_NR0_A1_S3, 14)

FIELD(RR_D3_NS11_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x40, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS11_NR0_A1_S3, 14)

FIELD(RR_D3_NS12_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,

```

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=4 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=5 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=6 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=7 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=8 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=9 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=10 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=11 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*

```

        0x40, 0xc0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS12_NR0_A1_S3, 14)

FIELD(RR_D3_NS13_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x40, 0xd0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS13_NR0_A1_S3, 14)

FIELD(RR_D3_NS14_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x40, 0xe0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS14_NR0_A1_S3, 14)

FIELD(RR_D3_NS15_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x40, 0xf0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS15_NR0_A1_S3, 14)

FIELD(RR_D3_NS16_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x41, 0x00, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS16_NR0_A1_S3, 14)

FIELD(RR_D3_NS17_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x41, 0x10, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS17_NR0_A1_S3, 14)

FIELD(RR_D3_NS18_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x41, 0x20, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS18_NR0_A1_S3, 14)

FIELD(RR_D3_NS19_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x41, 0x30, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS19_NR0_A1_S3, 14)

FIELD(RR_D3_NS20_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x41, 0x40, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00

```

/ I frame: N(S)=12 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=13 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=14 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=15 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=16 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=17 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=18 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=19 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=20 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS20_NR0_A1_S3, 14)

FIELD(RR_D3_NS21_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,
0x03,

0x41, 0x50, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

ENDFIELD(RR_D3_NS21_NR0_A1_S3, 14)

/ sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=21 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

FIELD(RR_D3_NS22_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,
0x03,

0x41, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

ENDFIELD(RR_D3_NS22_NR0_A1_S3, 14)

/ sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=22 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

FIELD(RR_D3_NS23_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,
0x03,

0x41, 0x70, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

ENDFIELD(RR_D3_NS23_NR0_A1_S3, 14)

/ sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=23 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

FIELD(RR_D3_NS24_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,
0x03,

0x41, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

ENDFIELD(RR_D3_NS24_NR0_A1_S3, 14)

/ sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=24 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

FIELD(RR_D3_NS25_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,
0x03,

0x41, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

ENDFIELD(RR_D3_NS25_NR0_A1_S3, 14)

/ sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=25 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

FIELD(RR_D3_NS26_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,
0x03,

0x41, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

ENDFIELD(RR_D3_NS26_NR0_A1_S3, 14)

/ sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=26 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

FIELD(RR_D3_NS27_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,
0x03,

0x41, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

ENDFIELD(RR_D3_NS27_NR0_A1_S3, 14)

/ sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=27 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

FIELD(RR_D3_NS28_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,
0x03,

0x41, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

ENDFIELD(RR_D3_NS28_NR0_A1_S3, 14)

/ sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=28 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

```

FIELD(RR_D3_NS29_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x41, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS29_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=29 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS30_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x41, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS30_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=30 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS31_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x41, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS31_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=31 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS32_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x42, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS32_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=32 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS33_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x42, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS33_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=33 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS34_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x42, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS34_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=34 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS35_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x42, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS35_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=35 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS36_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x42, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS36_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=36 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS37_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x42, 0x50, 0x00, /* I frame: N(S)=37 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS37_NR0_A1_S3, 14)

FIELD(RR_D3_NS38_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x42, 0x60, 0x00, /* I frame: N(S)=38 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS38_NR0_A1_S3, 14)

FIELD(RR_D3_NS39_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x42, 0x70, 0x00, /* I frame: N(S)=39 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS39_NR0_A1_S3, 14)

FIELD(RR_D3_NS40_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x42, 0x80, 0x00, /* I frame: N(S)=40 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS40_NR0_A1_S3, 14)

FIELD(RR_D3_NS41_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x42, 0x90, 0x00, /* I frame: N(S)=41 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS41_NR0_A1_S3, 14)

FIELD(RR_D3_NS42_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x42, 0xa0, 0x00, /* I frame: N(S)=42 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS42_NR0_A1_S3, 14)

FIELD(RR_D3_NS43_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x42, 0xb0, 0x00, /* I frame: N(S)=43 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS43_NR0_A1_S3, 14)

FIELD(RR_D3_NS44_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x42, 0xc0, 0x00, /* I frame: N(S)=44 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS44_NR0_A1_S3, 14)

FIELD(RR_D3_NS45_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x42, 0xd0, 0x00, /* I frame: N(S)=45 N(R)=0 A=1 RR */

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        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS45_NR0_A1_S3, 14)

FIELD(RR_D3_NS46_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x42, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS46_NR0_A1_S3, 14)

FIELD(RR_D3_NS47_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x42, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS47_NR0_A1_S3, 14)

FIELD(RR_D3_NS48_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS48_NR0_A1_S3, 14)

FIELD(RR_D3_NS49_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS49_NR0_A1_S3, 14)

FIELD(RR_D3_NS50_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS50_NR0_A1_S3, 14)

FIELD(RR_D3_NS51_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS51_NR0_A1_S3, 14)

FIELD(RR_D3_NS52_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS52_NR0_A1_S3, 14)

FIELD(RR_D3_NS53_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS53_NR0_A1_S3, 14)

```

/ FCS: 0x00 for sim. */*
/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=46 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=47 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=48 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=49 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=50 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=51 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=52 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=53 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

```

FIELD(RR_D3_NS54_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS54_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=54 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS55_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS55_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=55 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS56_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS56_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=56 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS57_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS57_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=57 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS58_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS58_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=58 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS59_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS59_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=59 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS60_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS60_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=60 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS61_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x43, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS61_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=61 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS62_NR0_A1_S3)

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0x50, 0x00, 0x00, 0x00,
0x03,
0x43, 0xe0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS62_NR0_A1_S3, 14)

FIELD(RR_D3_NS63_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x43, 0xf0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS63_NR0_A1_S3, 14)

FIELD(RR_D3_NS64_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0x00, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS64_NR0_A1_S3, 14)

FIELD(RR_D3_NS65_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0x10, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS65_NR0_A1_S3, 14)

FIELD(RR_D3_NS66_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0x20, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS66_NR0_A1_S3, 14)

FIELD(RR_D3_NS67_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0x30, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS67_NR0_A1_S3, 14)

FIELD(RR_D3_NS68_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0x40, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS68_NR0_A1_S3, 14)

FIELD(RR_D3_NS69_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0x50, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS69_NR0_A1_S3, 14)

FIELD(RR_D3_NS70_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,

```

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=62 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=63 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=64 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=65 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=66 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=67 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=68 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=69 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

```

0x44, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS70_NR0_A1_S3, 14)

FIELD(RR_D3_NS71_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0x70, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS71_NR0_A1_S3, 14)

FIELD(RR_D3_NS72_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS72_NR0_A1_S3, 14)

FIELD(RR_D3_NS73_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS73_NR0_A1_S3, 14)

FIELD(RR_D3_NS74_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS74_NR0_A1_S3, 14)

FIELD(RR_D3_NS75_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS75_NR0_A1_S3, 14)

FIELD(RR_D3_NS76_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS76_NR0_A1_S3, 14)

FIELD(RR_D3_NS77_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0xd0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS77_NR0_A1_S3, 14)

FIELD(RR_D3_NS78_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x44, 0xe0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

```

/ I frame: N(S)=70 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=71 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=72 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=73 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=74 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=75 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=76 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=77 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=78 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS78_NR0_A1_S3, 14)

FIELD(RR_D3_NS79_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,

0x03,

0x44, 0xf0, 0x00,

0x42, 0x43, 0x44, 0x00, 0x00, 0x00

/ sdu.l_buf = 80, sdu.o_buf = 0 */*

/ PD = 0, C/R = 0, SAPI = 3 */*

/ I frame: N(S)=79 N(R)=0 A=1 RR */*

/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS79_NR0_A1_S3, 14)

FIELD(RR_D3_NS80_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,

0x03,

0x45, 0x00, 0x00,

0x42, 0x43, 0x44, 0x00, 0x00, 0x00

/ sdu.l_buf = 80, sdu.o_buf = 0 */*

/ PD = 0, C/R = 0, SAPI = 3 */*

/ I frame: N(S)=80 N(R)=0 A=1 RR */*

/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS80_NR0_A1_S3, 14)

FIELD(RR_D3_NS81_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,

0x03,

0x45, 0x10, 0x00,

0x42, 0x43, 0x44, 0x00, 0x00, 0x00

/ sdu.l_buf = 80, sdu.o_buf = 0 */*

/ PD = 0, C/R = 0, SAPI = 3 */*

/ I frame: N(S)=81 N(R)=0 A=1 RR */*

/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS81_NR0_A1_S3, 14)

FIELD(RR_D3_NS82_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,

0x03,

0x45, 0x20, 0x00,

0x42, 0x43, 0x44, 0x00, 0x00, 0x00

/ sdu.l_buf = 80, sdu.o_buf = 0 */*

/ PD = 0, C/R = 0, SAPI = 3 */*

/ I frame: N(S)=82 N(R)=0 A=1 RR */*

/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS82_NR0_A1_S3, 14)

FIELD(RR_D3_NS83_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,

0x03,

0x45, 0x30, 0x00,

0x42, 0x43, 0x44, 0x00, 0x00, 0x00

/ sdu.l_buf = 80, sdu.o_buf = 0 */*

/ PD = 0, C/R = 0, SAPI = 3 */*

/ I frame: N(S)=83 N(R)=0 A=1 RR */*

/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS83_NR0_A1_S3, 14)

FIELD(RR_D3_NS84_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,

0x03,

0x45, 0x40, 0x00,

0x42, 0x43, 0x44, 0x00, 0x00, 0x00

/ sdu.l_buf = 80, sdu.o_buf = 0 */*

/ PD = 0, C/R = 0, SAPI = 3 */*

/ I frame: N(S)=84 N(R)=0 A=1 RR */*

/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS84_NR0_A1_S3, 14)

FIELD(RR_D3_NS85_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,

0x03,

0x45, 0x50, 0x00,

0x42, 0x43, 0x44, 0x00, 0x00, 0x00

/ sdu.l_buf = 80, sdu.o_buf = 0 */*

/ PD = 0, C/R = 0, SAPI = 3 */*

/ I frame: N(S)=85 N(R)=0 A=1 RR */*

/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS85_NR0_A1_S3, 14)

FIELD(RR_D3_NS86_NR0_A1_S3)

0x50, 0x00, 0x00, 0x00,

0x03,

0x45, 0x60, 0x00,

0x42, 0x43, 0x44, 0x00, 0x00, 0x00

/ sdu.l_buf = 80, sdu.o_buf = 0 */*

/ PD = 0, C/R = 0, SAPI = 3 */*

/ I frame: N(S)=86 N(R)=0 A=1 RR */*

/ FCS: 0x00 for sim. */*

ENDFIELD(RR_D3_NS86_NR0_A1_S3, 14)

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FIELD(RR_D3_NS87_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x45, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS87_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=87 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS88_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x45, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS88_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=88 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS89_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x45, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS89_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=89 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS90_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x45, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS90_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=90 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS91_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x45, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS91_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=91 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS92_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x45, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS92_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=92 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS93_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x45, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS93_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=93 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS94_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x45, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS94_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=94 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS95_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,

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```

0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x45, 0x0f, 0x00, /* I frame: N(S)=95 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS95_NR0_A1_S3, 14)

FIELD(RR_D3_NS96_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x46, 0x00, 0x00, /* I frame: N(S)=96 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS96_NR0_A1_S3, 14)

FIELD(RR_D3_NS97_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x46, 0x10, 0x00, /* I frame: N(S)=97 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS97_NR0_A1_S3, 14)

FIELD(RR_D3_NS98_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x46, 0x20, 0x00, /* I frame: N(S)=98 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS98_NR0_A1_S3, 14)

FIELD(RR_D3_NS99_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x46, 0x30, 0x00, /* I frame: N(S)=99 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS99_NR0_A1_S3, 14)

FIELD(RR_D3_NS100_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x46, 0x40, 0x00, /* I frame: N(S)=100 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS100_NR0_A1_S3, 14)

FIELD(RR_D3_NS101_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x46, 0x50, 0x00, /* I frame: N(S)=101 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS101_NR0_A1_S3, 14)

FIELD(RR_D3_NS102_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x46, 0x60, 0x00, /* I frame: N(S)=102 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS102_NR0_A1_S3, 14)

FIELD(RR_D3_NS103_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x46, 0x70, 0x00, /* I frame: N(S)=103 N(R)=0 A=1 RR */

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0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS103_NR0_A1_S3, 14)

FIELD(RR_D3_NS104_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x46, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS104_NR0_A1_S3, 14)

FIELD(RR_D3_NS105_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x46, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS105_NR0_A1_S3, 14)

FIELD(RR_D3_NS106_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x46, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS106_NR0_A1_S3, 14)

FIELD(RR_D3_NS107_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x46, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS107_NR0_A1_S3, 14)

FIELD(RR_D3_NS108_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x46, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS108_NR0_A1_S3, 14)

FIELD(RR_D3_NS109_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x46, 0xd0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS109_NR0_A1_S3, 14)

FIELD(RR_D3_NS110_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x46, 0xe0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS110_NR0_A1_S3, 14)

FIELD(RR_D3_NS111_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x46, 0xf0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS111_NR0_A1_S3, 14)

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FIELD(RR_D3_NS112_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x47, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS112_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=112 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS113_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x47, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS113_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=113 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS114_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x47, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS114_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=114 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS115_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x47, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS115_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=115 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS116_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x47, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS116_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=116 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS117_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x47, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS117_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=117 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS118_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x47, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS118_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=118 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS119_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x47, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS119_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=119 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS120_NR0_A1_S3)

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0x50, 0x00, 0x00, 0x00,
0x03,
0x47, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS120_NR0_A1_S3, 14)

FIELD(RR_D3_NS121_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x47, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS121_NR0_A1_S3, 14)

FIELD(RR_D3_NS122_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x47, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS122_NR0_A1_S3, 14)

FIELD(RR_D3_NS123_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x47, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS123_NR0_A1_S3, 14)

FIELD(RR_D3_NS124_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x47, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS124_NR0_A1_S3, 14)

FIELD(RR_D3_NS125_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x47, 0xd0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS125_NR0_A1_S3, 14)

FIELD(RR_D3_NS126_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x47, 0xe0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS126_NR0_A1_S3, 14)

FIELD(RR_D3_NS127_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x47, 0xf0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS127_NR0_A1_S3, 14)

FIELD(RR_D3_NS128_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=120 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=121 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=122 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=123 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=124 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=125 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=126 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=127 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

```

0x48, 0x00, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS128_NR0_A1_S3, 14)

FIELD(RR_D3_NS129_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x48, 0x10, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS129_NR0_A1_S3, 14)

FIELD(RR_D3_NS130_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x48, 0x20, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS130_NR0_A1_S3, 14)

FIELD(RR_D3_NS131_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x48, 0x30, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS131_NR0_A1_S3, 14)

FIELD(RR_D3_NS132_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x48, 0x40, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS132_NR0_A1_S3, 14)

FIELD(RR_D3_NS133_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x48, 0x50, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS133_NR0_A1_S3, 14)

FIELD(RR_D3_NS134_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x48, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS134_NR0_A1_S3, 14)

FIELD(RR_D3_NS135_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x48, 0x70, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS135_NR0_A1_S3, 14)

FIELD(RR_D3_NS136_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x48, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

```

/ I frame: N(S)=128 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=129 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=130 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=131 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=132 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=133 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=134 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=135 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=136 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

```

ENDFIELD(RR_D3_NS136_NR0_A1_S3, 14)

FIELD(RR_D3_NS137_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x48, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS137_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=137 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS138_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x48, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS138_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=138 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS139_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x48, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS139_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=139 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS140_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x48, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS140_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=140 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS141_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x48, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS141_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=141 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS142_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x48, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS142_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=142 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS143_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x48, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS143_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=143 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS144_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS144_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=144 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

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FIELD(RR_D3_NS145_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS145_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=145 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS146_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS146_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=146 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS147_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS147_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=147 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS148_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS148_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=148 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS149_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS149_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=149 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS150_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS150_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=150 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS151_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS151_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=151 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS152_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS152_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=152 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS153_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x49, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS153_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x49, 0x90, 0x00, /* I frame: N(S)=153 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS153_NR0_A1_S3, 14)

FIELD(RR_D3_NS154_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x49, 0xa0, 0x00, /* I frame: N(S)=154 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS154_NR0_A1_S3, 14)

FIELD(RR_D3_NS155_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x49, 0xb0, 0x00, /* I frame: N(S)=155 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS155_NR0_A1_S3, 14)

FIELD(RR_D3_NS156_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x49, 0xc0, 0x00, /* I frame: N(S)=156 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS156_NR0_A1_S3, 14)

FIELD(RR_D3_NS157_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x49, 0xd0, 0x00, /* I frame: N(S)=157 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS157_NR0_A1_S3, 14)

FIELD(RR_D3_NS158_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x49, 0xe0, 0x00, /* I frame: N(S)=158 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS158_NR0_A1_S3, 14)

FIELD(RR_D3_NS159_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x49, 0xf0, 0x00, /* I frame: N(S)=159 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS159_NR0_A1_S3, 14)

FIELD(RR_D3_NS160_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4a, 0x00, 0x00, /* I frame: N(S)=160 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS160_NR0_A1_S3, 14)

FIELD(RR_D3_NS161_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4a, 0x10, 0x00, /* I frame: N(S)=161 N(R)=0 A=1 RR */

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        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS161_NR0_A1_S3, 14)

FIELD(RR_D3_NS162_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS162_NR0_A1_S3, 14)

FIELD(RR_D3_NS163_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS163_NR0_A1_S3, 14)

FIELD(RR_D3_NS164_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS164_NR0_A1_S3, 14)

FIELD(RR_D3_NS165_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS165_NR0_A1_S3, 14)

FIELD(RR_D3_NS166_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS166_NR0_A1_S3, 14)

FIELD(RR_D3_NS167_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS167_NR0_A1_S3, 14)

FIELD(RR_D3_NS168_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS168_NR0_A1_S3, 14)

FIELD(RR_D3_NS169_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS169_NR0_A1_S3, 14)

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FIELD(RR_D3_NS170_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS170_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=170 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS171_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS171_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=171 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS172_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS172_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=172 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS173_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS173_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=173 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS174_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS174_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=174 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS175_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4a, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS175_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=175 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS176_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4b, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS176_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=176 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS177_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4b, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS177_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=177 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS178_NR0_A1_S3)

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0x50, 0x00, 0x00, 0x00,
0x03,
0x4b, 0x20, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS178_NR0_A1_S3, 14)

FIELD(RR_D3_NS179_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4b, 0x30, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS179_NR0_A1_S3, 14)

FIELD(RR_D3_NS180_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4b, 0x40, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS180_NR0_A1_S3, 14)

FIELD(RR_D3_NS181_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4b, 0x50, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS181_NR0_A1_S3, 14)

FIELD(RR_D3_NS182_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4b, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS182_NR0_A1_S3, 14)

FIELD(RR_D3_NS183_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4b, 0x70, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS183_NR0_A1_S3, 14)

FIELD(RR_D3_NS184_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4b, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS184_NR0_A1_S3, 14)

FIELD(RR_D3_NS185_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4b, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS185_NR0_A1_S3, 14)

FIELD(RR_D3_NS186_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=178 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=179 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=180 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=181 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=182 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=183 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=184 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=185 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

```

        0x4b, 0xa0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS186_NR0_A1_S3, 14)

FIELD(RR_D3_NS187_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x4b, 0xb0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS187_NR0_A1_S3, 14)

FIELD(RR_D3_NS188_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x4b, 0xc0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS188_NR0_A1_S3, 14)

FIELD(RR_D3_NS189_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x4b, 0xd0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS189_NR0_A1_S3, 14)

FIELD(RR_D3_NS190_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x4b, 0xe0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS190_NR0_A1_S3, 14)

FIELD(RR_D3_NS191_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x4b, 0xf0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS191_NR0_A1_S3, 14)

FIELD(RR_D3_NS192_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x4c, 0x00, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS192_NR0_A1_S3, 14)

FIELD(RR_D3_NS193_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x4c, 0x10, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS193_NR0_A1_S3, 14)

FIELD(RR_D3_NS194_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x4c, 0x20, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00

```

/ I frame: N(S)=186 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=187 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=188 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=189 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=190 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=191 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=192 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=193 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=194 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */*

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ENDFIELD(RR_D3_NS194_NR0_A1_S3, 14)

FIELD(RR_D3_NS195_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS195_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=195 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS196_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS196_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=196 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS197_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS197_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=197 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS198_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS198_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=198 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS199_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS199_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=199 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS200_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS200_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=200 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS201_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS201_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=201 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS202_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS202_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=202 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

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FIELD(RR_D3_NS203_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS203_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=203 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS204_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS204_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=204 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS205_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS205_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=205 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS206_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS206_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=206 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS207_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4c, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS207_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=207 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS208_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4d, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS208_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=208 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS209_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4d, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS209_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=209 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS210_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4d, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS210_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=210 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS211_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4d, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS211_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4d, 0x30, 0x00, /* I frame: N(S)=211 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS211_NR0_A1_S3, 14)

FIELD(RR_D3_NS212_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4d, 0x40, 0x00, /* I frame: N(S)=212 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS212_NR0_A1_S3, 14)

FIELD(RR_D3_NS213_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4d, 0x50, 0x00, /* I frame: N(S)=213 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS213_NR0_A1_S3, 14)

FIELD(RR_D3_NS214_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4d, 0x60, 0x00, /* I frame: N(S)=214 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS214_NR0_A1_S3, 14)

FIELD(RR_D3_NS215_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4d, 0x70, 0x00, /* I frame: N(S)=215 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS215_NR0_A1_S3, 14)

FIELD(RR_D3_NS216_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4d, 0x80, 0x00, /* I frame: N(S)=216 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS216_NR0_A1_S3, 14)

FIELD(RR_D3_NS217_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4d, 0x90, 0x00, /* I frame: N(S)=217 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS217_NR0_A1_S3, 14)

FIELD(RR_D3_NS218_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4d, 0xa0, 0x00, /* I frame: N(S)=218 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS218_NR0_A1_S3, 14)

FIELD(RR_D3_NS219_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4d, 0xb0, 0x00, /* I frame: N(S)=219 N(R)=0 A=1 RR */

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0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS219_NR0_A1_S3, 14)

FIELD(RR_D3_NS220_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4d, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS220_NR0_A1_S3, 14)

FIELD(RR_D3_NS221_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4d, 0xd0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS221_NR0_A1_S3, 14)

FIELD(RR_D3_NS222_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4d, 0xe0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS222_NR0_A1_S3, 14)

FIELD(RR_D3_NS223_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4d, 0xf0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS223_NR0_A1_S3, 14)

FIELD(RR_D3_NS224_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4e, 0x00, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS224_NR0_A1_S3, 14)

FIELD(RR_D3_NS225_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4e, 0x10, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS225_NR0_A1_S3, 14)

FIELD(RR_D3_NS226_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4e, 0x20, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS226_NR0_A1_S3, 14)

FIELD(RR_D3_NS227_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4e, 0x30, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS227_NR0_A1_S3, 14)

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FIELD(RR_D3_NS228_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4e, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS228_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=228 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS229_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4e, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS229_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=229 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS230_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4e, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS230_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=230 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS231_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4e, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS231_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=231 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS232_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4e, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS232_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=232 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS233_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4e, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS233_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=233 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS234_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4e, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS234_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=234 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS235_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4e, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS235_NR0_A1_S3, 14)
/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=235 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS236_NR0_A1_S3)

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0x50, 0x00, 0x00, 0x00,
0x03,
0x4e, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS236_NR0_A1_S3, 14)

FIELD(RR_D3_NS237_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4e, 0xd0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS237_NR0_A1_S3, 14)

FIELD(RR_D3_NS238_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4e, 0xe0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS238_NR0_A1_S3, 14)

FIELD(RR_D3_NS239_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4e, 0xf0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS239_NR0_A1_S3, 14)

FIELD(RR_D3_NS240_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4f, 0x00, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS240_NR0_A1_S3, 14)

FIELD(RR_D3_NS241_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4f, 0x10, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS241_NR0_A1_S3, 14)

FIELD(RR_D3_NS242_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4f, 0x20, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS242_NR0_A1_S3, 14)

FIELD(RR_D3_NS243_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x4f, 0x30, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS243_NR0_A1_S3, 14)

FIELD(RR_D3_NS244_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,

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/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=236 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=237 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=238 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=239 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=240 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=241 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=242 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=243 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*

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0x4f, 0x40, 0x00, /* I frame: N(S)=244 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS244_NR0_A1_S3, 14)

FIELD(RR_D3_NS245_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4f, 0x50, 0x00, /* I frame: N(S)=245 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS245_NR0_A1_S3, 14)

FIELD(RR_D3_NS246_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4f, 0x60, 0x00, /* I frame: N(S)=246 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS246_NR0_A1_S3, 14)

FIELD(RR_D3_NS247_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4f, 0x70, 0x00, /* I frame: N(S)=247 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS247_NR0_A1_S3, 14)

FIELD(RR_D3_NS248_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4f, 0x80, 0x00, /* I frame: N(S)=248 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS248_NR0_A1_S3, 14)

FIELD(RR_D3_NS249_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4f, 0x90, 0x00, /* I frame: N(S)=249 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS249_NR0_A1_S3, 14)

FIELD(RR_D3_NS250_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4f, 0xa0, 0x00, /* I frame: N(S)=250 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS250_NR0_A1_S3, 14)

FIELD(RR_D3_NS251_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4f, 0xb0, 0x00, /* I frame: N(S)=251 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS251_NR0_A1_S3, 14)

FIELD(RR_D3_NS252_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x4f, 0xc0, 0x00, /* I frame: N(S)=252 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */

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ENDFIELD(RR_D3_NS252_NR0_A1_S3, 14)

FIELD(RR_D3_NS253_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4f, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS253_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=253 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS254_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4f, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS254_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=254 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS255_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x4f, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS255_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=255 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS256_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS256_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=256 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS257_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS257_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=257 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS258_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS258_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=258 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS259_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS259_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=259 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS260_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS260_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=260 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

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FIELD(RR_D3_NS261_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS261_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=261 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS262_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS262_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=262 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS263_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS263_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=263 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS264_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS264_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=264 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS265_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS265_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=265 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS266_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS266_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=266 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS267_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS267_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=267 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS268_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x50, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS268_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=268 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS269_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x50, 0xd0, 0x00, /* I frame: N(S)=269 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS269_NR0_A1_S3, 14)

FIELD(RR_D3_NS270_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x50, 0xe0, 0x00, /* I frame: N(S)=270 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS270_NR0_A1_S3, 14)

FIELD(RR_D3_NS271_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x50, 0xf0, 0x00, /* I frame: N(S)=271 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS271_NR0_A1_S3, 14)

FIELD(RR_D3_NS272_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x51, 0x00, 0x00, /* I frame: N(S)=272 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS272_NR0_A1_S3, 14)

FIELD(RR_D3_NS273_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x51, 0x10, 0x00, /* I frame: N(S)=273 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS273_NR0_A1_S3, 14)

FIELD(RR_D3_NS274_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x51, 0x20, 0x00, /* I frame: N(S)=274 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS274_NR0_A1_S3, 14)

FIELD(RR_D3_NS275_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x51, 0x30, 0x00, /* I frame: N(S)=275 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS275_NR0_A1_S3, 14)

FIELD(RR_D3_NS276_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x51, 0x40, 0x00, /* I frame: N(S)=276 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS276_NR0_A1_S3, 14)

FIELD(RR_D3_NS277_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x51, 0x50, 0x00, /* I frame: N(S)=277 N(R)=0 A=1 RR */

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        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS277_NR0_A1_S3, 14)

FIELD(RR_D3_NS278_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS278_NR0_A1_S3, 14)

FIELD(RR_D3_NS279_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS279_NR0_A1_S3, 14)

FIELD(RR_D3_NS280_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS280_NR0_A1_S3, 14)

FIELD(RR_D3_NS281_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS281_NR0_A1_S3, 14)

FIELD(RR_D3_NS282_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS282_NR0_A1_S3, 14)

FIELD(RR_D3_NS283_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS283_NR0_A1_S3, 14)

FIELD(RR_D3_NS284_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS284_NR0_A1_S3, 14)

FIELD(RR_D3_NS285_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS285_NR0_A1_S3, 14)

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FIELD(RR_D3_NS286_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS286_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=286 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS287_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x51, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS287_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=287 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS288_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x52, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS288_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=288 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS289_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x52, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS289_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=289 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS290_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x52, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS290_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=290 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS291_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x52, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS291_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=291 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS292_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x52, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS292_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=292 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS293_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x52, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS293_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=293 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS294_NR0_A1_S3)

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```

0x50, 0x00, 0x00, 0x00,
0x03,
0x52, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS294_NR0_A1_S3, 14)

FIELD(RR_D3_NS295_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x52, 0x70, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS295_NR0_A1_S3, 14)

FIELD(RR_D3_NS296_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x52, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS296_NR0_A1_S3, 14)

FIELD(RR_D3_NS297_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x52, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS297_NR0_A1_S3, 14)

FIELD(RR_D3_NS298_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x52, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS298_NR0_A1_S3, 14)

FIELD(RR_D3_NS299_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x52, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS299_NR0_A1_S3, 14)

FIELD(RR_D3_NS300_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x52, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS300_NR0_A1_S3, 14)

FIELD(RR_D3_NS301_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x52, 0xd0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS301_NR0_A1_S3, 14)

FIELD(RR_D3_NS302_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=294 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=295 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=296 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=297 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=298 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=299 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=300 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=301 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

```

        0x52, 0xe0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS302_NR0_A1_S3, 14)

FIELD(RR_D3_NS303_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x52, 0xf0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS303_NR0_A1_S3, 14)

FIELD(RR_D3_NS304_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x53, 0x00, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS304_NR0_A1_S3, 14)

FIELD(RR_D3_NS305_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x53, 0x10, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS305_NR0_A1_S3, 14)

FIELD(RR_D3_NS306_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x53, 0x20, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS306_NR0_A1_S3, 14)

FIELD(RR_D3_NS307_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x53, 0x30, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS307_NR0_A1_S3, 14)

FIELD(RR_D3_NS308_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x53, 0x40, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS308_NR0_A1_S3, 14)

FIELD(RR_D3_NS309_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x53, 0x50, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS309_NR0_A1_S3, 14)

FIELD(RR_D3_NS310_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x53, 0x60, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00

```

```

/* I frame: N(S)=302 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=303 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=304 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=305 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=306 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=307 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=308 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=309 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf= 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=310 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

ENDFIELD(RR_D3_NS310_NR0_A1_S3, 14)

FIELD(RR_D3_NS311_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x53, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS311_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=311 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS312_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x53, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS312_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=312 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS313_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x53, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS313_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=313 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS314_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x53, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS314_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=314 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS315_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x53, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS315_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=315 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS316_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x53, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS316_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=316 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS317_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x53, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS317_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=317 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS318_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x53, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS318_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=318 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

FIELD(RR_D3_NS319_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x53, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS319_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=319 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS320_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x54, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS320_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=320 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS321_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x54, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS321_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=321 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS322_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x54, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS322_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=322 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS323_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x54, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS323_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=323 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS324_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x54, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS324_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=324 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS325_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x54, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS325_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=325 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS326_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x54, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS326_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=326 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS327_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,

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```

0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x54, 0x70, 0x00, /* I frame: N(S)=327 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS327_NR0_A1_S3, 14)

FIELD(RR_D3_NS328_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x54, 0x80, 0x00, /* I frame: N(S)=328 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS328_NR0_A1_S3, 14)

FIELD(RR_D3_NS329_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x54, 0x90, 0x00, /* I frame: N(S)=329 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS329_NR0_A1_S3, 14)

FIELD(RR_D3_NS330_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x54, 0xa0, 0x00, /* I frame: N(S)=330 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS330_NR0_A1_S3, 14)

FIELD(RR_D3_NS331_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x54, 0xb0, 0x00, /* I frame: N(S)=331 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS331_NR0_A1_S3, 14)

FIELD(RR_D3_NS332_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x54, 0xc0, 0x00, /* I frame: N(S)=332 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS332_NR0_A1_S3, 14)

FIELD(RR_D3_NS333_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x54, 0xd0, 0x00, /* I frame: N(S)=333 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS333_NR0_A1_S3, 14)

FIELD(RR_D3_NS334_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x54, 0xe0, 0x00, /* I frame: N(S)=334 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS334_NR0_A1_S3, 14)

FIELD(RR_D3_NS335_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x54, 0xf0, 0x00, /* I frame: N(S)=335 N(R)=0 A=1 RR */

```

```

        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS335_NR0_A1_S3, 14)

FIELD(RR_D3_NS336_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS336_NR0_A1_S3, 14)

FIELD(RR_D3_NS337_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS337_NR0_A1_S3, 14)

FIELD(RR_D3_NS338_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS338_NR0_A1_S3, 14)

FIELD(RR_D3_NS339_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS339_NR0_A1_S3, 14)

FIELD(RR_D3_NS340_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS340_NR0_A1_S3, 14)

FIELD(RR_D3_NS341_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS341_NR0_A1_S3, 14)

FIELD(RR_D3_NS342_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS342_NR0_A1_S3, 14)

FIELD(RR_D3_NS343_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS343_NR0_A1_S3, 14)

```

/* FCS: 0x00 for sim. */

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=336 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=337 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=338 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=339 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=340 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=341 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=342 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=343 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

FIELD(RR_D3_NS344_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS344_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=344 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS345_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS345_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=345 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS346_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS346_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=346 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS347_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS347_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=347 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS348_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS348_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=348 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS349_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS349_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=349 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS350_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS350_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=350 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS351_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x55, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS351_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=351 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS352_NR0_A1_S3)

```

```

0x50, 0x00, 0x00, 0x00,
0x03,
0x56, 0x00, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS352_NR0_A1_S3, 14)

FIELD(RR_D3_NS353_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x56, 0x10, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS353_NR0_A1_S3, 14)

FIELD(RR_D3_NS354_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x56, 0x20, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS354_NR0_A1_S3, 14)

FIELD(RR_D3_NS355_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x56, 0x30, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS355_NR0_A1_S3, 14)

FIELD(RR_D3_NS356_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x56, 0x40, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS356_NR0_A1_S3, 14)

FIELD(RR_D3_NS357_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x56, 0x50, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS357_NR0_A1_S3, 14)

FIELD(RR_D3_NS358_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x56, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS358_NR0_A1_S3, 14)

FIELD(RR_D3_NS359_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x56, 0x70, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS359_NR0_A1_S3, 14)

FIELD(RR_D3_NS360_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,

```

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=352 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=353 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=354 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=355 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=356 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=357 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=358 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=359 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*

```

        0x56, 0x80, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS360_NR0_A1_S3, 14)

FIELD(RR_D3_NS361_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x56, 0x90, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS361_NR0_A1_S3, 14)

FIELD(RR_D3_NS362_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x56, 0xa0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS362_NR0_A1_S3, 14)

FIELD(RR_D3_NS363_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x56, 0xb0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS363_NR0_A1_S3, 14)

FIELD(RR_D3_NS364_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x56, 0xc0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS364_NR0_A1_S3, 14)

FIELD(RR_D3_NS365_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x56, 0xd0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS365_NR0_A1_S3, 14)

FIELD(RR_D3_NS366_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x56, 0xe0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS366_NR0_A1_S3, 14)

FIELD(RR_D3_NS367_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x56, 0xf0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS367_NR0_A1_S3, 14)

FIELD(RR_D3_NS368_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x57, 0x00, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00

```

/ I frame: N(S)=360 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=361 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=362 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=363 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=364 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=365 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=366 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=367 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=368 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

```

ENDFIELD(RR_D3_NS368_NR0_A1_S3, 14)

FIELD(RR_D3_NS369_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS369_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=369 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS370_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS370_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=370 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS371_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS371_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=371 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS372_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS372_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=372 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS373_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS373_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=373 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS374_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS374_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=374 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS375_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS375_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=375 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS376_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS376_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=376 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

FIELD(RR_D3_NS377_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS377_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=377 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS378_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS378_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=378 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS379_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS379_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=379 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS380_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS380_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=380 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS381_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS381_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=381 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS382_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS382_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=382 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS383_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x57, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS383_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=383 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS384_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x58, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS384_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=384 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS385_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x58, 0x10, 0x00, /* I frame: N(S)=385 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00, /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS385_NRO_A1_S3, 14)

FIELD(RR_D3_NS386_NRO_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x58, 0x20, 0x00, /* I frame: N(S)=386 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00, /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS386_NRO_A1_S3, 14)

FIELD(RR_D3_NS387_NRO_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x58, 0x30, 0x00, /* I frame: N(S)=387 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00, /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS387_NRO_A1_S3, 14)

FIELD(RR_D3_NS388_NRO_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x58, 0x40, 0x00, /* I frame: N(S)=388 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00, /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS388_NRO_A1_S3, 14)

FIELD(RR_D3_NS389_NRO_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x58, 0x50, 0x00, /* I frame: N(S)=389 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00, /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS389_NRO_A1_S3, 14)

FIELD(RR_D3_NS390_NRO_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x58, 0x60, 0x00, /* I frame: N(S)=390 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00, /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS390_NRO_A1_S3, 14)

FIELD(RR_D3_NS391_NRO_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x58, 0x70, 0x00, /* I frame: N(S)=391 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00, /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS391_NRO_A1_S3, 14)

FIELD(RR_D3_NS392_NRO_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x58, 0x80, 0x00, /* I frame: N(S)=392 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00, /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS392_NRO_A1_S3, 14)

FIELD(RR_D3_NS393_NRO_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x58, 0x90, 0x00, /* I frame: N(S)=393 N(R)=0 A=1 RR */

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0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS393_NR0_A1_S3, 14)

FIELD(RR_D3_NS394_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x58, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS394_NR0_A1_S3, 14)

FIELD(RR_D3_NS395_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x58, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS395_NR0_A1_S3, 14)

FIELD(RR_D3_NS396_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x58, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS396_NR0_A1_S3, 14)

FIELD(RR_D3_NS397_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x58, 0xd0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS397_NR0_A1_S3, 14)

FIELD(RR_D3_NS398_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x58, 0xe0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS398_NR0_A1_S3, 14)

FIELD(RR_D3_NS399_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x58, 0xf0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS399_NR0_A1_S3, 14)

FIELD(RR_D3_NS400_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x59, 0x00, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS400_NR0_A1_S3, 14)

FIELD(RR_D3_NS401_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x59, 0x10, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS401_NR0_A1_S3, 14)

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FIELD(RR_D3_NS402_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x59, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS402_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=402 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS403_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x59, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS403_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=403 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS404_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x59, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS404_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=404 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS405_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x59, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS405_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=405 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS406_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x59, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS406_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=406 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS407_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x59, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS407_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=407 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS408_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x59, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS408_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=408 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS409_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x59, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS409_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=409 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS410_NR0_A1_S3)

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0x50, 0x00, 0x00, 0x00,
0x03,
0x59, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS410_NR0_A1_S3, 14)

FIELD(RR_D3_NS411_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x59, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS411_NR0_A1_S3, 14)

FIELD(RR_D3_NS412_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x59, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS412_NR0_A1_S3, 14)

FIELD(RR_D3_NS413_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x59, 0xd0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS413_NR0_A1_S3, 14)

FIELD(RR_D3_NS414_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x59, 0xe0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS414_NR0_A1_S3, 14)

FIELD(RR_D3_NS415_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x59, 0xf0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS415_NR0_A1_S3, 14)

FIELD(RR_D3_NS416_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5a, 0x00, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS416_NR0_A1_S3, 14)

FIELD(RR_D3_NS417_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5a, 0x10, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS417_NR0_A1_S3, 14)

FIELD(RR_D3_NS418_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,

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/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=410 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=411 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=412 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=413 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=414 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=415 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=416 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=417 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

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0x5a, 0x20, 0x00, /* I frame: N(S)=418 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS418_NR0_A1_S3, 14)

FIELD(RR_D3_NS419_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5a, 0x30, 0x00, /* I frame: N(S)=419 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS419_NR0_A1_S3, 14)

FIELD(RR_D3_NS420_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5a, 0x40, 0x00, /* I frame: N(S)=420 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS420_NR0_A1_S3, 14)

FIELD(RR_D3_NS421_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5a, 0x50, 0x00, /* I frame: N(S)=421 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS421_NR0_A1_S3, 14)

FIELD(RR_D3_NS422_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5a, 0x60, 0x00, /* I frame: N(S)=422 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS422_NR0_A1_S3, 14)

FIELD(RR_D3_NS423_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5a, 0x70, 0x00, /* I frame: N(S)=423 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS423_NR0_A1_S3, 14)

FIELD(RR_D3_NS424_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5a, 0x80, 0x00, /* I frame: N(S)=424 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS424_NR0_A1_S3, 14)

FIELD(RR_D3_NS425_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5a, 0x90, 0x00, /* I frame: N(S)=425 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS425_NR0_A1_S3, 14)

FIELD(RR_D3_NS426_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf= 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5a, 0xa0, 0x00, /* I frame: N(S)=426 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */

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ENDFIELD(RR_D3_NS426_NR0_A1_S3, 14)

FIELD(RR_D3_NS427_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5a, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS427_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=427 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS428_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5a, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS428_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=428 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS429_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5a, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS429_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=429 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS430_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5a, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS430_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=430 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS431_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5a, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS431_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=431 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS432_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS432_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=432 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS433_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS433_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=433 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS434_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS434_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=434 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

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FIELD(RR_D3_NS435_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS435_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=435 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS436_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS436_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=436 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS437_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS437_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=437 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS438_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS438_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=438 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS439_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS439_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=439 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS440_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS440_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=440 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS441_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS441_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=441 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS442_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5b, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS442_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=442 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS443_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,

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0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5b, 0xb0, 0x00, /* I frame: N(S)=443 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS443_NR0_A1_S3, 14)

FIELD(RR_D3_NS444_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5b, 0xc0, 0x00, /* I frame: N(S)=444 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS444_NR0_A1_S3, 14)

FIELD(RR_D3_NS445_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5b, 0xd0, 0x00, /* I frame: N(S)=445 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS445_NR0_A1_S3, 14)

FIELD(RR_D3_NS446_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5b, 0xe0, 0x00, /* I frame: N(S)=446 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS446_NR0_A1_S3, 14)

FIELD(RR_D3_NS447_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5b, 0xf0, 0x00, /* I frame: N(S)=447 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS447_NR0_A1_S3, 14)

FIELD(RR_D3_NS448_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5c, 0x00, 0x00, /* I frame: N(S)=448 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS448_NR0_A1_S3, 14)

FIELD(RR_D3_NS449_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5c, 0x10, 0x00, /* I frame: N(S)=449 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS449_NR0_A1_S3, 14)

FIELD(RR_D3_NS450_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5c, 0x20, 0x00, /* I frame: N(S)=450 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS450_NR0_A1_S3, 14)

FIELD(RR_D3_NS451_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5c, 0x30, 0x00, /* I frame: N(S)=451 N(R)=0 A=1 RR */

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0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS451_NR0_A1_S3, 14)

FIELD(RR_D3_NS452_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5c, 0x40, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS452_NR0_A1_S3, 14)

FIELD(RR_D3_NS453_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5c, 0x50, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS453_NR0_A1_S3, 14)

FIELD(RR_D3_NS454_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5c, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS454_NR0_A1_S3, 14)

FIELD(RR_D3_NS455_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5c, 0x70, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS455_NR0_A1_S3, 14)

FIELD(RR_D3_NS456_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5c, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS456_NR0_A1_S3, 14)

FIELD(RR_D3_NS457_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5c, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS457_NR0_A1_S3, 14)

FIELD(RR_D3_NS458_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5c, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS458_NR0_A1_S3, 14)

FIELD(RR_D3_NS459_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5c, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS459_NR0_A1_S3, 14)

/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=452 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=453 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=454 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=455 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=456 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=457 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=458 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=459 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

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FIELD(RR_D3_NS460_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5c, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS460_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=460 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS461_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5c, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS461_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=461 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS462_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5c, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS462_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=462 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS463_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5c, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS463_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=463 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS464_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5d, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS464_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=464 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS465_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5d, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS465_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=465 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS466_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5d, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS466_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=466 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS467_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5d, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS467_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=467 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS468_NR0_A1_S3)

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0x50, 0x00, 0x00, 0x00,
0x03,
0x5d, 0x40, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS468_NR0_A1_S3, 14)

FIELD(RR_D3_NS469_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5d, 0x50, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS469_NR0_A1_S3, 14)

FIELD(RR_D3_NS470_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5d, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS470_NR0_A1_S3, 14)

FIELD(RR_D3_NS471_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5d, 0x70, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS471_NR0_A1_S3, 14)

FIELD(RR_D3_NS472_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5d, 0x80, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS472_NR0_A1_S3, 14)

FIELD(RR_D3_NS473_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5d, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS473_NR0_A1_S3, 14)

FIELD(RR_D3_NS474_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5d, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS474_NR0_A1_S3, 14)

FIELD(RR_D3_NS475_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x5d, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS475_NR0_A1_S3, 14)

FIELD(RR_D3_NS476_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00,
0x03,

```

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=468 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=469 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=470 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=471 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=472 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=473 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=474 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=475 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf = 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*

```

        0x5d, 0xc0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS476_NR0_A1_S3, 14)

FIELD(RR_D3_NS477_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x5d, 0xd0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS477_NR0_A1_S3, 14)

FIELD(RR_D3_NS478_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x5d, 0xe0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS478_NR0_A1_S3, 14)

FIELD(RR_D3_NS479_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x5d, 0xf0, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS479_NR0_A1_S3, 14)

FIELD(RR_D3_NS480_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x5e, 0x00, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS480_NR0_A1_S3, 14)

FIELD(RR_D3_NS481_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x5e, 0x10, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS481_NR0_A1_S3, 14)

FIELD(RR_D3_NS482_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x5e, 0x20, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS482_NR0_A1_S3, 14)

FIELD(RR_D3_NS483_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x5e, 0x30, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS483_NR0_A1_S3, 14)

FIELD(RR_D3_NS484_NR0_A1_S3)
        0x50, 0x00, 0x00, 0x00,
        0x03,
        0x5e, 0x40, 0x00,
        0x42, 0x43, 0x44, 0x00, 0x00, 0x00

```

/ I frame: N(S)=476 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=477 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=478 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=479 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=480 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=481 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=482 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=483 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

/ sdu.l_buf= 80, sdu.o_buf = 0 */*
/ PD = 0, C/R = 0, SAPI = 3 */*
/ I frame: N(S)=484 N(R)=0 A=1 RR */*
/ FCS: 0x00 for sim. */*

```

ENDFIELD(RR_D3_NS484_NR0_A1_S3, 14)

FIELD(RR_D3_NS485_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0x50, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS485_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=485 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS486_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0x60, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS486_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=486 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS487_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0x70, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS487_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=487 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS488_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0x80, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS488_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=488 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS489_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0x90, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS489_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=489 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS490_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0xa0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS490_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=490 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS491_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0xb0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS491_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=491 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS492_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0xc0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS492_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=492 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

FIELD(RR_D3_NS493_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0xd0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS493_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=493 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS494_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS494_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=494 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS495_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5e, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS495_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=495 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS496_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5f, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS496_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=496 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS497_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5f, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS497_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=497 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS498_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5f, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS498_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=498 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS499_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5f, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS499_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=499 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS500_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5f, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS500_NR0_A1_S3, 14)
/* sdu.l_buf= 80, sdu.o_buf= 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=500 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

FIELD(RR_D3_NS501_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,

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```

0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5f, 0x50, 0x00, /* I frame: N(S)=501 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS501_NR0_A1_S3, 14)

FIELD(RR_D3_NS502_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5f, 0x60, 0x00, /* I frame: N(S)=502 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS502_NR0_A1_S3, 14)

FIELD(RR_D3_NS503_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5f, 0x70, 0x00, /* I frame: N(S)=503 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS503_NR0_A1_S3, 14)

FIELD(RR_D3_NS504_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5f, 0x80, 0x00, /* I frame: N(S)=504 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS504_NR0_A1_S3, 14)

FIELD(RR_D3_NS505_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5f, 0x90, 0x00, /* I frame: N(S)=505 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS505_NR0_A1_S3, 14)

FIELD(RR_D3_NS506_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5f, 0xa0, 0x00, /* I frame: N(S)=506 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS506_NR0_A1_S3, 14)

FIELD(RR_D3_NS507_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5f, 0xb0, 0x00, /* I frame: N(S)=507 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS507_NR0_A1_S3, 14)

FIELD(RR_D3_NS508_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5f, 0xc0, 0x00, /* I frame: N(S)=508 N(R)=0 A=1 RR */
0x42, 0x43, 0x44, 0x00, 0x00, 0x00 /* FCS: 0x00 for sim. */
ENDFIELD(RR_D3_NS508_NR0_A1_S3, 14)

FIELD(RR_D3_NS509_NR0_A1_S3)
0x50, 0x00, 0x00, 0x00, /* sdu.l_buf = 80, sdu.o_buf = 0 */
0x03, /* PD = 0, C/R = 0, SAPI = 3 */
0x5f, 0xd0, 0x00, /* I frame: N(S)=509 N(R)=0 A=1 RR */

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```

        0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS509_NR0_A1_S3, 14)

FIELD(RR_D3_NS510_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5f, 0xe0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS510_NR0_A1_S3, 14)

FIELD(RR_D3_NS511_NR0_A1_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x5f, 0xf0, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS511_NR0_A1_S3, 14)

/*
  A-Bit = 0
*/
FIELD(RR_D3_NS0_NR0_A0_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x00, 0x00, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS0_NR0_A0_S3, 14)

FIELD(RR_D3_NS1_NR0_A0_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x00, 0x10, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS1_NR0_A0_S3, 14)

FIELD(RR_D3_NS2_NR0_A0_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x00, 0x20, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS2_NR0_A0_S3, 14)

FIELD(RR_D3_NS3_NR0_A0_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x00, 0x30, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS3_NR0_A0_S3, 14)

FIELD(RR_D3_NS4_NR0_A0_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,
    0x00, 0x40, 0x00,
    0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS4_NR0_A0_S3, 14)

FIELD(RR_D3_NS5_NR0_A0_S3)
    0x50, 0x00, 0x00, 0x00,
    0x03,

```

/* FCS: 0x00 for sim. */

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=510 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=511 N(R)=0 A=1 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=0 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=1 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=2 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=3 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=4 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */

```

```

0x00, 0x50, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS5_NR0_A0_S3, 14)

FIELD(RR_D3_NS6_NR0_A0_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x00, 0x60, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS6_NR0_A0_S3, 14)

FIELD(RR_D3_NS505_NR0_A0_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x1f, 0x90, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS505_NR0_A0_S3, 14)

FIELD(RR_D3_NS506_NR0_A0_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x1f, 0xa0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS506_NR0_A0_S3, 14)

FIELD(RR_D3_NS507_NR0_A0_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x1f, 0xb0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS507_NR0_A0_S3, 14)

FIELD(RR_D3_NS508_NR0_A0_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x1f, 0xc0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS508_NR0_A0_S3, 14)

FIELD(RR_D3_NS509_NR0_A0_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x1f, 0xd0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS509_NR0_A0_S3, 14)

FIELD(RR_D3_NS510_NR0_A0_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x1f, 0xe0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00
ENDFIELD(RR_D3_NS510_NR0_A0_S3, 14)

FIELD(RR_D3_NS511_NR0_A0_S3)
0x50, 0x00, 0x00, 0x00,
0x03,
0x1f, 0xf0, 0x00,
0x42, 0x43, 0x44, 0x00, 0x00, 0x00

```

```

/* I frame: N(S)=5 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=6 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=505 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=506 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=507 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=508 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=509 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=510 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

```

/* sdu.l_buf = 80, sdu.o_buf = 0 */
/* PD = 0, C/R = 0, SAPI = 3 */
/* I frame: N(S)=511 N(R)=0 A=0 RR */
/* FCS: 0x00 for sim. */

```

ENDFIELD(RR_D3_NS511_NR0_A0_S3, 14)

/*

2.3 Declarations

NOTE: This section must be located behind all other parameter sections due to function calls in the DECLARATION macro. The only exceptions are the following two sections (Parameter Arrays / Parameter Structures) which contain the definitions of the declarations in this section.

*/

/*

2.3.1 Array declarations

NOTE: The arrays are defined in section 2.4.

*/

DECLARATION (ARRAY_8_NULL)

DECLARATION (A_REF_0)

DECLARATION (A_REF_1)

DECLARATION (A_REF_2)

DECLARATION (A_REF_3)

DECLARATION (A_REF_4)

DECLARATION (A_REF_5)

DECLARATION (A_REF_6)

DECLARATION (A_REF_7)

DECLARATION (A_REF_8)

DECLARATION (A_REF_9)

DECLARATION (A_REF_10)

DECLARATION (A_REF_11)

DECLARATION (A_REF_12)

DECLARATION (A_REF_13)

DECLARATION (A_REF_14)

DECLARATION (A_REF_15)

DECLARATION (A_REF_16)

DECLARATION (A_REF_17)

DECLARATION (A_REF_18)

DECLARATION (A_REF_19)

DECLARATION (A_REF_20)

DECLARATION (A_REF_21)

DECLARATION (A_REF_22)

DECLARATION (A_REF_23)

DECLARATION (A_REF_24)

DECLARATION (A_REF_25)

DECLARATION (A_REF_26)

DECLARATION (A_REF_27)

DECLARATION (A_REF_28)

DECLARATION (A_REF_29)

DECLARATION (A_REF_30)

DECLARATION (A_REF_31)

/*

2.3.2 Structure declarations

NOTE: The arrays are defined in section 2.5.

*/

DECLARATION (LLREQ_QOS_DEL4_REL3)

DECLARATION (LLREQ_QOS_DEL4_REL5)

DECLARATION (GRRREQ_QOS_PEAKSUB)
DECLARATION (LLGMM_KC_NULL)

DECLARATION (REF_0)
DECLARATION (REF_1)
DECLARATION (REF_2)
DECLARATION (REF_3)
DECLARATION (REF_4)
DECLARATION (REF_5)
DECLARATION (REF_6)
DECLARATION (REF_7)
DECLARATION (REF_8)
DECLARATION (REF_9)
DECLARATION (REF_10)
DECLARATION (REF_11)
DECLARATION (REF_12)
DECLARATION (REF_13)
DECLARATION (REF_14)
DECLARATION (REF_15)
DECLARATION (REF_16)
DECLARATION (REF_17)
DECLARATION (REF_18)
DECLARATION (REF_19)
DECLARATION (REF_20)
DECLARATION (REF_21)
DECLARATION (REF_22)
DECLARATION (REF_23)
DECLARATION (REF_24)
DECLARATION (REF_25)
DECLARATION (REF_26)
DECLARATION (REF_27)
DECLARATION (REF_28)
DECLARATION (REF_29)
DECLARATION (REF_30)
DECLARATION (REF_31)

/*

2.4 Parameter Arrays

NOTE: The arrays have been declared in section 2.3.

*/

/*

2.4.1 LLGMM Ciphering key Kc

*/

BEGINARRAY (ARRAY_8_NULL, 8)
0, 0, 0, 0, 0, 0, 0, 0

ENDARRAY

/*

2.5 Parameter Structures

NOTE: The structures have been declared in section 2.3.

*/

/*

2.5.1 LL QoS Structures

```

*/
BEGIN_PSTRUCT ("ll_qos", LLREQ_QOS_DEL4_REL3)
    SET_COMP ("delay", LL_DELAY_4)
    SET_COMP ("relclass", LL_RLC_PROT)
    SET_COMP ("peak", LL_PEAK_SUB)
    SET_COMP ("preced", LL_PRECED_SUB)
    SET_COMP ("mean", LL_MEAN_SUB)
    SET_COMP ("reserved_1", 0)
    SET_COMP ("reserved_2", 0)
    SET_COMP ("reserved_3", 0)
ENDSTRUCT

BEGIN_PSTRUCT ("ll_qos", LLREQ_QOS_DEL4_REL5)
    SET_COMP ("delay", LL_DELAY_4)
    SET_COMP ("relclass", LL_NO_REL)
    SET_COMP ("peak", LL_PEAK_SUB)
    SET_COMP ("preced", LL_PRECED_SUB)
    SET_COMP ("mean", LL_MEAN_SUB)
    SET_COMP ("reserved_1", 0)
    SET_COMP ("reserved_2", 0)
    SET_COMP ("reserved_3", 0)
ENDSTRUCT
/*

```

2.5.2 GRR QoS Structures

```

*/
/*
NOTE: GRR must only regard the member "peak" of the profiles. Thus, all other members may be skipped. Unfortunately, this is not
possible, so they are also set to their subscribed values by the protocol stack when running in _SIMULATION_ mode.
*/
BEGIN_PSTRUCT ("grr_qos", GRRREQ_QOS_PEAKESUB)
    SET_COMP ("delay", GRR_DELAY_SUB)
    SET_COMP ("relclass", GRR_RELCLASS_SUB)
    SET_COMP ("peak", GRR_PEAK_SUB)
    SET_COMP ("preced", GRR_PRECED_SUB)
    SET_COMP ("mean", GRR_MEAN_SUB)
    SET_COMP ("reserved_1", 0)
    SET_COMP ("reserved_2", 0)
    SET_COMP ("reserved_3", 0)
ENDSTRUCT
/*

```

2.5.3 LLGMM Cipherring key Kc

```

*/
BEGIN_PSTRUCT ("llgmm_kc", LLGMM_KC_NULL)
    SET_COMP ("key", ARRAY_8_NULL)
ENDSTRUCT
/*

```

2.5.4 LL Reference Structs

```

*/
BEGIN_PSTRUCT ("reference1", REF_0)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_0)

```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_1)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_1)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_2)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_2)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_3)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_3)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_4)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_4)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_5)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_5)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_6)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_6)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_7)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_7)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_8)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_8)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_9)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_9)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_10)
    SET_COMP ("ref_nsapi", NSAPI_1)
```

```
        SET_COMP ("ref_npdu_num", NPDU_NUM_1)
        SET_COMP ("ref_seg_num", SEGNUM_10)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_11)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_11)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_12)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_12)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_13)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_13)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_14)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_14)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_15)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_15)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_16)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_16)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_17)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_17)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_18)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_18)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_19)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_19)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_20)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_20)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_21)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_21)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_22)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_22)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_23)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_23)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_24)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_24)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_25)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_25)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_26)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_26)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_27)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_27)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_28)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_28)
ENDSTRUCT
```

```
BEGIN_PSTRUCT ("reference1", REF_29)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_29)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_30)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_30)
```

ENDSTRUCT

```
BEGIN_PSTRUCT ("reference1", REF_31)
    SET_COMP ("ref_nsapi", NSAPI_1)
    SET_COMP ("ref_npdu_num", NPDU_NUM_1)
    SET_COMP ("ref_seg_num", SEGNUM_31)
```

ENDSTRUCT

```
BEGIN_PSTRUCT_ARRAY (A_REF_0, 1) REF_0 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_1, 1) REF_1 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_2, 1) REF_2 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_3, 1) REF_3 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_4, 1) REF_4 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_5, 1) REF_5 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_6, 1) REF_6 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_7, 1) REF_7 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_8, 1) REF_8 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_9, 1) REF_9 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_10, 1) REF_10 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_11, 1) REF_11 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_12, 1) REF_12 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_13, 1) REF_13 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_14, 1) REF_14 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_15, 1) REF_15 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_16, 1) REF_16 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_17, 1) REF_17 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_18, 1) REF_18 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_19, 1) REF_19 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_20, 1) REF_20 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_21, 1) REF_21 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_22, 1) REF_22 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_23, 1) REF_23 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_24, 1) REF_24 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_25, 1) REF_25 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_26, 1) REF_26 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_27, 1) REF_27 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_28, 1) REF_28 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_29, 1) REF_29 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_30, 1) REF_30 ENDARRAY
BEGIN_PSTRUCT_ARRAY (A_REF_31, 1) REF_31 ENDARRAY
```

3 TEST CASES

3.1 Routing (internal) (LLC000)

3.1.1 LLC000: Setup Routing and PCO View for LLC Tests

Description:

Routings for LLC tests are set.

Preamble:

None

	GMM	L3	LLC	RLC/MAC
COMMAND (TAP RESET)				
COMMAND (GMM RESET)				
COMMAND (SMS RESET)				
COMMAND (SND RESET)				
COMMAND (LLC RESET)				
COMMAND (GRR RESET)				
COMMAND (TAP REDIRECT CLEAR)				
COMMAND (GMM REDIRECT CLEAR)				
COMMAND (SMS REDIRECT CLEAR)				
COMMAND (SND REDIRECT CLEAR)				
COMMAND (LLC REDIRECT CLEAR)				
COMMAND (GRR REDIRECT CLEAR)				
COMMAND (GMM REDIRECT LLC NULL)				
COMMAND (SMS REDIRECT LLC NULL)				
COMMAND (SND REDIRECT LLC NULL)				
COMMAND (GRR REDIRECT LLC NULL)				
COMMAND (LLC REDIRECT GMM TAP)				
COMMAND (LLC REDIRECT SMS TAP)				
COMMAND (LLC REDIRECT SND TAP)				
COMMAND (LLC REDIRECT GRR TAP)				
COMMAND (TAP REDIRECT TAP LLC)				

Parametrization:

Primitive	Parameter	Value
-----------	-----------	-------

History:

06-Oct-1999	DB	Initial
28-Jan-2000	DB	Changed SNDCP to SND

3.2 TLLI assignment/unassignment (LLC001 – LLC009)

3.2.1 LLC001: TLLI assignment (no ciphering), LLC ready

Description:

LLC receives the primitive LLGMM_ASSIGN_REQ. LLC enters state 'TLLI Assigned/ADM'. LLC is ready to receive unacknowledged data frames.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
(1)				
		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

21-Sep-1999	DB	Initial
25-Nov-1999	DB	Removed parameter assign, used old_tlli/new_tlli instead
30-Nov-1999	DB	Removed flow control for SAPI 1
18-Feb-2000	DB	Changed tlli of primitives to NOT_USED

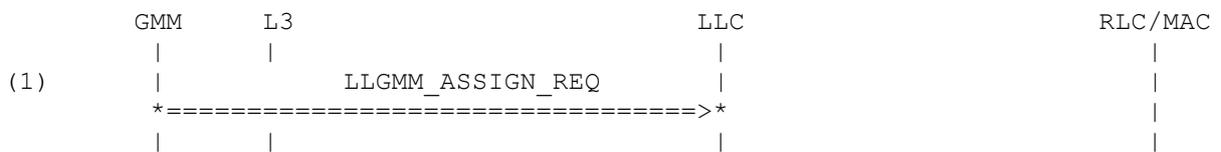
3.2.2 LLC002: TLLI assignment (no ciphering), LLC not ready

Description:

LLC receives the primitive LLGMM_ASSIGN_REQ. LLC enters state 'TLLI Assigned/ADM'. LLC is not ready to receive unacknowledged data frames.

Preamble:

LLC003



Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM

History:

21-Sep-1999	DB	Initial
25-Nov-1999	DB	Removed parameter assign, used old_tlli/new_tlli instead

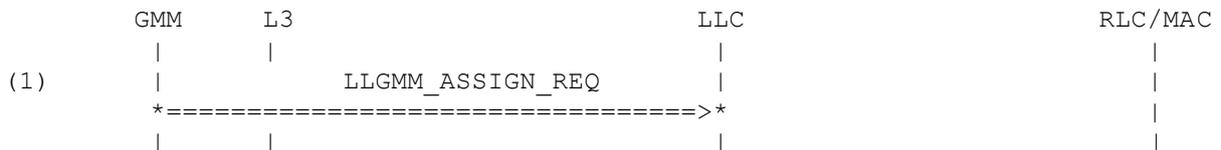
3.2.3 LLC003: TLLI unassignment (no ciphering)

Description:

LLC receives the primitive LLGMM_ASSIGN_REQ. LLC enters state 'TLLI Assigned/ADM'. LLC is ready to receive unacknowledged data frames.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	TLLI_LOCAL_1
	new_tlli	LLGMM_TLLI_INVALID

History:

06-Jan-2000	DB	Initial
10-Jan-2000	DB	Inserted xid_valid in LL_XID_IND
22-May-2000	DB	Inserted reserved_* parameter(s)

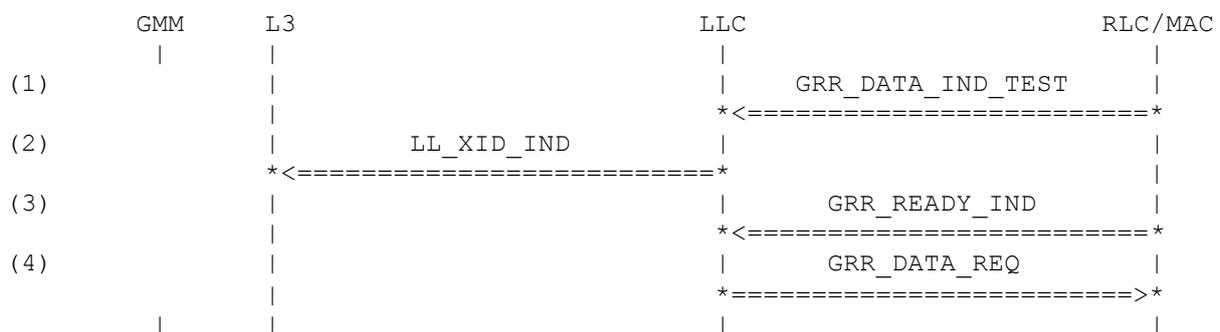
3.3.2 LLC051: Negotiation of acceptable parameters on SAPI 1 (Version, unknown parameter, N201-U)

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters are acceptable, the unknown parameter is ignored. LLC sends an XID response containing all known XID parameters.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	XID1_VE0_XA31_NU500_SAPI1
(2) LL_XID_IND	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_500
	n201_i	NOT_USED
	xid_valid	LL_XID_INVALID
	sdu	NOT_USED
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID1_VE0_NU500_SAPI1

History:

02-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

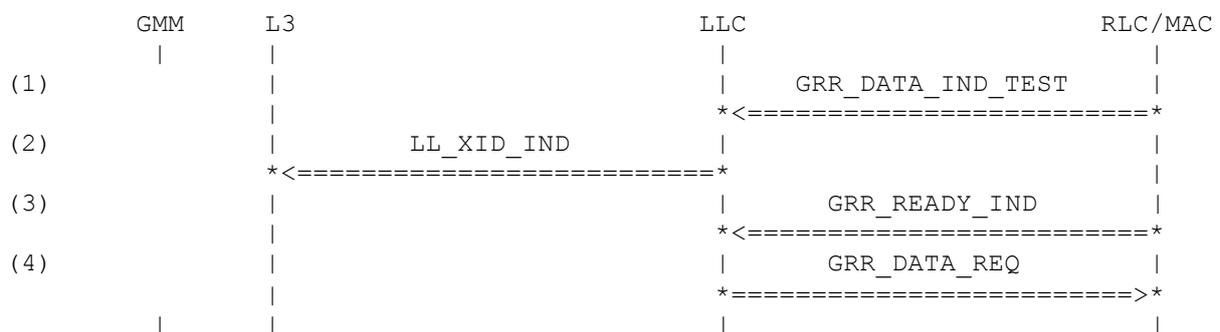
3.3.3 LLC052: Negotiation of acceptable parameters on SAPI 1 (Version, N201-U, N201-U, N201-U)

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters are acceptable (all instances of N201-U but the first are ignored). LLC sends an XID response containing the accepted XID parameters.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	XID1_VE0_NU500_NU750_NU1000_SAPI1
(2) LL_XID_IND	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_500
	n201_i	NOT_USED
	xid_valid	LL_XID_INVALID
	sdu	NOT_USED
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID1_VE0_NU500_SAPI1

History:

02-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

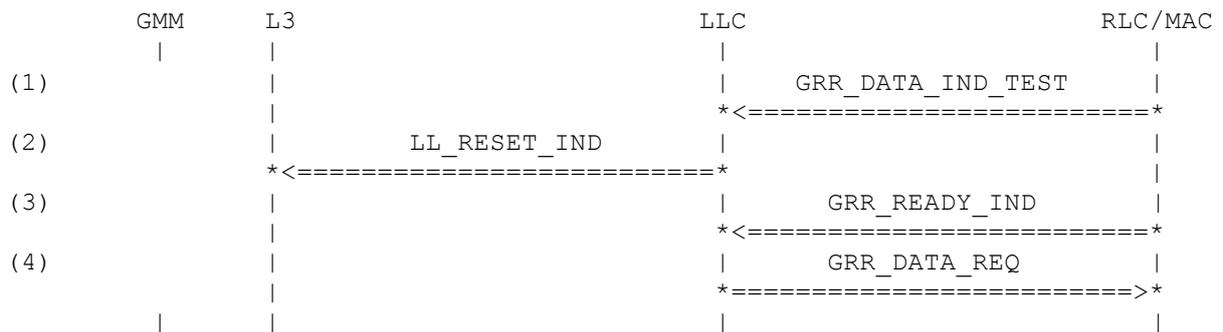
3.3.4 LLC053: Negotiation of parameter IOV-UI on SAPI 1

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameter is acceptable. LLC sends, as required, an empty XID response confirming the value.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID1_IOV_UI_SAPI1
(2) LL_RESET_IND	sapi tlli	LL_SAPI_1 TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_SAPI1

History:

17-Aug-2001 GS Initial

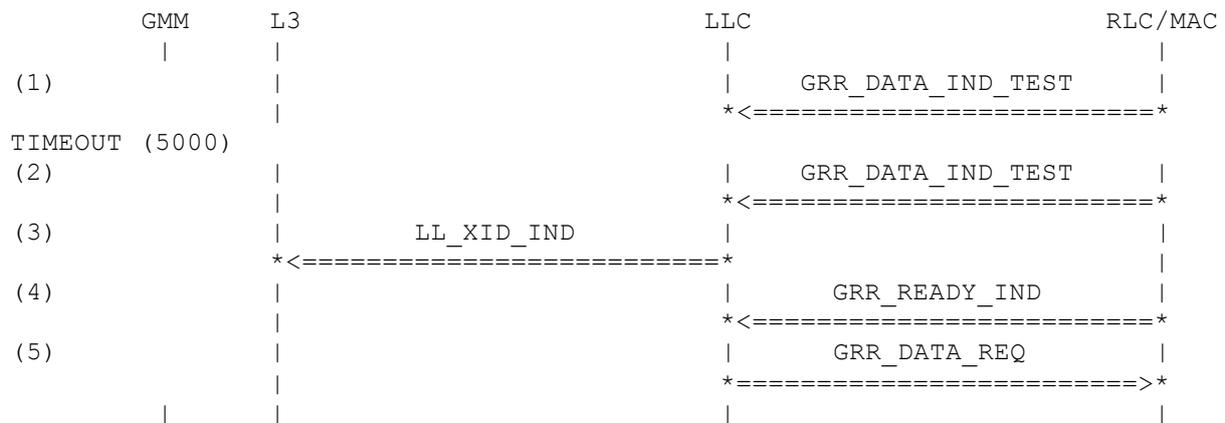
3.3.5 LLC055: XID information field exceeds SDU length, frame discarded

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The XID information field of the frame exceeds the SDU length, so the frame is discarded. After 5 seconds, LLC receives another primitive. The received XID parameters are acceptable. LLC sends an XID response containing the same XID parameters.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	XID1_EXCEEDSSDULENGTH_SAPI1
(2) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	XID1_VE0_NU500_SAPI1
(3) LL_XID_IND	sapi	LL_SAPI_1
	tli	TLLI_LOCAL_1
	n201_u	N201_U_500
	n201_i	NOT_USED
	xid_valid	LL_XID_INVALID
(4) GRR_READY_IND	sdu	NOT_USED
(5) GRR_DATA_REQ	sapi	LL_SAPI_1
	tli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID1_VE0_NU500_SAPI1

History:

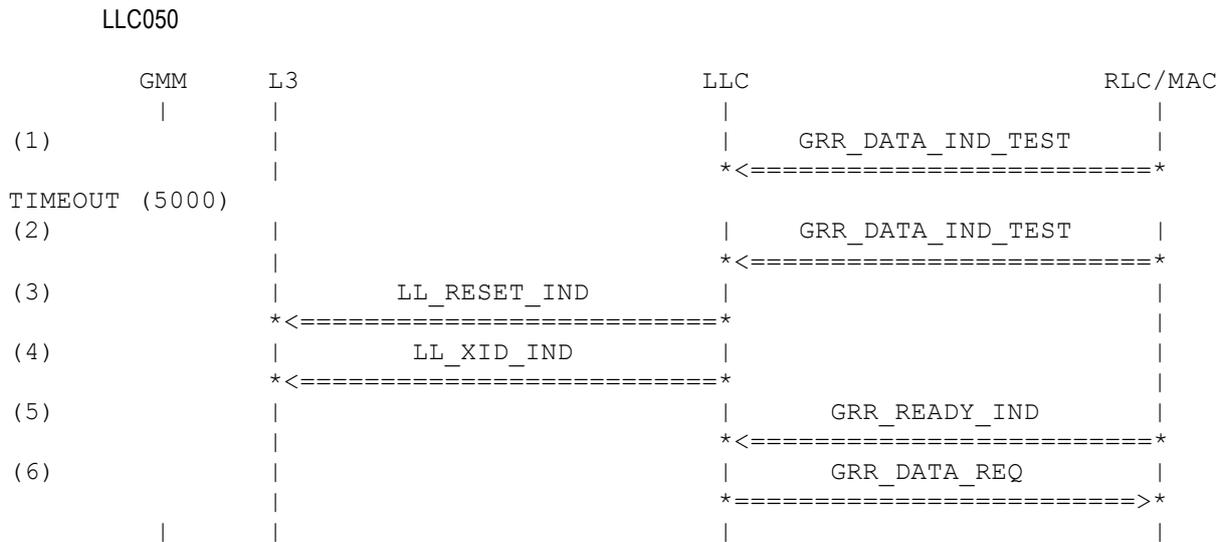
02-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.3.6 LLC057: Negotiation (Version, N201-U), invalid XID information field, Reset LLC

Description:

LLC has already negotiated N201-U (LLC050). LLC then receives the primitive GRR_DATA_IND (U frame: XID command). The XID information field of the frame is invalid (Reset not the first parameter), so the frame is discarded. After 5 seconds, LLC receives another primitive, containing the Reset parameter as first parameter. LLC informs layer 3 of the reset.

Preamble:



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	XID1_VE0_NU500_RE_SAPI1
(2) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	XID1_RE_VE0_NU500_SAPI1
(3) LL_RESET_IND	sapi	LL_SAPI_1
	tli	TLLI_LOCAL_1
(4) LL_XID_IND	sapi	LL_SAPI_1
	tli	TLLI_LOCAL_1
	n201_u	N201_U_500
	n201_i	NOT_USED
	xid_valid	LL_XID_INVALID
(5) GRR_READY_IND	sdu	NOT_USED
(6) GRR_DATA_REQ	sapi	LL_SAPI_1
	tli	TLLI_LOCAL_1

grr_qos	NOT_USED
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	XID1_VE0_NU500_SAPI1

History:

02-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

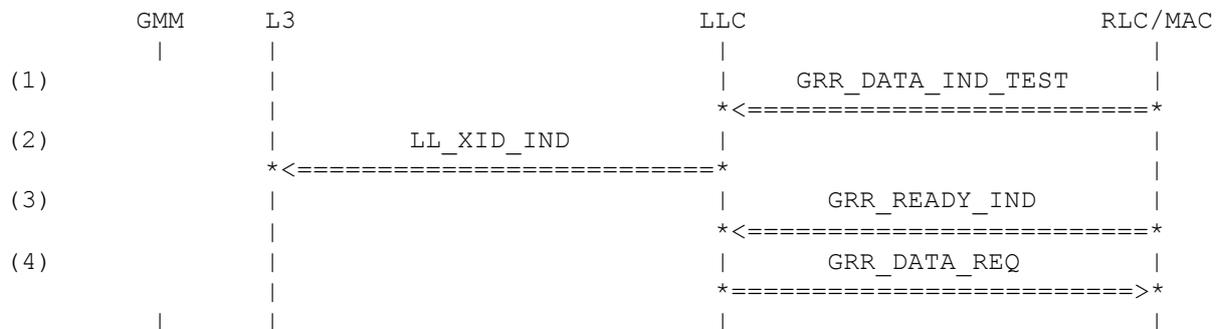
3.3.7 LLC058: Negotiation of all parameters with minimum acceptable values on SAPI 3

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters are acceptable. LLC sends an XID response containing the same XID parameters.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	XID1_SGSN_ALLMIN_SAPI3
(2) LL_XID_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_MIN
	n201_i	N201_I_MIN
	xid_valid	LL_XID_INVALID
	sdu	NOT_USED
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID1_SGSN_ALLMIN_SAPI3

History:

06-Jan-2000	DB	Initial
10-Jan-2000	DB	Inserted xid_valid in LL_XID_IND
22-May-2000	DB	Inserted reserved_* parameter(s)

3.3.8 LLC059: Negotiation of all parameters with maximum acceptable values on all SAPIs

Description:

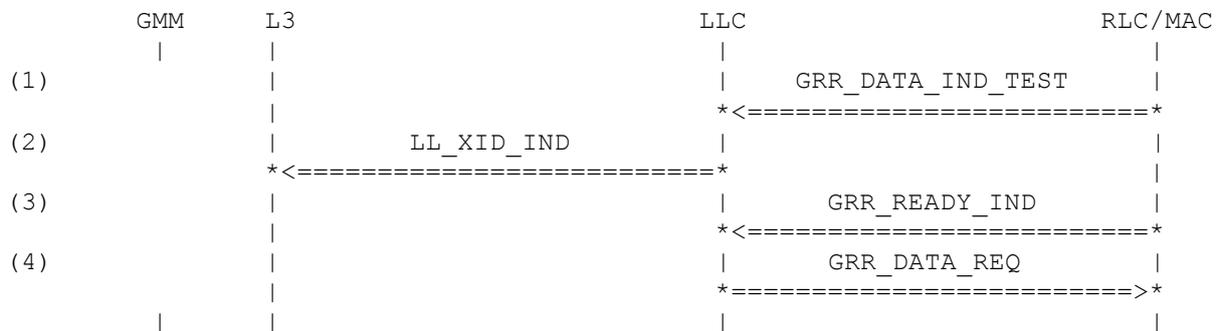
LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters are acceptable. LLC sends an XID response containing the same XID parameters.

Preamble:

LLC001

Variants:

<A>....<F>



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	XID1_SGSN_ALLMAX_SAPI1
<C>	sdu	XID1_SGSN_ALLMAX_SAPI3
<D>	sdu	XID1_SGSN_ALLMAX_SAPI5
<E>	sdu	XID1_SGSN_ALLMAX_SAPI7
<F>	sdu	XID1_SGSN_ALLMAX_SAPI9
	sdu	XID1_SGSN_ALLMAX_SAPI11
(2) LL_XID_IND		
<A>	sapi	LL_SAPI_1
	sapi	LL_SAPI_3
<C>	sapi	LL_SAPI_5
<D>	sapi	LL_SAPI_7
<E>	sapi	LL_SAPI_9
<F>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_MAX
<A>	n201_i	N201_I_ZERO
	n201_i	N201_I_MAX
<C>	n201_i	N201_I_MAX
<D>	n201_i	N201_I_ZERO
<E>	n201_i	N201_I_MAX
<F>	n201_i	N201_I_MAX
	xid_valid	LL_XID_INVALID
	sdu	NOT_USED

(3) GRR_READY_IND

(4) GRR_DATA_REQ

<A>	sapi	LL_SAPI_1
	sapi	LL_SAPI_3
<C>	sapi	LL_SAPI_5
<D>	sapi	LL_SAPI_7
<E>	sapi	LL_SAPI_9
<F>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	XID1_SGSN_ALLMAX_SAPI1
	sdu	XID1_SGSN_ALLMAX_SAPI3
<C>	sdu	XID1_SGSN_ALLMAX_SAPI5
<D>	sdu	XID1_SGSN_ALLMAX_SAPI7
<E>	sdu	XID1_SGSN_ALLMAX_SAPI9
<F>	sdu	XID1_SGSN_ALLMAX_SAPI11

History:

06-Jan-2000	DB	Initial
10-Jan-2000	DB	Inserted xid_valid in LL_XID_IND
22-May-2000	DB	Inserted reserved_* parameter(s)
09-Feb-2001	GS	Rewritten to support all sapis 1, 3, 5, 7, 9 and 11

3.3.9 LLC060: Negotiation of unacceptable parameters on SAPI 1 (Version, N201-U)

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters are unacceptable (out-of-range). LLC sends an XID response containing the same XID parameters, but with N201-U set to the current (= default) value.

Preamble:

LLC001

	GMM	L3	LLC	RLC/MAC
(1)				
			GRR_DATA_IND_TEST	
			<=====	
(2)				
			GRR_READY_IND	
			<=====	
(3)				
			GRR_DATA_REQ	
			=====>*	

Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	XID1_VE0_NU200_SAPI1
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1

grr_qos	NOT_USED
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	XID1_VE0_NU400_SAPI1

History:

06-Jan-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

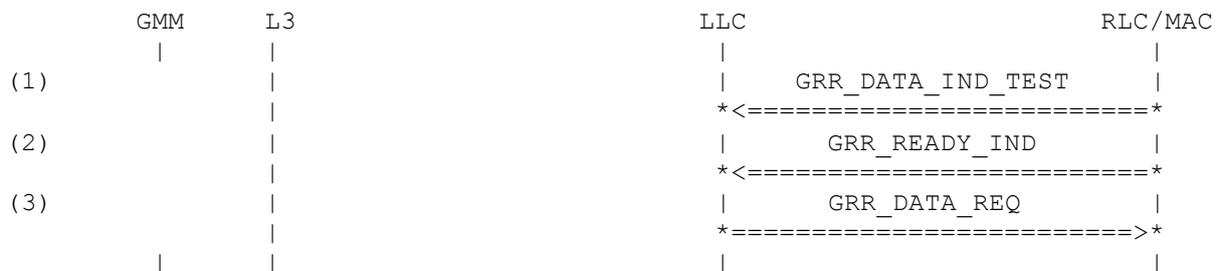
3.3.10 LLC068: Negotiation of all parameters with one less than minimum values on SAPI 3

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters are unacceptable. LLC sends an XID response containing the same XID parameters. LLC058 is executed before to ensure that all parameters have minimum values.

Preamble:

LLC058



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli sdu XID1_SGSN_ALLLESSTHANMIN_SAPI3	TLLI_LOCAL_1
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_SGSN_ALLMIN_SAPI3

History:

07-Jan-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.3.11 LLC069: Negotiation of all parameters with one more than maximum values on SAPI 3

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters are unacceptable. LLC sends an XID response containing the same XID parameters. LLC059B is executed before to ensure that all parameters have maximum values.

Preamble:

LLC059B

	GMM	L3	LLC	RLC/MAC
(1)			GRR_DATA_IND_TEST	
			<=====	
(2)			GRR_READY_IND	
			<=====	
(3)			GRR_DATA_REQ	
			=====>*	

Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli sdu XID1_SGSN_ALLMORETHANMAX_SAPI3	TLLI_LOCAL_1
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_SGSN_ALLMAX_SAPI3

History:

07-Jan-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

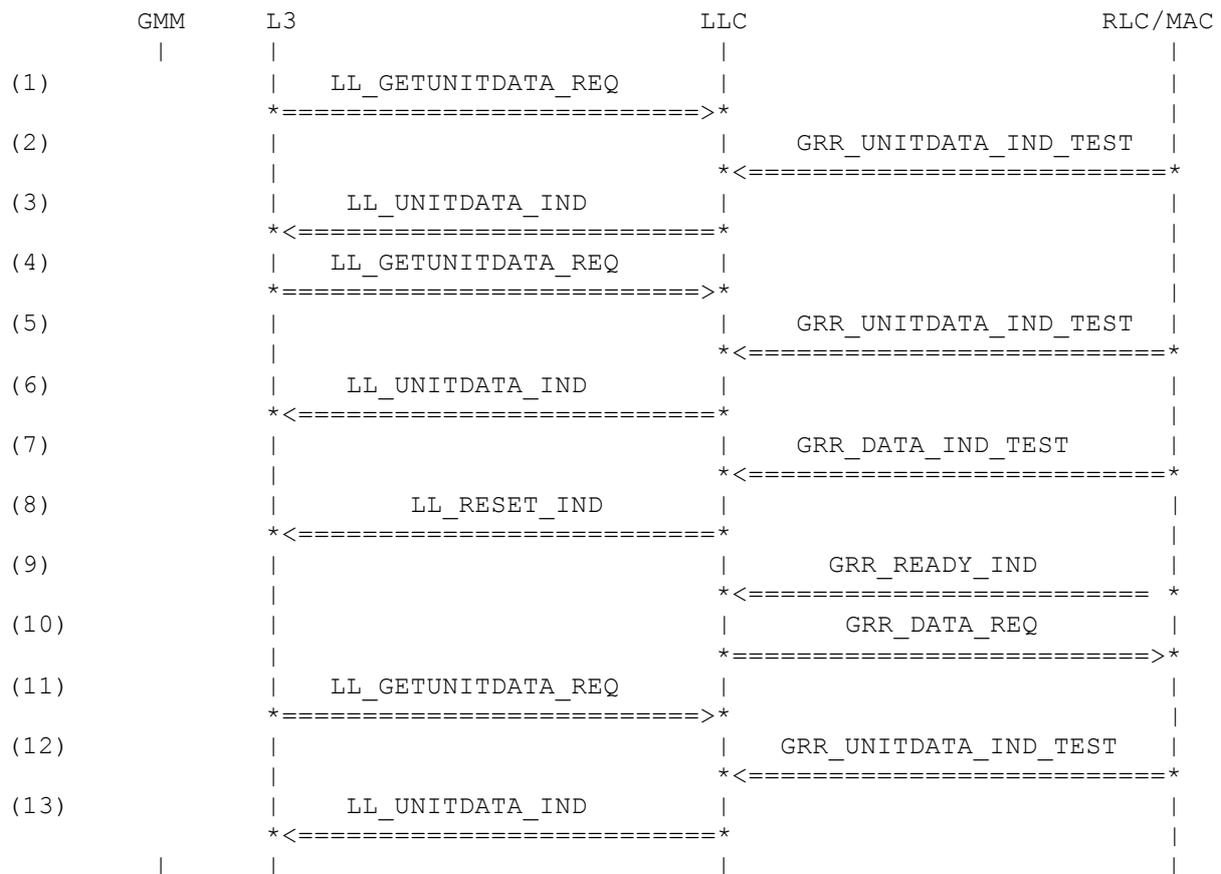
3.3.12 LLC070: Unacknowledged downlink data transfer, XID negotiation containing only Reset

Description:

After two unacknowledged frames have been received in downlink direction, LLC receives an XID command containing the Reset parameter. LLC is being resetted (including N(UR) being set to 0) and LL_RESET_IND is sent to layer 3. LLC sends an XID response frame to its peer to complete the negotiation. Another unacknowledged frame is received with N(U) = 0.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
(2) GRR_UNITDATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI3
(3) LL_UNITDATA_IND	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_SAPI3
(4) LL_GETUNITDATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
(5) GRR_UNITDATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_NU1_SAPI3

(6) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_NU1_SAPI3
(7) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID1_RE_SAPI3
(8) LL_RESET_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(9) GRR_READY_IND		
(10) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_SAPI3
(11) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(12) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI3
(13) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI3

History:

28-Aug-2000 SLM Initial

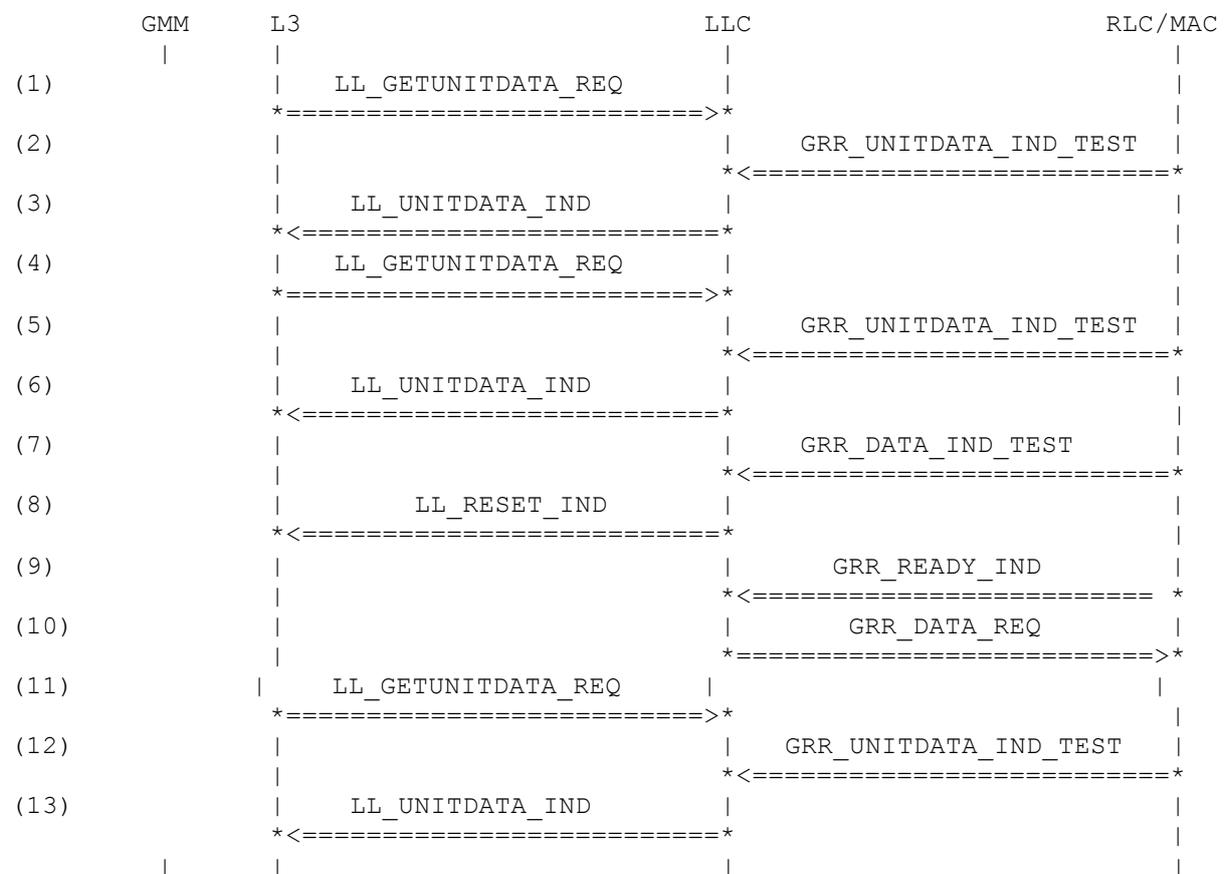
3.3.13 LLC071: Unacknowledged downlink data transfer, XID negotiation containing Reset

Description:

After two unacknowledged frames have been received in downlink direction, LLC receives an XID command containing the Reset parameter. LLC is being resetted (including N(UR) being set to 0) and LL_RESET_IND is sent to layer 3. LLC sends an XID response frame to its peer to complete the negotiation. Another unacknowledged frame is received with N(U) = 0.

Preamble:

LLC001

**Parametrization:**

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
(2) GRR_UNITDATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI3
(3) LL_UNITDATA_IND	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1

	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_SAPI3
(4) LL_GETUNITDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(5) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_NU1_SAPI3
(6) LL_UNITDATA_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_NU1_SAPI3
(7) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	XID1_RE_VE0_NU500_SAPI3
(8) LL_RESET_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(9) GRR_READY_IND		
(10) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID1_VE0_NU500_SAPI3
(11) LL_GETUNITDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(12) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI3
(13) LL_UNITDATA_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED

cipher
sdu
LL_CIPHER_OFF
LLIND_SDU100_SAPI3

History:

28-Aug-2000	SLM	Initial
23-Feb-2001	GS	XID_IND removed

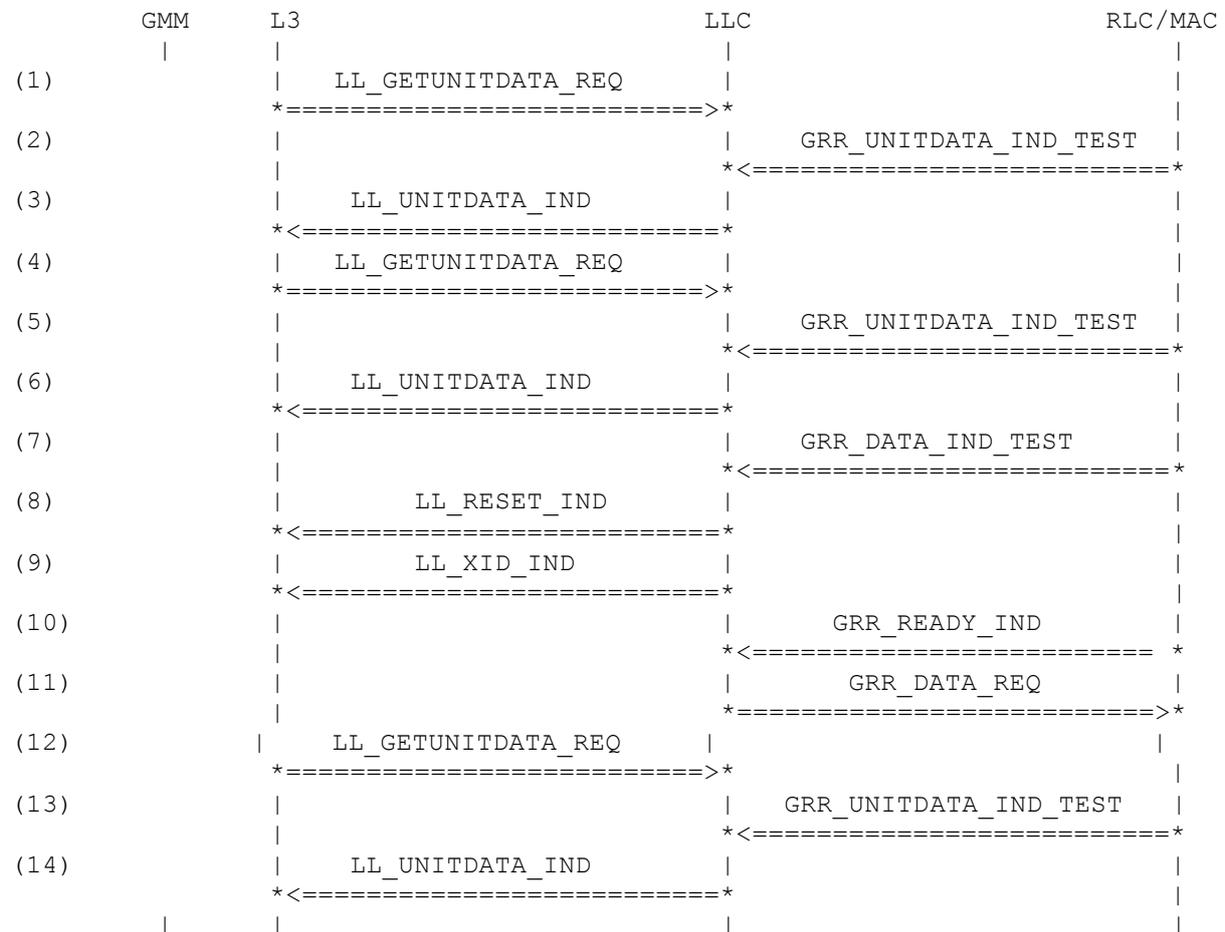
3.3.14 LLC072: Unacknowledged downlink data transfer, XID negotiation containing Reset

Description:

After two unacknowledged frames have been received in downlink direction, LLC receives an XID command containing the Reset parameter. LLC is being resetted (including N(UR) being set to 0) and LL_RESET_IND is sent to layer 3. Next an LL_XID_IND is sent to indicate that the value of N201-U is changed from default to a new value. LLC sends an XID response frame to its peer to complete the negotiation. Another unacknowledged frame is received with N(U) = 0.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1

(2) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI3
(3) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI3
(4) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(5) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_NU1_SAPI3
(6) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_NU1_SAPI3
(7) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID1_RE_VE0_NU400_SAPI3
(8) LL_RESET_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(9) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_400 NOT_USED LL_XID_INVALID NOT_USED
(10) GRR_READY_IND		
(11) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_VE0_NU400_SAPI3

(12) LL_GETUNITDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1

(13) GRR_UNITDATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	GRRIND_DESCLIST100_SAPI3

(14) LL_UNITDATA_IND

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
reserved_unitdata_ind1	NOT_USED
reserved_unitdata_ind2	NOT_USED
reserved_unitdata_ind3	NOT_USED
reserved_unitdata_ind4	NOT_USED
reserved_unitdata_ind5	NOT_USED
cipher	LL_CIPHER_OFF
sdu	LLIND_SDU100_SAPI3

History:

23-Feb-2001	GS	Initial
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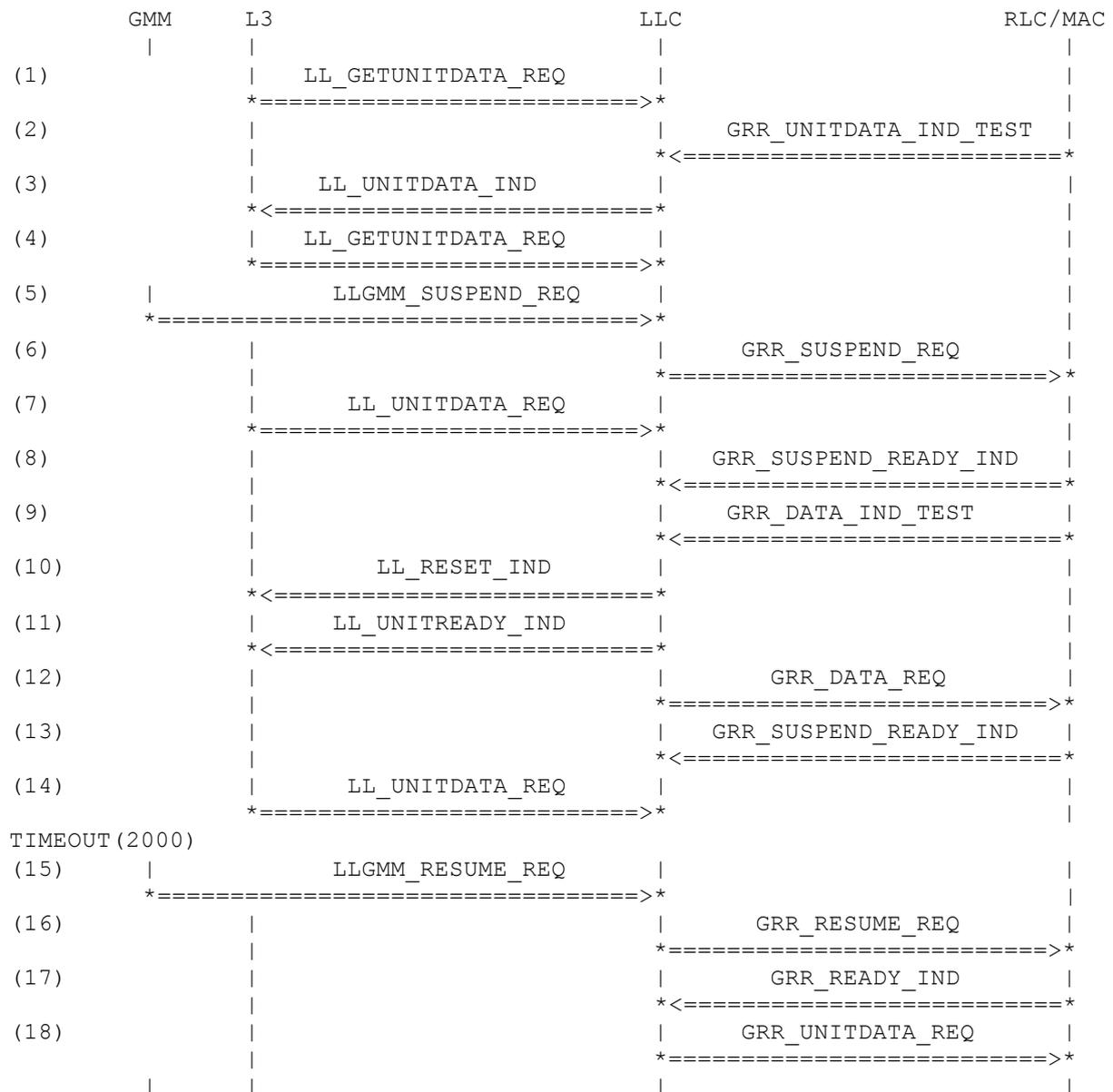
3.3.15 LLC073: Unacknowledged downlink data transfer, XID negotiation containing Reset

Description:

After two unacknowledged frames have been received in downlink direction, LLC is suspended by GMM GRR is not ready to receive data, so the frames are buffered. LLC receives an XID command containing the Reset parameter. LLC is being resetted (including N(UR) being set to 0) and LL_RESET_IND is sent to layer 3. All uplink buffers are cleared. Another unacknowledged frame is received with N(U) = 0. Flow control is reestablished RLC/MAC indicates ready, LLC sends the last buffered UI frame containing the layer 3 data.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
(2) GRR_UNITDATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI3
(3) LL_UNITDATA_IND	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED

	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_SAPI3
(4) LL_GETUNITDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(5) LLGMM_SUSPEND_REQ		
	susp_cause	LLGMM_RAU
(6) GRR_SUSPEND_REQ		
(7) LL_UNITDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(8) GRR_SUSPEND_READY_IND		
(9) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	XID1_RE_VE0_NU500_SAPI3
(10) LL_RESET_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(11) LL_UNITREADY_IND		
	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(12) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID1_VE0_NU500_SAPI3
(13) GRR_SUSPEND_READY_IND		
(14) LL_UNITDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100

- (15) LLGMM_RESUME_REQ
- (16) GRR_RESUME_REQ
- (17) GRR_READY_IND
- (18) GRR_UNITDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAQSUB
radio_prio	LL_RADIO_PRIO_3
reserved_unitdata_req1	NOT_USED
reserved_unitdata_req2	NOT_USED
sdu	GRRREQ_SDU100_SAPI3

History:

22-May-2002 UT Initial

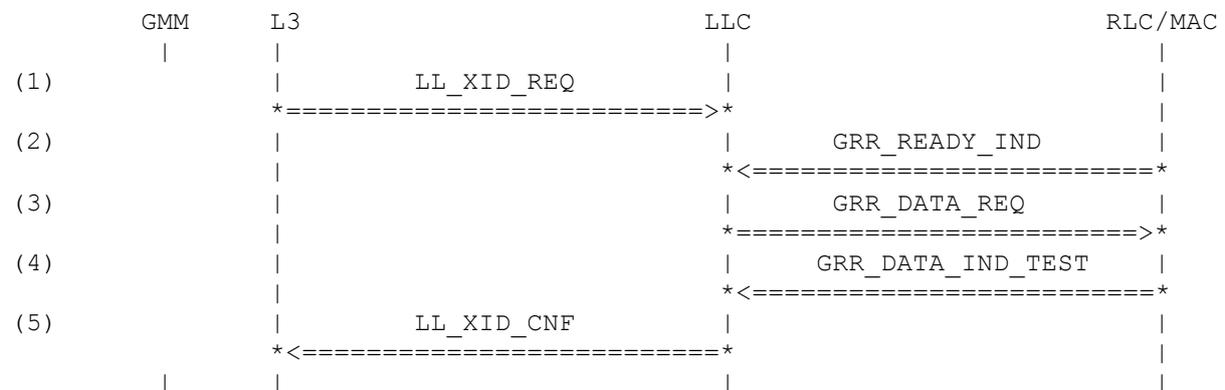
3.3.16 LLC080: Negotiation of XID parameters on SAPI 3 initiated by SNDCP

Description:

LLC is assigned and ready to send data. LLC receives a LL_XID_REQ and therefor sends a XID command message to its peer entity. The peer answers with a XID response. The layer 3 XID parameters are forwarded to SNDCP in an LL_XID_CNF.

Preamble:

LLC001



Parametrization:

	<u>Primitive</u>	<u>Parameter</u>	<u>Value</u>
(1)	LL_XID_REQ	sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		sdu	EMPTY_SDU
(2)	GRR_READY_IND		
(3)	GRR_DATA_REQ	sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	NOT_USED
		radio_prio	GRR_RADIO_PRIO_1
		cause	GRR_DTACS_DEF

	reserved_data_req sdu	NOT_USED XID_CR0_EMPTY_L3_SAPI3
(4) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_CR0_EMPTY_L3_SAPI3
(5) LL_XID_CNF	sapi tlli n201_u n201_i sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_DEF_SAPI3 N201_I_DEF EMPTY_SDU

History: 10-Oct-2001 GS Initial

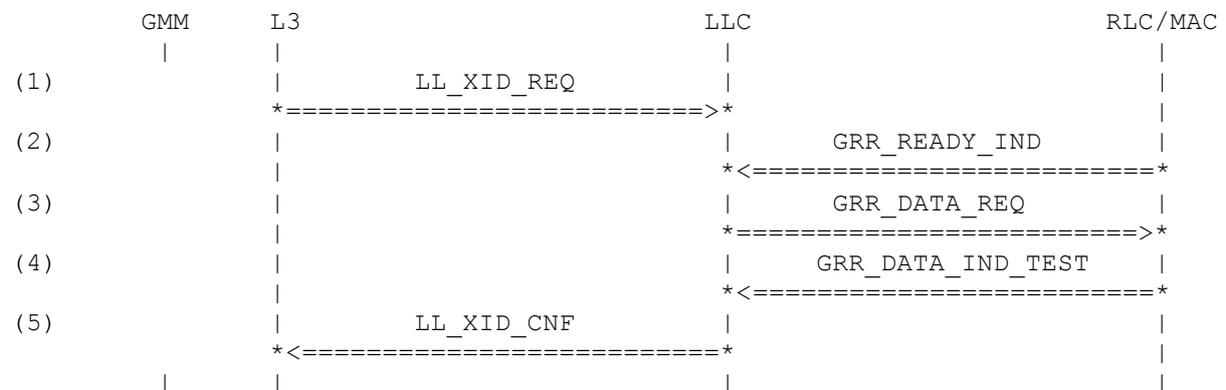
3.3.17 LLC081: Negotiation of XID parameters on SAPI 11 initiated by SNDSCP, network answers with all values set to default.

Description:

LLC is assigned and ready to send data. LLC receives a LL_XID_REQ and therefor sends a XID command message to its peer entity. The peer answers with a XID response including all default values. The layer 3 XID parameters are forwarded to SNDSCP in an LL_XID_CNF.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) LL_XID_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF

	reserved_data_req sdu	NOT_USED XID_CR0_EMPTY_L3_SAPI11
(4) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_CR0_ALL_DEF_SAPI11
(5) LL_XID_CNF	sapi tlli n201_u n201_i sdu	LL_SAPI_11 TLLI_LOCAL_1 N201_U_DEF_SAPI3 N201_I_DEF EMPTY_SDU

History: 11-Oct-2001 GS Initial

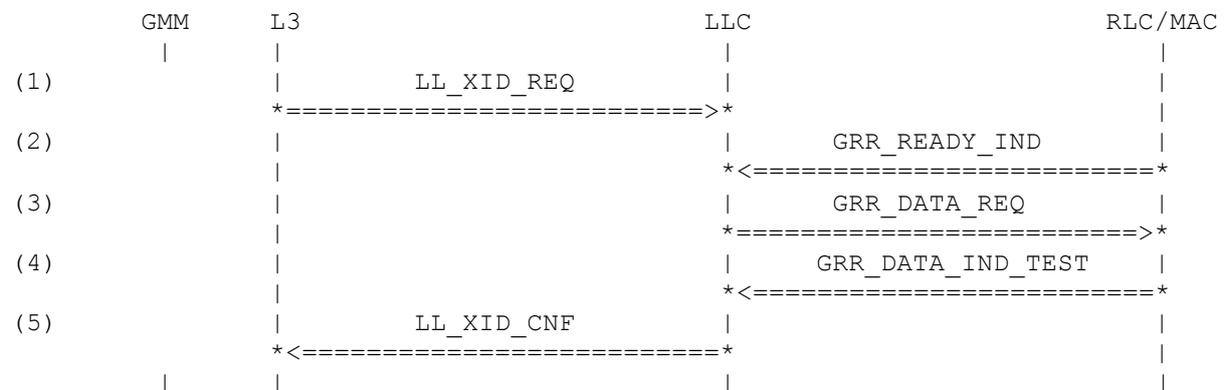
3.3.18 LLC082: Negotiation of XID parameters on SAPI 11 initiated by SNDCP, network answers with all - some changed - values.

Description:

LLC is assigned and ready to send data. LLC receives a LL_XID_REQ and therefor sends a XID command message to its peer entity. The peer answers with a XID response including all default values. The layer 3 XID parameters are forwarded to SNDCP in an LL_XID_CNF.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) LL_XID_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF

	reserved_data_req sdu	NOT_USED XID_CR0_EMPTY_L3_SAPI11
(4) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_CR0_ALL_DIF_SAPI11
(5) LL_XID_CNF	sapi tlli n201_u n201_i sdu	LL_SAPI_11 TLLI_LOCAL_1 N201_U_DEF_SAPI3 N201_I_DEF EMPTY_SDU

History:

11-Oct-2001	GS	Initial
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3.3.19 LLC083: Restart Negotiation after Reset on SAPI 3, 5, 9, 11

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command) containing the parameters IOV UI and reset. The received XID parameter is acceptable. LLC sends, as required, an empty XID response confirming the value. Additional xid commands are sent on sapi 3, 5, 9, 11 because XID parameter are requested.

Preamble:

LLC001

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC	CONFIG	SAPI 3	N201_U 520)
COMMAND	(LLC	CONFIG	SAPI 5	N201_U 520)
COMMAND	(LLC	CONFIG	SAPI 9	N201_U 520)
COMMAND	(LLC	CONFIG	SAPI 11	N201_U 520)
COMMAND	(LLC	CONFIG	SAPI 3	N201_I 520)
COMMAND	(LLC	CONFIG	SAPI 5	N201_I 520)
COMMAND	(LLC	CONFIG	SAPI 9	N201_I 520)
COMMAND	(LLC	CONFIG	SAPI 11	N201_I 520)
(1)			GRR_DATA_IND_TEST	
			*<=====	
(2)		LL_RESET_IND		
		*<=====		
(3)			GRR_READY_IND	
			*<=====	
(4)			GRR_DATA_REQ	
			*=====>	
(5)			GRR_READY_IND	
			*<=====	
(6)			GRR_DATA_REQ	
			*=====>	
(7)			GRR_DATA_IND_TEST	
			*<=====	
(8)		LL_XID_IND		
		*<=====		
(9)			GRR_READY_IND	
			*<=====	
(10)			GRR_DATA_REQ	
			*=====>	
(11)			GRR_DATA_IND_TEST	
			*<=====	
(12)		LL_XID_IND		
		*<=====		
(13)			GRR_READY_IND	
			*<=====	
(14)			GRR_DATA_REQ	
			*=====>	
(15)			GRR_DATA_IND_TEST	
			*<=====	
(16)		LL_XID_IND		
		*<=====		
(17)			GRR_READY_IND	
			*<=====	
(18)			GRR_DATA_REQ	
			*=====>	
(19)			GRR_DATA_IND_TEST	
			*<=====	
(20)		LL_XID_IND		
		*<=====		

Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	XID1_IOV_UI_SAPI1

(2) LL_RESET_IND	sapi tlli	LL_SAPI_1 TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_SAPI1
(5) GRR_READY_IND		
(6) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI3
(7) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_RSP_SAPI3
(8) LL_XID_IND	sapi tlli n201_u n201_j xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_520 NOT_USED LL_XID_INVALID NOT_USED
(9) GRR_READY_IND		
(10) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI5
(11) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_RSP_SAPI5
(12) LL_XID_IND	sapi tlli n201_u n201_j xid_valid sdu	LL_SAPI_5 TLLI_LOCAL_1 N201_U_520 NOT_USED LL_XID_INVALID NOT_USED
(13) GRR_READY_IND		

(14) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI9
(15) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_RSP_SAPI9
(16) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_9 TLLI_LOCAL_1 N201_U_520 NOT_USED LL_XID_INVALID NOT_USED
(17) GRR_READY_IND		
(18) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_11 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI11
(19) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_RSP_SAPI11
(20) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_11 TLLI_LOCAL_1 N201_U_520 NOT_USED LL_XID_INVALID NOT_USED

History:

15-Oct-2002	UT	Initial
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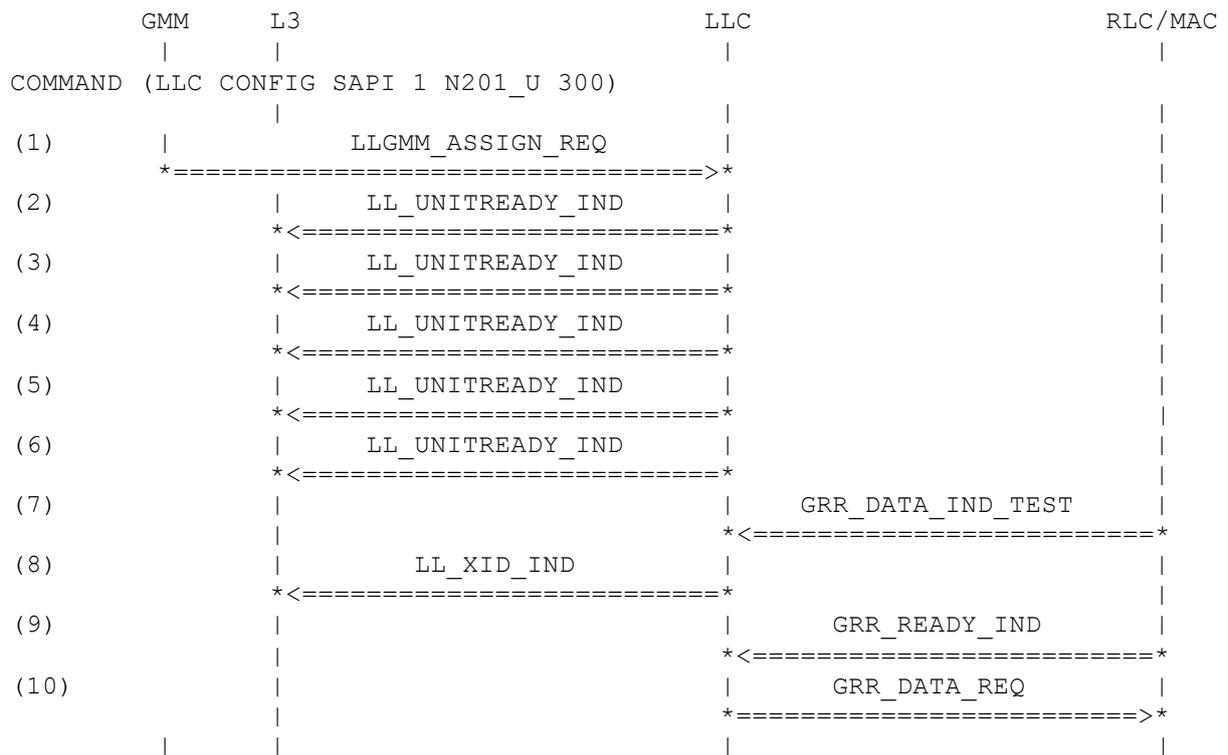
3.3.20 LLC084: Negotiation of acceptable parameters on SAPI 1 (Version, N201-U)

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters are acceptable. LLC sends an XID response containing the same XID parameters except N201_U. The requested parameter by the MS is lower than the requested parameter by the SGSSN.

Preamble:

LLC000



Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	XID1_VE0_NU500_SAPI1
(8) LL_XID_IND	sapi	LL_SAPI_1

	tli	TLLI_LOCAL_1
	n201_u	N201_U_300
	n201_i	NOT_USED
	xid_valid	LL_XID_INVALID
	sdu	NOT_USED
(9) GRR_READY_IND		
(10) GRR_DATA_REQ	sapi	LL_SAPI_1
	tli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID1_VE0_NU300_SAPI1

History:

16-Oct-2002	UT	Initial
27-May-2003	UT	Send LL_UNITREADY_IND for each sapi after LL_GMM_ASSIGN_REQ

3.3.21 LLC085: Restart Negotiation after Reset on SAPI 3, 5, 9, 11, SAPI 1 N201 is included

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command) containing the parameters IOV UI and reset. The received XID parameter is acceptable. LLC sends an XID response containing N201_U on SAPI 1. Additional xid commands are sent on sapi 3, 5, 9, 11 because XID parameter are requested.

Preamble:

LLC001

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC	CONFIG	SAPI 1 N201_U 300)	
COMMAND	(LLC	CONFIG	SAPI 3 N201_U 520)	
COMMAND	(LLC	CONFIG	SAPI 5 N201_U 520)	
COMMAND	(LLC	CONFIG	SAPI 9 N201_U 520)	
COMMAND	(LLC	CONFIG	SAPI 11 N201_U 520)	
COMMAND	(LLC	CONFIG	SAPI 3 N201_I 520)	
COMMAND	(LLC	CONFIG	SAPI 5 N201_I 520)	
COMMAND	(LLC	CONFIG	SAPI 9 N201_I 520)	
COMMAND	(LLC	CONFIG	SAPI 11 N201_I 520)	
(1)			GRR_DATA_IND_TEST	
			*<=====	
(2)		LL_RESET_IND		
		*<=====		
(3)		LL_XID_IND		
		*<=====		
(4)			GRR_READY_IND	
			*<=====	
(5)			GRR_DATA_REQ	
			*=====>	
(6)			GRR_READY_IND	
			*<=====	
(7)			GRR_DATA_REQ	
			*=====>	
(8)			GRR_DATA_IND_TEST	
			*<=====	
(9)		LL_XID_IND		
		*<=====		
(10)			GRR_READY_IND	
			*<=====	
(11)			GRR_DATA_REQ	
			*=====>	
(12)			GRR_DATA_IND_TEST	
			*<=====	
(13)		LL_XID_IND		
		*<=====		
(14)			GRR_READY_IND	
			*<=====	
(15)			GRR_DATA_REQ	
			*=====>	
(16)			GRR_DATA_IND_TEST	
			*<=====	
(17)		LL_XID_IND		
		*<=====		
(18)			GRR_READY_IND	
			*<=====	
(19)			GRR_DATA_REQ	
			*=====>	
(20)			GRR_DATA_IND_TEST	
			*<=====	
(21)		LL_XID_IND		
		*<=====		

Parametrization:

Primitive	Parameter	Value
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(1) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 XID1_IOV_UI_SAPI1
(2) LL_RESET_IND	sapi tli	LL_SAPI_1 TLLI_LOCAL_1
(3) LL_XID_IND	sapi tli n201_u n201_i xid_valid sdu	LL_SAPI_1 TLLI_LOCAL_1 N201_U_300 NOT_USED LL_XID_INVALID NOT_USED
(4) GRR_READY_IND		
(5) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_NU300_SAPI1
(6) GRR_READY_IND		
(7) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI3
(8) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 XID_RSP_SAPI3
(9) LL_XID_IND	sapi tli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_520 NOT_USED LL_XID_INVALID NOT_USED
(10) GRR_READY_IND		
(11) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI5

(12) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_RSP_SAPI5
(13) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_5 TLLI_LOCAL_1 N201_U_520 NOT_USED LL_XID_INVALID NOT_USED
(14) GRR_READY_IND		
(15) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI9
(16) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_RSP_SAPI9
(17) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_9 TLLI_LOCAL_1 N201_U_520 NOT_USED LL_XID_INVALID NOT_USED
(18) GRR_READY_IND		
(19) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_11 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI11
(20) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_RSP_SAPI11
(21) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_11 TLLI_LOCAL_1 N201_U_520 NOT_USED LL_XID_INVALID NOT_USED

History:

16-Oct-2002	UT	Initial
28-May-2003	UT	LL_XID_IND send after LL_RESET_IND

3.3.22 LLC086: Negotiation of acceptable parameters on SAPI 1 (Version, N201-U), received value acceptable

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters are acceptable. LLC sends an XID response containing the same XID parameters because the requested parameter by the MS is greater than the requested parameter by the SGSSN.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC	CONFIG	SAPI 1	N201_U 600)
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)			GRR_DATA_IND_TEST	
			<=====	
(8)		LL_XID_IND		
		<=====		
(9)			GRR_READY_IND	
			<=====	
(10)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli new_tlli llgmm_kc ciphering_algorithm	LLGMM_TLLI_INVALID TLLI_LOCAL_1 LLGMM_KC_NULL LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi tlli	LL_SAPI_3 LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi tlli	LL_SAPI_5 LL_TLLI_INVALID

(4) LL_UNITREADY_IND	sapi tlli	LL_SAPI_7 LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi tlli	LL_SAPI_9 LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi tlli	LL_SAPI_11 LL_TLLI_INVALID
(7) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID1_VE0_NU500_SAPI1
(8) LL_XID_IND	sapi tlli n201_u n201_j xid_valid sdu	LL_SAPI_1 TLLI_LOCAL_1 N201_U_500 NOT_USED LL_XID_INVALID NOT_USED
(9) GRR_READY_IND		
(10) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_VE0_NU500_SAPI1

History:

16-Oct-2002	UT	Initial
27-May-2003	UT	Send LL_UNITREADY_IND for each sapi after LL_GMM_ASSIGN_REQ

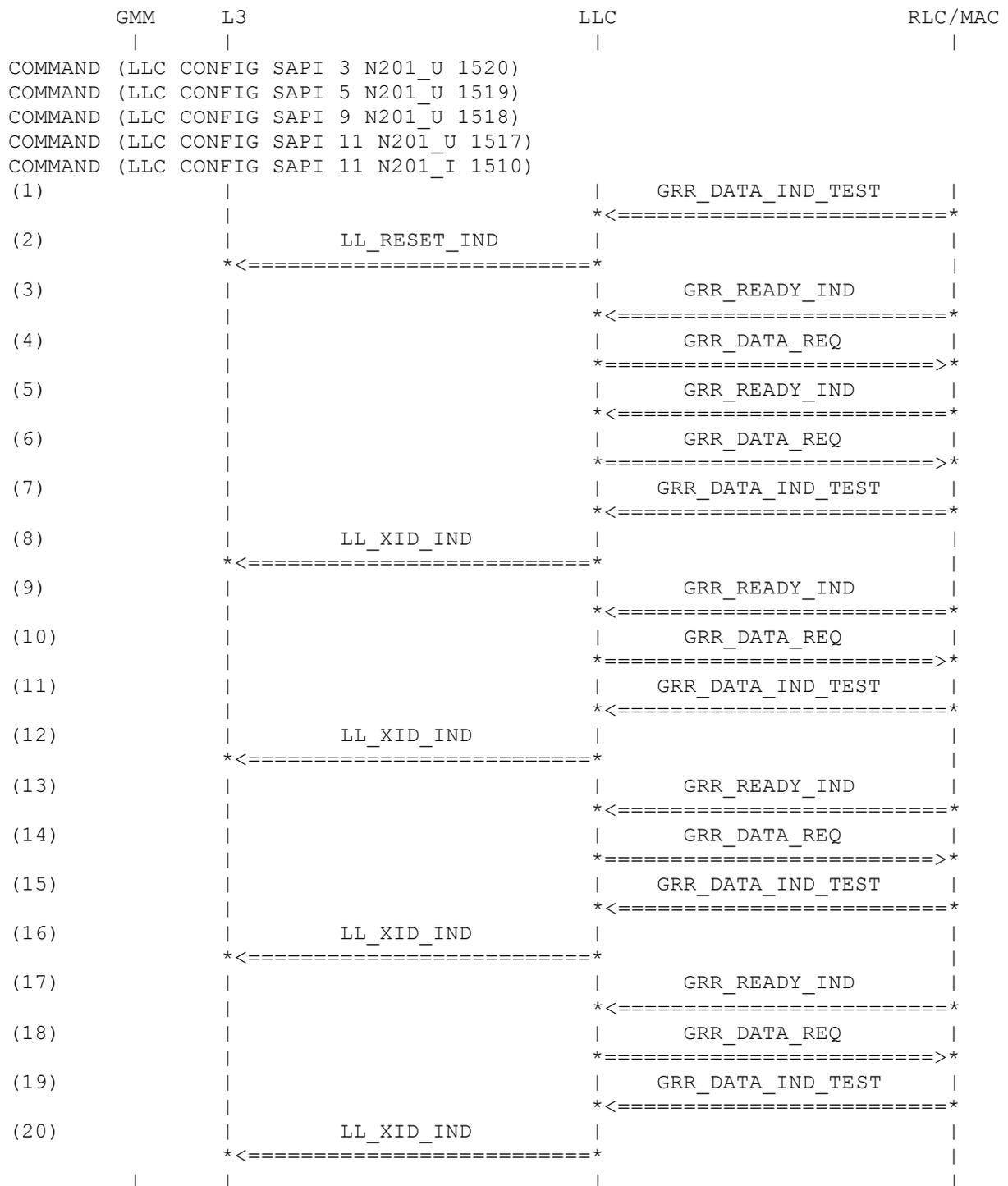
3.3.23 LLC087: Restart Negotiation after Reset on SAPI 3, 5, 9, 11

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command) containing the parameters IOV UI and reset. The received XID parameter is acceptable. LLC sends, as required, an empty XID response confirming the value. Additional xid commands are sent on sapi 3, 5, 9, 11 because XID parameter are requested.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	XID1_IOV_UI_SAPI1
(2) LL_RESET_IND	sapi	LL_SAPI_1
	tli	TLLI_LOCAL_1

(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_SAPI1
(5) GRR_READY_IND		
(6) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI3_N201U_1520
(7) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_SAPI3
(8) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_1520 NOT_USED LL_XID_INVALID NOT_USED
(9) GRR_READY_IND		
(10) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI5_N201U_1519
(11) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_RSP_SAPI5_N201U_1519
(12) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_5 TLLI_LOCAL_1 N201_U_1519 NOT_USED LL_XID_INVALID NOT_USED
(13) GRR_READY_IND		
(14) GRR_DATA_REQ	sapi tlli grr_qos	LL_SAPI_9 TLLI_LOCAL_1 NOT_USED

	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID_CMD_SAPI9_N201U_1518
(15) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	XID_RSP_SAPI9_N201U_1510
(16) LL_XID_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_1510
	n201_i	NOT_USED
	xid_valid	LL_XID_INVALID
	sdu	NOT_USED
(17) GRR_READY_IND		
(18) GRR_DATA_REQ		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID_CMD_SAPI11_N201_U_I_1517_1510
(19) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	XID_RSP_EMPTY
(20) LL_XID_IND		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	n201_u	NOT_USED
	n201_i	NOT_USED
	xid_valid	LL_XID_INVALID
	sdu	NOT_USED

History:

16-Oct-2002	UT	Initial
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3.3.24 LLC088: Negotiation of XID parameters on SAPI 11 initiated by SINDCP, network answers with all values set to default. Attention for XID on SAPI fails due to timeout N200 times.

Description:

LLC is assigned and ready to send data. LLC receives a LL_XID_REQ and therefor sends a XID command message to its peer entity. The peer answers with a XID response including all default values. The layer 3 XID parameters are forwarded to SINDCP in an LL_XID_CNF. There are further requested XID parameters on SAPI 3 and 9. After three retransmissions on sapi 3 there is no response of the peer. An LLGMM_STATUS_IND is send to Layer 3 with cause NO_PEER_RESPONSE.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_U 300)			
COMMAND	(LLC CONFIG SAPI 9 N201_U 400)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)		LL_XID_REQ		
		=====>		
(8)			GRR_READY_IND	
			<=====	
(9)			GRR_DATA_REQ	
			=====>	
(10)			GRR_DATA_IND_TEST	
			<=====	
(11)		LL_XID_CNF		
		<=====		
(12)			GRR_READY_IND	
			<=====	
(13)			GRR_DATA_REQ	
			=====>	
(14)			GRR_READY_IND	
			<=====	
TIMEOUT (5000)				
(15)			GRR_DATA_REQ	
			=====>	
(16)			GRR_READY_IND	
			<=====	
TIMEOUT (5000)				
(17)			GRR_DATA_REQ	
			=====>	
(18)			GRR_READY_IND	
			<=====	
TIMEOUT (5000)				
(19)			GRR_DATA_REQ	
			=====>	
(20)			GRR_READY_IND	
			<=====	
TIMEOUT (5000)				
(21)		LLGMM_STATUS_IND		
		<=====		

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1

	llgmm_kc ciphering_algorithm	LLGMM_KC_NULL LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi tlli	LL_SAPI_3 LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi tlli	LL_SAPI_5 LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi tlli	LL_SAPI_7 LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi tlli	LL_SAPI_9 LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi tlli	LL_SAPI_11 LL_TLLI_INVALID
(7) LL_XID_REQ	sapi tlli sdu	LL_SAPI_11 TLLI_LOCAL_1 EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_11 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CR0_EMPTY_L3_SAPI11
(10) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_CR0_ALL_DEF_SAPI11
(11) LL_XID_CNF	sapi tlli n201_u n201_i sdu	LL_SAPI_11 TLLI_LOCAL_1 N201_U_DEF_SAPI11 N201_I_DEF EMPTY_SDU
(12) GRR_READY_IND		
(13) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_CMD_SAPI3_N201U_300
(14) GRR_READY_IND		
(15) GRR_DATA_REQ	sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID_CMD_SAPI3_N201U_300
(16) GRR_READY_IND		
(17) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID_CMD_SAPI3_N201U_300
(18) GRR_READY_IND		
(19) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	NOT_USED
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID_CMD_SAPI3_N201U_300
(20) GRR_READY_IND		
(21) LLGMM_STATUS_IND		
	error_cause	LLGMM_ERRCS_XID_NO_PEER_RES

History:

11-Oct-2001	UT	Initial
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3.3.25 LLC089: XID Negotiation, XID value is sent in the response until explicitly negotiated.

Description:

LLC receives the primitive GRR_DATA_IND (U frame: XID command). The received XID parameters is IOV_UI. LLC sends an XID response containing the same XID parameters and additionally including N201_U(requested by MS). The N201_U value is send in every response until explicitly negotiated by the peer. The next response doesn't contain this parameter anymore.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_U 300)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)			GRR_DATA_IND_TEST	
			<=====	
(8)		LL_XID_IND		
		<=====		
(9)			GRR_READY_IND	
			<=====	
(10)			GRR_DATA_REQ	
			=====>	
(11)			GRR_DATA_IND_TEST	
			<=====	
(12)		LL_XID_IND		
		<=====		
(13)			GRR_READY_IND	
			<=====	
(14)			GRR_DATA_REQ	
			=====>	
(15)			GRR_DATA_IND_TEST	
			<=====	
(16)		LL_XID_IND		
		<=====		
(17)			GRR_READY_IND	
			<=====	
(18)			GRR_DATA_REQ	
			=====>	
(19)			GRR_DATA_IND_TEST	
			<=====	
(20)			GRR_READY_IND	
			<=====	
(21)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID

(3) LL_UNITREADY_IND	sapi tli	LL_SAPI_5 LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi tli	LL_SAPI_7 LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi tli	LL_SAPI_9 LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi tli	LL_SAPI_11 LL_TLLI_INVALID
(7) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 XID_IOV_UI_SAPI3
(8) LL_XID_IND	sapi tli n201_u n201_j xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_300 NOT_USED LL_XID_INVALID NOT_USED
(9) GRR_READY_IND		
(10) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_NU300_SAPI3
(11) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 XID_SAPI3_NI1520
(12) LL_XID_IND	sapi tli n201_u n201_j xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_300 N201_I_1520 LL_XID_INVALID NOT_USED
(13) GRR_READY_IND		
(14) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_SAPI3_NI1520_NU300

(15) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_NUI1520_NU200_SAPI3
(16) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_200 N201_I_1520 LL_XID_INVALID NOT_USED
(17) GRR_READY_IND		
(18) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_NUI1520_NU200_SAPI3
(19) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID_N5_SAPI3
(20) GRR_READY_IND		
(21) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_N5_SAPI3

History: 28-Oct-2000 UT Initial

3.3.26 LLC090: XID Collision, MS requested N201_U < SGSN received N201_U

Description:

LLC is in state ADM and receives a XID command including an empty block of layer 3 XID parameters. The parameters are forwarded to SNDCCP. While waiting for the response an LL_XID_REQ is received from layer 3. The request is ignored but the requested value is sent within the response.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_U 300)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)			GRR_DATA_IND_TEST	
			<=====	
(8)		LL_XID_IND		
		<=====		
(9)		LL_XID_REQ		
		=====>		
(10)		LL_XID_RES		
		=====>		
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	XID1_VE0_NU400_L3_SAPI3

(8) LL_XID_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_300 NOT_USED LL_XID_VALID EMPTY_SDU
(9) LL_XID_REQ	sapi tlli sdu	LL_SAPI_3 TLLI_LOCAL_1 XID_CMD_SAPI3_N201U_300
(10) LL_XID_RES	sapi tlli sdu	LL_SAPI_3 TLLI_LOCAL_1 EMPTY_SDU
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_NU300_L3_SAPI3

History:

01-Nov-2002	UT	Initial
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3.3.27 LLC091: Layer-3 initiated ABM establishment, UA response with xid parameter

Description:

LLC receives the primitive LL_ESTABLISH_REQ from Sndcp. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with UA :

<A>: containing N201 below default, ABM is established.

: containing all xid values set to min except T200 and N200 set to max, ABM is established.

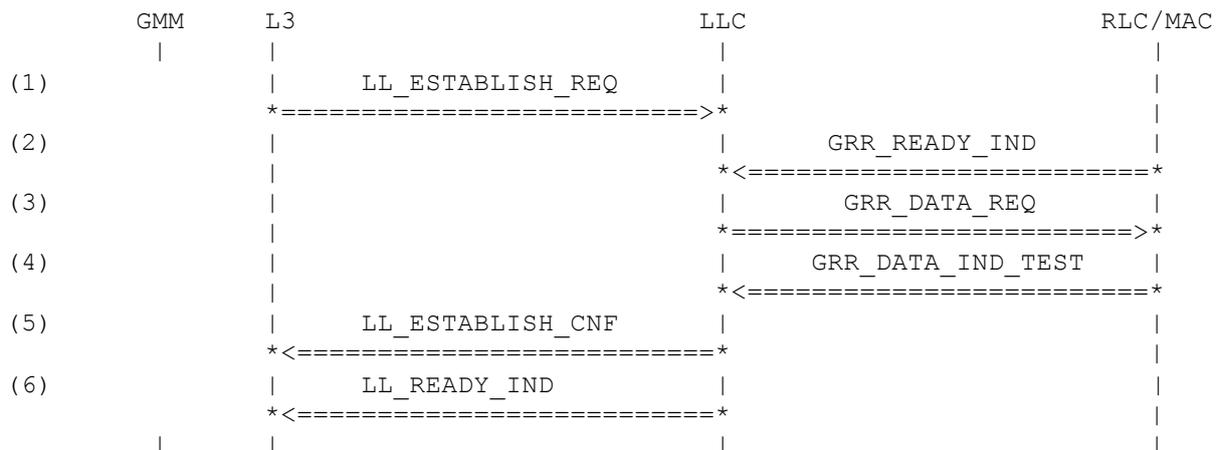
<C>: containing all default values, ABM is established.

Preamble:

LLC001

Variants:

<A>....<C>

**Parametrization:**

Primitive	Parameter	Value
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_9
<C>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_9
<C>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(4) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_N201U_400_SAPI3
	sdu	SGSN_UA1_ALL_MIN_EXCPT_T200_N200_SAPI9
<C>	sdu	SGSN_UA1_ALL_DEF_SAPI11
(5) LL_ESTABLISH_CNF		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_9
<C>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_400
	n201_u	N201_U_MIN
<C>	n201_u	N201_U_DEF_SAPI11
<A>	n201_i	N201_I_DEF
	n201_i	N201_I_MIN

<C>	n201_i xid_valid sdu	N201_I_DEF LL_XID_VALID EMPTY_SDU
(6) LL_READY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_9
<C>	sapi tlli	LL_SAPI_11 TLLI_LOCAL_1

History: 01-Nov-2002 UT

3.3.28 LLC092: Network initiated ABM establishment, SGSN SABM contains N201_U values, UA response with same values

Description:

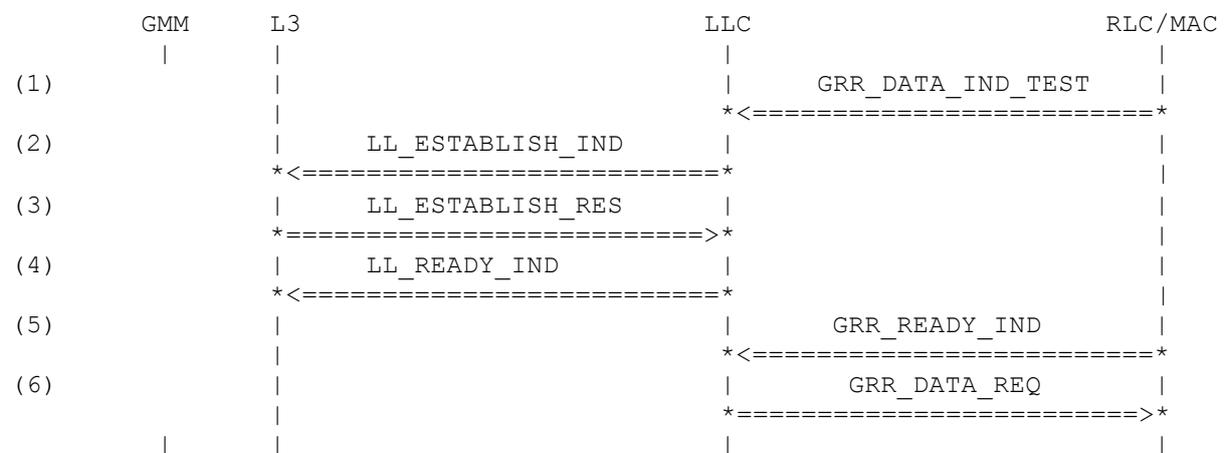
LLC receives the SABM command frame in GRR_DATA_IND_TEST from SDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. No layer-3 XID parameters are included. LLC informs SDCP of the establishment and transmits the UA response to its peer. <A>: N201_U > default. : N201_U < default.

Preamble:

LLC001

Variants:

<A>....



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_SABM_N201U_600_SAPI5
	sdu	SGSN_SABM_N201U_300_SAPI9
(2) LL_ESTABLISH_IND		
<A>	sapi	LL_SAPI_5
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_600
	n201_u	N201_U_300

	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(3) LL_ESTABLISH_RES		
<A>	sapi	LL_SAPI_5
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(4) LL_READY_IND		
<A>	sapi	LL_SAPI_5
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(5) GRR_READY_IND		
(6) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_5
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIQ_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_UA1_N201U_600_SAPI5
	sdu	MS_UA1_N201U_300_SAPI9

History:

03-Nov-2002 UT initial

3.3.29 LLC093: Network initiated ABM establishment, LLC ready, SGSN xid value > than requested value

Description:

LLC receives the SABM command frame in GRR_DATA_IND_TEST from Sndcp. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. Layer-3 XID parameters are included. LLC informs Sndcp of the establishment and transmits the UA response with N201_U, requested on MS side, to its peer.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 5 N201_U 300)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)			GRR_DATA_IND_TEST	
			<=====	
(8)		LL_ESTABLISH_IND		
		<=====		
(9)		LL_ESTABLISH_RES		
		=====>		
(10)		LL_READY_IND		
		<=====		
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1

	sdu	SGSN_SABM_N201U_600_SAPI5
(8) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_5 TLLI_LOCAL_1 N201_U_300 N201_I_DEF LL_XID_VALID EMPTY_SDU
(9) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_5 TLLI_LOCAL_1 LL_XID_VALID EMPTY_SDU
(10) LL_READY_IND	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_UA1_N201U_300_SAPI5

History:

03-Nov-2002	UT	initial
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3.3.30 LLC094: Network initiated ABM establishment with empty L3 parameter, MS UA response with N201_U

Description:

LLC receives the SABM command frame in GRR_DATA_IND_TEST from SNDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. Layer-3 XID parameters are included. LLC informs SNDCP of the establishment and transmits the UA response containing L3 parameters and N201_U (requested on MS side) to its peer.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 11 N201_U 400)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)			GRR_DATA_IND_TEST	
			<=====	
(8)		LL_ESTABLISH_IND		
		<=====		
(9)		LL_ESTABLISH_RES		
		=====>		
(10)		LL_READY_IND		
		<=====		
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1

	sdu	
	SGSN_SABM_EMPTY_L3_XID_SAPI11	
(8) LL_ESTABLISH_IND	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_400
	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(9) LL_ESTABLISH_RES	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(10) LL_READY_IND	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_UA1_N201U_400_SAPI11

History:

03-Nov-2002	UT	initial
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3.3.31 LLC095: Layer-3 initiated ABM establishment with N201_U and empty L3 par., SGSN UA response with same parameters

Description:

LLC receives the primitive LL_ESTABLISH_REQ from SMDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. N201_U is requested. LLC transmits the SABM command, containing empty layer 3 parameters and N201_U, to its peer to establish ABM. The peer responds with UA containing same values.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_U 400)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)		LL_ESTABLISH_REQ		
		=====>		
(8)			GRR_READY_IND	
			<=====	
(9)			GRR_DATA_REQ	
			=====>	
(10)			GRR_DATA_IND_TEST	
			<=====	
(11)		LL_ESTABLISH_CNF		
		<=====		
(12)		LL_READY_IND		
		<=====		

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) LL_ESTABLISH_REQ	sapi	LL_SAPI_3

	tlli sdu	TLLI_LOCAL_1 EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu MS_SABM_EMPTY_L3_XID_NU400_SAPI3	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED
(10) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_N201U_400_SAPI3
(11) LL_ESTABLISH_CNF	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_400 N201_I_DEF LL_XID_VALID EMPTY_SDU
(12) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1

History:

06-Nov-2002 UT

3.3.32 LLC096: Network initiated ABM establishment, LLC ready, SGSN xid value lower than requested

Description:

LLC receives the SABM command frame in GRR_DATA_IND_TEST from SndCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. No layer-3 XID parameters are included. LLC informs SndCP of the establishment and transmits the UA response to its peer.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_U 800)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)			GRR_DATA_IND_TEST	
			<=====	
(8)		LL_ESTABLISH_IND		
		<=====		
(9)		LL_ESTABLISH_RES		
		=====>		
(10)		LL_READY_IND		
		<=====		
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1

	sdu	SGSN_SABM_N201U_400_SAPI3
(8) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_400 N201_I_DEF LL_XID_VALID EMPTY_SDU
(9) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 LL_XID_VALID EMPTY_SDU
(10) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_UA1_N201U_400_SAPI3

History:

06-Nov-2002 UT initial

3.3.33 LLC097: Network initiated ABM establishment, LLC ready, SGSN xid value lower than MS requested value

Description:

LLC receives the SABM command frame in GRR_DATA_IND_TEST from SMDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. No layer-3 XID parameters are included. LLC informs SMDCP of the establishment and transmits the UA response to its peer. The UA response contains the N201_U value which received from peer.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_U 600)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)			GRR_DATA_IND_TEST	
			<=====	
(8)		LL_ESTABLISH_IND		
		<=====		
(9)		LL_ESTABLISH_RES		
		=====>		
(10)		LL_READY_IND		
		<=====		
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1

	sdu	SGSN_SABM_N201U_400_SAPI3
(8) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_400 N201_I_DEF LL_XID_VALID EMPTY_SDU
(9) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 LL_XID_VALID EMPTY_SDU
(10) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_UA1_N201U_400_SAPI3

History:

06-Nov-2002 UT initial

3.3.34 LLC098: XID Collision, MS requested N201_U < SGSN received N201_U

Description:

LLC is in state ADM and receives a XID command including an empty block of layer 3 XID parameters. The parameters are forwarded to SNDCCP. While waiting for the response an LL_XID_REQ is received from layer 3. The request is ignored but the requested value is sent within the response.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_I 520)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)		LL_XID_REQ		
		=====>		
(8)			GRR_READY_IND	
			<=====	
(9)			GRR_DATA_REQ	
			=====>	
(10)			GRR_DATA_IND_TEST	
			<=====	
(11)			GRR_DATA_IND_TEST	
			<=====	
(12)		LL_XID_CNF		
		<=====		

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) LL_XID_REQ	sapi	LL_SAPI_3

	tli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIQ_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	XID_N201I_520_EMPTY_L3_SAPI3
(10) GRR_DATA_IND_TEST		
	tli	TLLI_LOCAL_1
	sdu	XID_CR1_ALL_DEF_SAPI3
(11) GRR_DATA_IND_TEST		
	tli	TLLI_LOCAL_1
	sdu	XID_CR0_ALL_DEF_EXCPT_N201_I_SAPI3
(12) LL_XID_CNF		
	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	n201_u	N201_U_DEF_SAPI3
	n201_i	N201_I_520
	sdu	EMPTY_SDU

History:

01-Nov-2002	UT	Initial
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3.3.35 LLC099: Network initiated ABM establishment, MS requested N201_U < default value of N201_U

Description:

LLC receives the SABM command frame in GRR_DATA_IND_TEST from SndCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC informs SndCP of the establishment and transmits the UA response to its peer. The UA response contains the requested N201_U, value since the value is smaller than the default value

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 5 N201_U 300)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)			GRR_DATA_IND_TEST	
			<=====	
(8)		LL_ESTABLISH_IND		
		<=====		
(9)		LL_ESTABLISH_RES		
		=====>		
(10)		LL_READY_IND		
		<=====		
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1

	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI5
(8) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_5 TLLI_LOCAL_1 N201_U_300 N201_I_DEF LL_XID_VALID EMPTY_SDU
(9) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_5 TLLI_LOCAL_1 LL_XID_VALID EMPTY_SDU
(10) LL_READY_IND	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_UA1_N201U_300_SAPI5

History:

03-Nov-2002	UT	initial
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3.4 Unacknowledged transmission of small frames (LLC100 – LLC149)

3.4.1 LLC100: RLC/MAC unacknowledged mode, Radio Priority 3, RLC/MAC ready

Description:

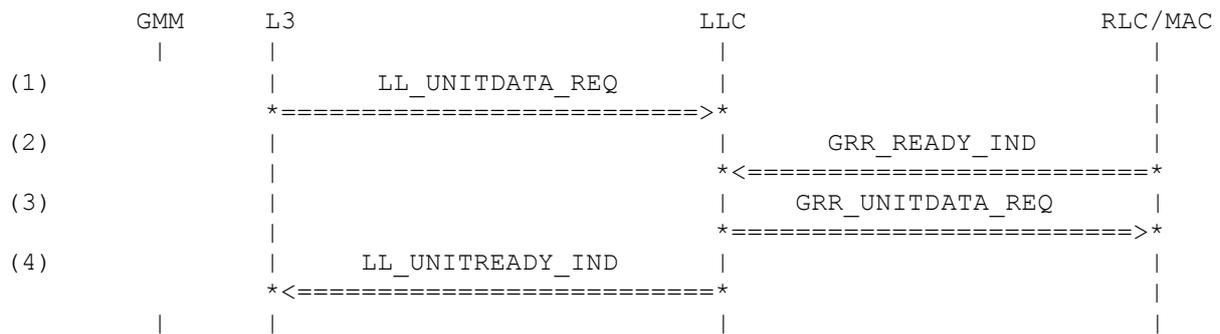
LLC receives data (100 octets) for unacknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an UI frame to RLC/MAC using the primitive GRR_UNITDATA_REQ.

Preamble:

LLC001

Variants:

<A>....<D>

**Parametrization:**

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) GRR_READY_IND		
(3) GRR_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKESSUB
	radio_prio	LL_RADIO_PRIO_3
	reserved_unitdata_req1	NOT_USED
	reserved_unitdata_req2	NOT_USED
<A>	sdu	GRRREQ_SDU100_SAPI3
	sdu	GRRREQ_SDU100_SAPI5
<C>	sdu	GRRREQ_SDU100_SAPI9
<D>	sdu	GRRREQ_SDU100_SAPI11
(4) LL_UNITREADY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

21-Sep-1999	DB	Initial
30-Nov-1999	DB	Removed SAPI 1 (see next test case)
18-Feb-2000	DB	Changed tlli of LL_UNITREADY_IND to NOT_USED
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
12-Sep-2001	GS	SAPI 7 variant removed

3.4.2 LLC110: RLC/MAC acknowledged mode, Radio Priority 3, RLC/MAC ready

Description:

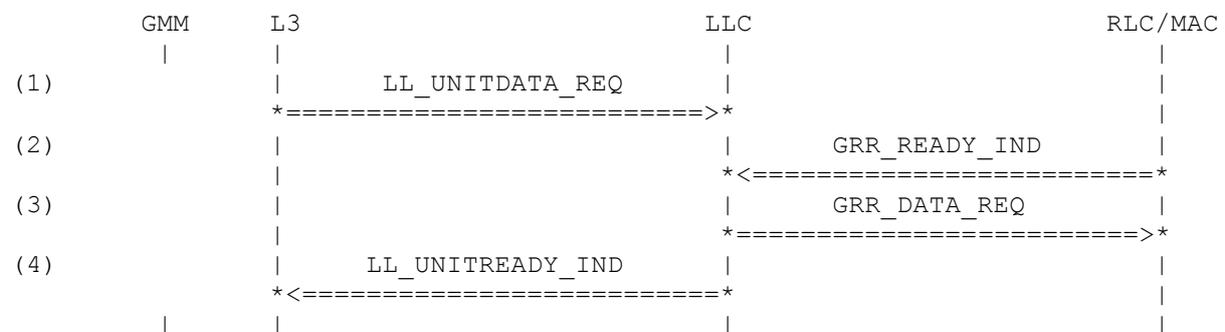
LLC receives data (100 octets) for unacknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an UI frame to RLC/MAC using the primitive GRR_DATA_REQ.

Preamble:

LLC001

Variants:

<A>....<E>



Parametrization:

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9

<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	LL_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	GRRREQ_SDU100_SAPI3_PM
	sdu	GRRREQ_SDU100_SAPI5_PM
<C>	sdu	GRRREQ_SDU100_SAPI7_PM
<D>	sdu	GRRREQ_SDU100_SAPI9_PM
<E>	sdu	GRRREQ_SDU100_SAPI11_PM

(4) LL_UNITREADY_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

21-Sep-1999	DB	Initial
30-Nov-1999	DB	Removed SAPI 1 (see next test case)
18-Feb-2000	DB	Changed tlli of LL_UNITREADY_IND to NOT_USED
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs

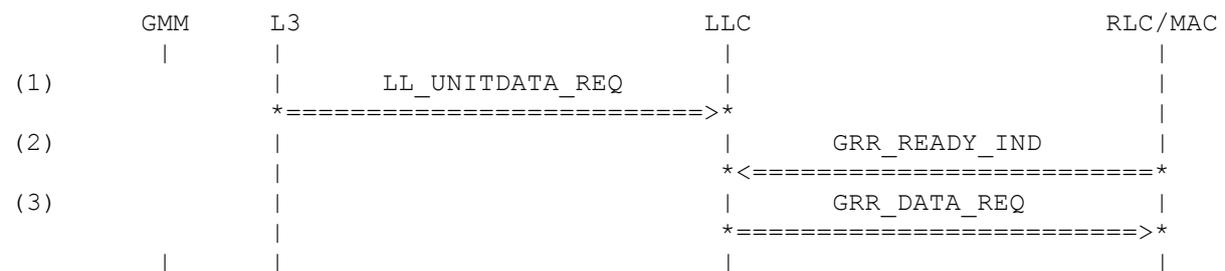
3.4.3 LLC111: SAPI 1, RLC/MAC acknowledged mode, Radio Priority 1, RLC/MAC ready

Description:

LLC receives data (100 octets) for unacknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an UI frame to RLC/MAC using the primitive GRR_DATA_REQ.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
-----------	-----------	-------

(1) LL_UNITDATA_REQ

sapi	LL_SAPI_1
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL3
radio_prio	LL_RADIO_PRIO_1
cipher	LL_CIPHER_OFF
reserved_unitdata_req1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE

	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	LL_RADIO_PPIO_1
	cause	GRR_DTACS_MOBILITY_MANAGEMENT
	reserved_data_req	NOT_USED
	sdu	GRRREQ_SDU100_SAPI1_PM

History:

30-Nov-1999	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
14-Sep-2001	GS	GRR_DATA_REQ cause changed

3.4.4 LLC140: Transmission of several frames for different SAPIs, RLC/MAC ready

Description:

LLC receives data (100 octets) for unacknowledged transmission on every SAPI. RLC/MAC is ready to receive data. LLC sends the received data for each SAPI in an UI frame to RLC/MAC using the primitive GRR_UNITDATA_REQ.

Preamble:

LLC002

	GMM	L3	LLC	RLC/MAC
(1)		LL_UNITDATA_REQ		
		=====>		
(2)		LL_UNITDATA_REQ		
		=====>		
(3)		LL_UNITDATA_REQ		
		=====>		
(4)		LL_UNITDATA_REQ		
		=====>		
(5)		LL_UNITDATA_REQ		
		=====>		
(6)		LL_UNITDATA_REQ		
		=====>		
(7)			GRR_READY_IND	
			<=====	
(8)			GRR_UNITDATA_REQ	
			=====>	
(9)			GRR_READY_IND	
			<=====	
(10)			GRR_UNITDATA_REQ	
			=====>	
(11)		LL_UNITREADY_IND		
		<=====		
(12)			GRR_READY_IND	
			<=====	
(13)			GRR_UNITDATA_REQ	
			=====>	
(14)		LL_UNITREADY_IND		
		<=====		
(15)			GRR_READY_IND	
			<=====	
(16)			GRR_DATA_REQ	
			=====>	
(17)		LL_UNITREADY_IND		
		<=====		
(18)			GRR_READY_IND	
			<=====	
(19)			GRR_UNITDATA_REQ	
			=====>	
(20)		LL_UNITREADY_IND		
		<=====		
(21)			GRR_READY_IND	
			<=====	
(22)			GRR_UNITDATA_REQ	
			=====>	
(23)		LL_UNITREADY_IND		
		<=====		

Parametrization:

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3

	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LL_UNITDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(3) LL_UNITDATA_REQ		
	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(4) LL_UNITDATA_REQ		
	sapi	LL_SAPI_7
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(5) LL_UNITDATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100

(6) LL_UNITDATA_REQ

sapi	LL_SAPI_11
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL5
radio_prio	LL_RADIO_PRIO_3
cipher	LL_CIPHER_OFF
reserved_unitdata_req1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE
reserved_unitdata_req4	NOT_USED
reserved_unitdata_req5	NOT_USED
sdu	LLREQ_SDU100

(7) GRR_READY_IND

(8) GRR_UNITDATA_REQ

sapi	LL_SAPI_1
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	LL_RADIO_PRIO_3
reserved_unitdata_req1	NOT_USED
reserved_unitdata_req2	NOT_USED
sdu	GRRREQ_SDU100_SAPI1

(9) GRR_READY_IND

(10) GRR_UNITDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	LL_RADIO_PRIO_3
reserved_unitdata_req1	NOT_USED
reserved_unitdata_req2	NOT_USED
sdu	GRRREQ_SDU100_SAPI3

(11) LL_UNITREADY_IND

sapi	LL_SAPI_3
tlli	LL_TLLI_INVALID

(12) GRR_READY_IND

(13) GRR_UNITDATA_REQ

sapi	LL_SAPI_5
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	LL_RADIO_PRIO_3
reserved_unitdata_req1	NOT_USED
reserved_unitdata_req2	NOT_USED
sdu	GRRREQ_SDU100_SAPI5

(14) LL_UNITREADY_IND

sapi	LL_SAPI_5
tlli	LL_TLLI_INVALID

(15) GRR_READY_IND

(16) GRR_DATA_REQ

sapi	LL_SAPI_7
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	LL_RADIO_PRIO_3
cause	GRR_DTACS_DEF

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_1
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100

History:

23-Sep-1999	DB	Initial
03-Apr-2000	DB	Corrected SAPI 1 behaviour: reliability class must be 3
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs

3.4.6 LLC146: RLC/MAC ready, transmit buffered frame (RLC/MAC acknowledged mode, Radio Priority 1)

Description:

LLC receives the indication that RLC/MAC is now ready to receive data. LLC sends the already buffered data (see LLC145) with GRR_UNITDATA_REQ.

Preamble:

LLC145



Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
(2) GRR_DATA_REQ	grr_qos	GRRREQ_QOS_PEAUSUB
	radio_prio	LL_RADIO_PRIO_1
	cause	GRR_DTACS_MOBILITY_MANAGEMENT
	reserved_data_req	NOT_USED
	sdu	GRRREQ_SDU100_SAPI1_PM

History:

23-Sep-1999	DB	Initial
30-Nov-1999	DB	Removed flow control for SAPI 1 (LL_UNITREADY_IND)
03-Apr-2000	DB	Corrected SAPI 1 behaviour: reliability class 3 -> GRR_DATA_REQ
22-May-2000	DB	Inserted reserved_* parameter(s)
14-Sep-2001	GS	GRR_DATA_REQ cause changed

3.5 Unacknowledged transmission of large frames (LLC150 – LLC199)

3.5.1 LLC150: RLC/MAC unacknowledged mode, Radio Priority 3, RLC/MAC ready

Description:

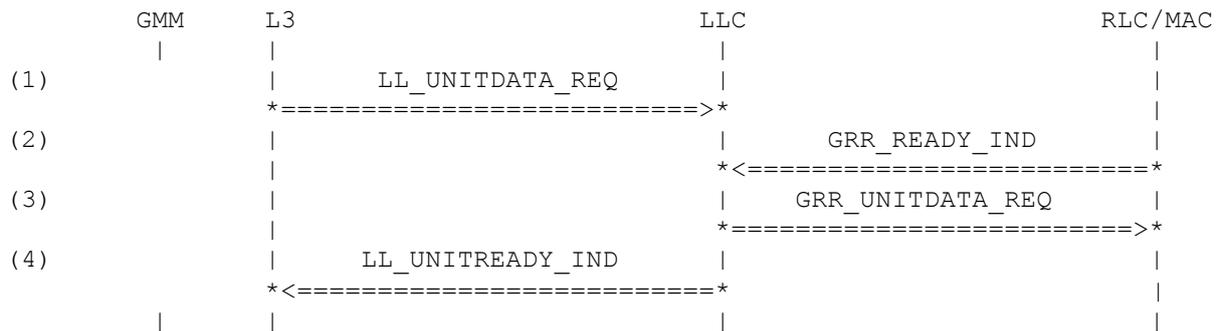
All XID values are set to max. LLC receives data (1000 octets) for unacknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an UI frame to RLC/MAC using the primitive GRR_UNITDATA_REQ.

Variants:

<A>....<D>

Preamble:

<A>LLC059B
 LLC059C
 <C>LLC059E
 <D>LLC059F



Parametrization:

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU1000
(2) GRR_READY_IND		

(3) GRR_UNITDATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	LL_RADIO_PRIO_3
	reserved_unitdata_req1	NOT_USED
	reserved_unitdata_req2	NOT_USED
<A>	sdu	GRRREQ_SDU1000_SAPI3
	sdu	GRRREQ_SDU1000_SAPI5
<C>	sdu	GRRREQ_SDU1000_SAPI9
<D>	sdu	GRRREQ_SDU1000_SAPI11

(4) LL_UNITREADY_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

21-Sep-1999	DB	Initial
30-Nov-1999	DB	Removed SAPI 1 (see next test case)
18-Feb-2000	DB	Changed tlli of LL_UNITREADY_IND to NOT_USED
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
09-Feb-2001	GS	Preamble 59 add instead of 1
12-Sep-2001	GS	SAPI 7 variant removed.

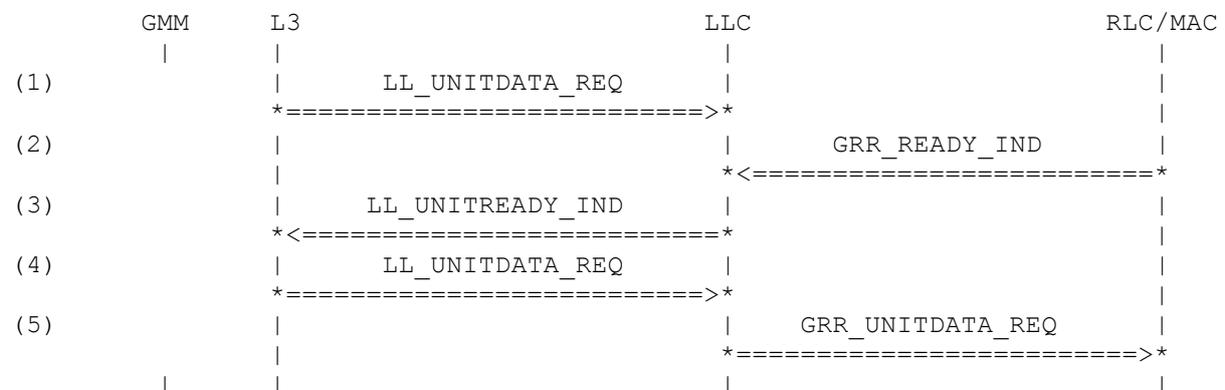
3.5.2 LLC155: RLC/MAC unacknowledged mode, Radio Priority 3, RLC/MAC ready, N201 violation in TX

Description:

All XID values are set to default. LLC receives data (1000 octets) for unacknowledged transmission on sapi 3. RLC/MAC is ready to receive data. LLC does not transmit the frame, because it violates N201-U. Then LLC receives data (100 octets) for unacknowledged transmission. LLC transmits the frame.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
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(1) LL_UNITDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL5
radio_prio	LL_RADIO_PRIO_3
cipher	LL_CIPHER_OFF
reserved_unitdata_req1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE
reserved_unitdata_req4	NOT_USED
reserved_unitdata_req5	NOT_USED
sdu	LLREQ_SDU1000

(2) GRR_READY_IND

(3) LL_UNITREADY_IND

sapi	LL_SAPI_3
tlli	LL_TLLI_INVALID

(4) LL_UNITDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL5
radio_prio	LL_RADIO_PRIO_3
cipher	LL_CIPHER_OFF
reserved_unitdata_req1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE
reserved_unitdata_req4	NOT_USED
reserved_unitdata_req5	NOT_USED
sdu	LLREQ_SDU100

(5) GRR_UNITDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAQSUB
radio_prio	LL_RADIO_PRIO_3
reserved_unitdata_req1	NOT_USED
reserved_unitdata_req2	NOT_USED
sdu	GRRREQ_SDU100_SAPI3_NU1

History:

09-Feb-2001	GS	Initial
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3.5.3 LLC160: RLC/MAC acknowledged mode, Radio Priority 3, RLC/MAC ready

Description:

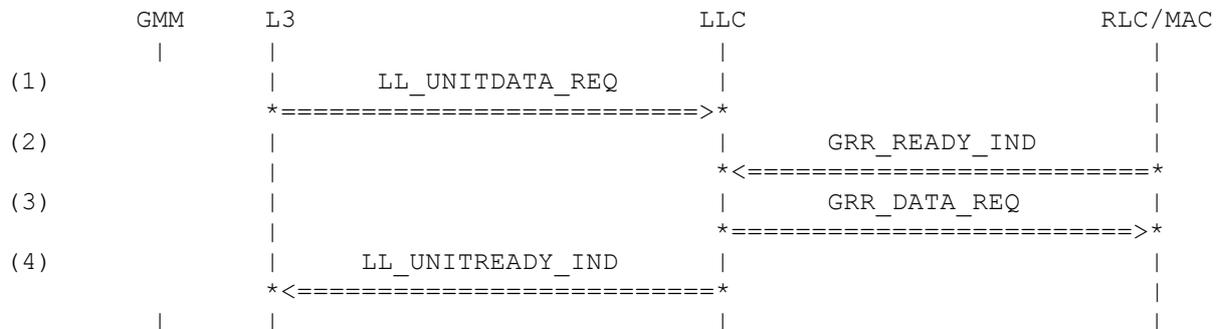
All XID values are set to max. LLC receives data (1000 octets) for unacknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an UI frame to RLC/MAC using the primitive GRR_DATA_REQ.

Variants:

<A>....<E>

Preamble:

<A>LLC059B
 LLC059C
 <C>LLC059D
 <D>LLC059E
 <E>LLC059F



Parametrization:

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU1000
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	LL_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	GRRREQ_SDU1000_SAPI3_PM
	sdu	GRRREQ_SDU1000_SAPI5_PM
<C>	sdu	GRRREQ_SDU1000_SAPI7_PM
<D>	sdu	GRRREQ_SDU1000_SAPI9_PM
<E>	sdu	GRRREQ_SDU1000_SAPI11_PM

(4) LL_UNITREADY_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

21-Sep-1999	DB	Initial
30-Nov-1999	DB	Removed SAPI 1 (see next test case)
18-Feb-2000	DB	Changed tlli of LL_UNITREADY_IND to NOT_USED
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
09-Feb-2001	GS	Preamble modified

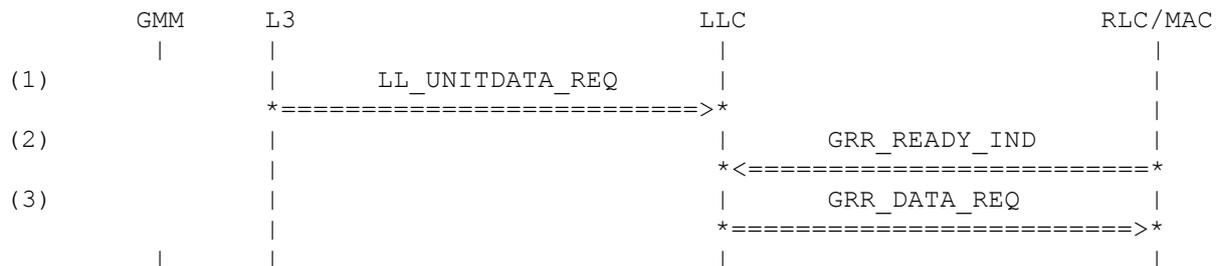
3.5.4 LLC161: SAPI 1, RLC/MAC acknowledged mode, Radio Priority 1, RLC/MAC ready

Description:

All XID values are set to max. LLC receives data (1000 octets) for unacknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an UI frame to RLC/MAC using the primitive GRR_DATA_REQ.

Preamble:

LLC059A



Parametrization:

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_1
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU1000
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	LL_RADIO_PRIO_1

cause	GRR_DTACS_MOBILITY_MANAGEMENT
reserved_data_req	NOT_USED
sdu	GRRREQ_SDU1000_SAPI1_PM

History:

30-Nov-1999	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
09-Feb-2001	GS	Preamble changed to 59A
14-Sep-2001	GS	GRR_DATA_REQ cause changed

3.6 Unacknowledged receipt of small frames (LLC200 – LLC249)

3.6.1 LLC200: RLC/MAC unacknowledged mode, layer 3 ready

Description:

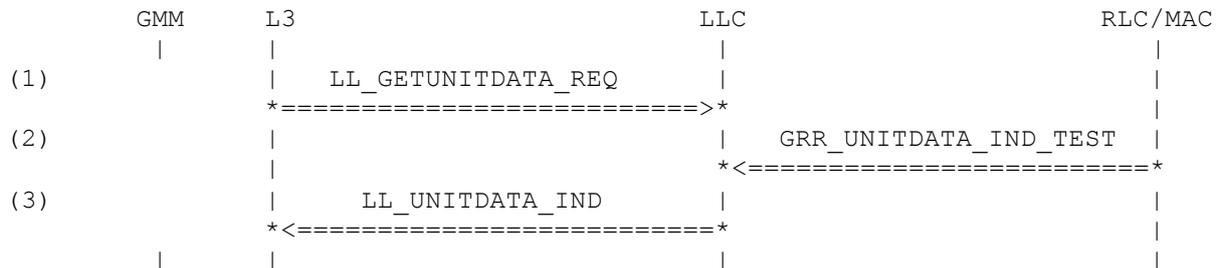
LLC receives unacknowledged data (100 octets) in an UI frame with the primitive GRR_UNITDATA_IND (RLC/MAC unacknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001

Variants:

<A>....<E>



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(2) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_SAPI3
	sdu	GRRIND_DESCLIST100_SAPI5
<C>	sdu	GRRIND_DESCLIST100_SAPI7
<D>	sdu	GRRIND_DESCLIST100_SAPI9
<E>	sdu	GRRIND_DESCLIST100_SAPI11
(3) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5

<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_SAPI3
	sdu	LLIND_SDU100_SAPI5
<C>	sdu	LLIND_SDU100_SAPI7
<D>	sdu	LLIND_SDU100_SAPI9
<E>	sdu	LLIND_SDU100_SAPI11

History:

21-Sep-1999	DB	Initial
30-Nov-1999	DB	Removed SAPI 1 (see next test case)
06-Mar-2000	DB	Moved LL_GETUNITDATA_REQ to the top
22-May-2000	DB	Inserted reserved_* parameter(s)

3.6.2 LLC210: RLC/MAC acknowledged mode, layer 3 ready

Description:

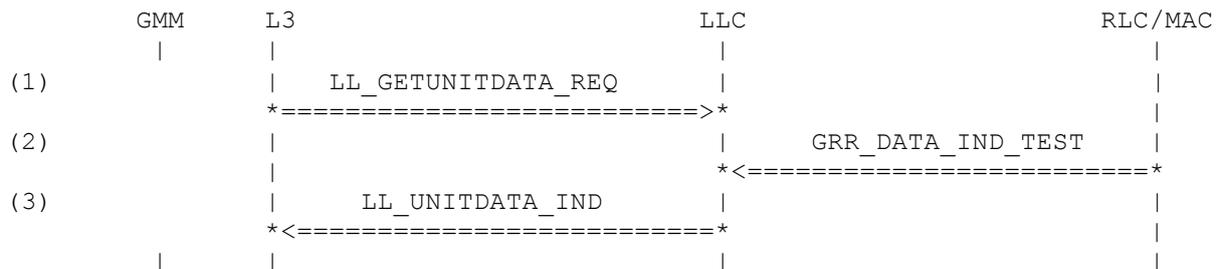
LLC receives unacknowledged data (100 octets) in an UI frame with the primitive GRR_DATA_IND (RLC/MAC acknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001

Variants:

<A>....<E>



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tli	TLLI_LOCAL_1
(2) GRR_DATA_IND_TEST		
<A>	tli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI3

	sdu	GRRIND_DESCLIST100_SAPI5
<C>	sdu	GRRIND_DESCLIST100_SAPI7
<D>	sdu	GRRIND_DESCLIST100_SAPI9
<E>	sdu	GRRIND_DESCLIST100_SAPI11
(3) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_SAPI3
	sdu	LLIND_SDU100_SAPI5
<C>	sdu	LLIND_SDU100_SAPI7
<D>	sdu	LLIND_SDU100_SAPI9
<E>	sdu	LLIND_SDU100_SAPI11

History:

21-Sep-1999	DB	Initial
30-Nov-1999	DB	Removed SAPI 1 (see next test case)
06-Mar-2000	DB	Moved LL_GETUNITDATA_REQ to the top
22-May-2000	DB	Inserted reserved_* parameter(s)

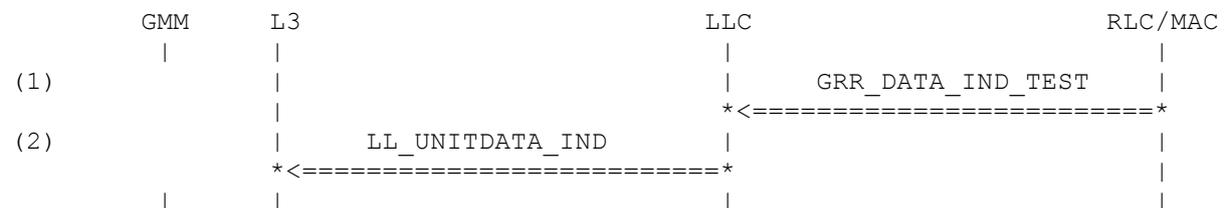
3.6.3 LLC211: SAPI 1, RLC/MAC acknowledged mode, layer 3 ready

Description:

LLC receives unacknowledged data (100 octets) in an UI frame with the primitive GRR_DATA_IND (RLC/MAC acknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI1
(2) LL_UNITDATA_IND	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1

reserved_unitdata_ind1	NOT_USED
reserved_unitdata_ind2	NOT_USED
reserved_unitdata_ind3	NOT_USED
reserved_unitdata_ind4	NOT_USED
reserved_unitdata_ind5	NOT_USED
cipher	LL_CIPHER_OFF
sdu	LLIND_SDU100_SAPI1

History:

30-Nov-1999	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.6.4 LLC240: Receipt of several frames for different SAPIs, layer 3 ready

Description:

LLC receives unacknowledged data (100 octets) in an UI frame for each SAPI with the primitive GRR_UNITDATA_IND (RLC/MAC unacknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001

	GMM	L3	LLC	RLC/MAC
(1)			GRR_UNITDATA_IND_TEST	
			*<=====	
(2)		LL_UNITDATA_IND		
		*<=====		
(3)		LL_GETUNITDATA_REQ		
		*=====>		
(4)			GRR_UNITDATA_IND_TEST	
			*<=====	
(5)		LL_UNITDATA_IND		
		*<=====		
(6)		LL_GETUNITDATA_REQ		
		*=====>		
(7)			GRR_UNITDATA_IND_TEST	
			*<=====	
(8)		LL_UNITDATA_IND		
		*<=====		
(9)		LL_GETUNITDATA_REQ		
		*=====>		
(10)			GRR_UNITDATA_IND_TEST	
			*<=====	
(11)		LL_UNITDATA_IND		
		*<=====		
(12)		LL_GETUNITDATA_REQ		
		*=====>		
(13)			GRR_UNITDATA_IND_TEST	
			*<=====	
(14)		LL_UNITDATA_IND		
		*<=====		
(15)		LL_GETUNITDATA_REQ		
		*=====>		
(16)			GRR_UNITDATA_IND_TEST	
			*<=====	
(17)		LL_UNITDATA_IND		
		*<=====		

Parametrization:

Primitive	Parameter	Value
(1) GRR_UNITDATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI1
(2) LL_UNITDATA_IND	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
(3) LL_GETUNITDATA_REQ	sdu	LLIND_SDU100_SAPI1
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

(4) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI3
(5) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI3
(6) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1
(7) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI5
(8) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_5 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI5
(9) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_7 TLLI_LOCAL_1
(10) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI7
(11) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_7 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI7
(12) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(13) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI9
(14) LL_UNITDATA_IND	sapi	LL_SAPI_9

	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_SAPI9
(15) LL_GETUNITDATA_REQ		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(16) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI11
(17) LL_UNITDATA_IND		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_SAPI11

History:

18-Oct-1999	DB	Initial
30-Nov-1999	DB	Removed flow control for SAPI 1 (LL_GETUNITDATA_REQ)
06-Mar-2000	DB	Moved LL_GETUNITDATA_REQ
22-May-2000	DB	Inserted reserved_* parameter(s)

3.6.5 LLC241: Receipt of several correctly numbered frames for one SAPI, layer 3 ready

Description:

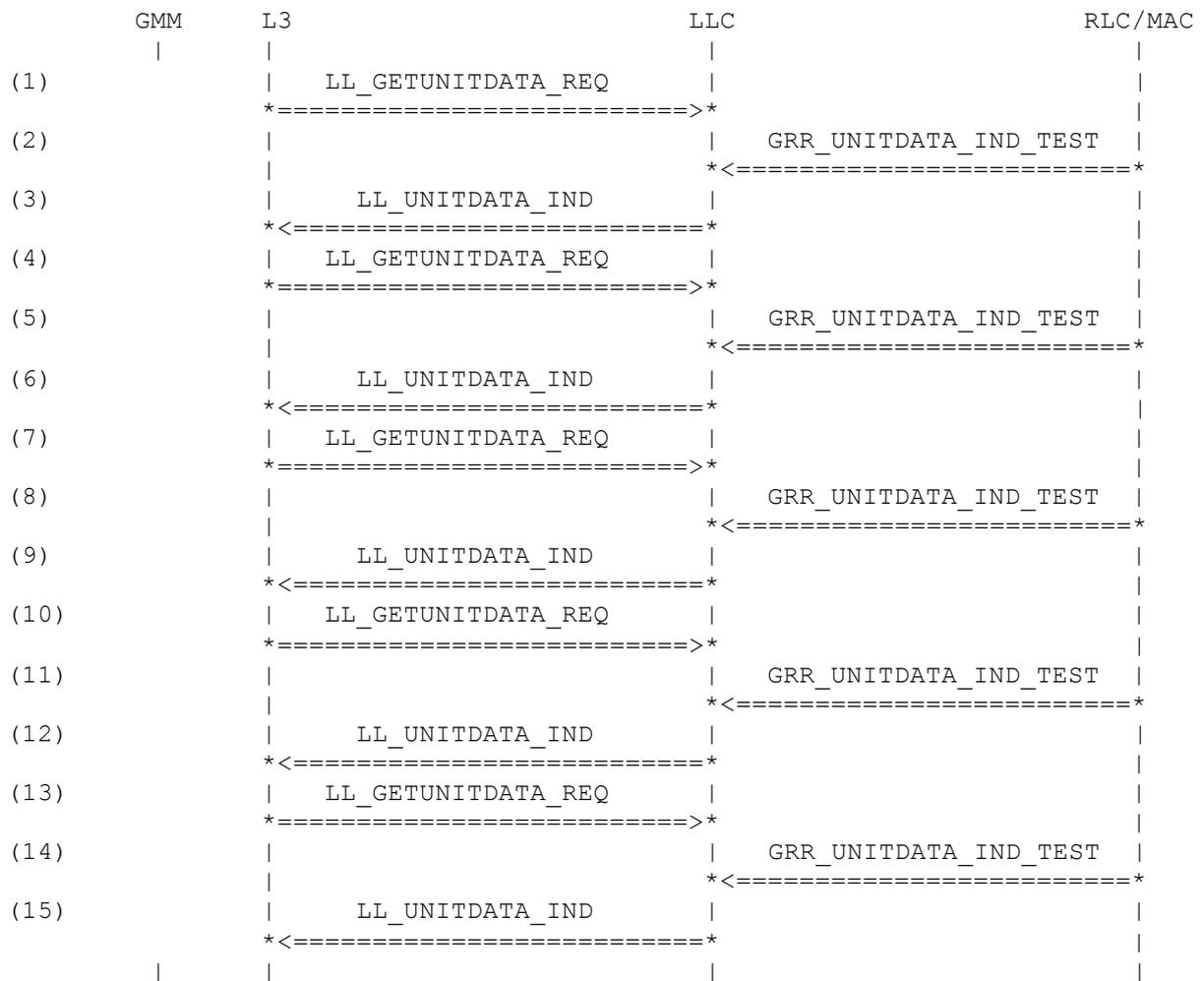
LLC receives unacknowledged data (100 octets) in UI frames with the primitive GRR_UNITDATA_IND (RLC/MAC unacknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001

Variants:

<A>...<E>



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(2) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_SAPI3
	sdu	GRRIND_DESCLIST100_SAPI5
<C>	sdu	GRRIND_DESCLIST100_SAPI7
<D>	sdu	GRRIND_DESCLIST100_SAPI9
<E>	sdu	GRRIND_DESCLIST100_SAPI11
(3) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11

	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_SAPI3
	sdu	LLIND_SDU100_SAPI5
<C>	sdu	LLIND_SDU100_SAPI7
<D>	sdu	LLIND_SDU100_SAPI9
<E>	sdu	LLIND_SDU100_SAPI11
(4) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(5) GRR_UNITDATA_IND_TEST		
<A>	sdu	GRRIND_DESCLIST100_NU1_SAPI3
	sdu	GRRIND_DESCLIST100_NU1_SAPI5
<C>	sdu	GRRIND_DESCLIST100_NU1_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU1_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU1_SAPI11
(6) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU1_SAPI3
	sdu	LLIND_SDU100_NU1_SAPI5
<C>	sdu	LLIND_SDU100_NU1_SAPI7
<D>	sdu	LLIND_SDU100_NU1_SAPI9
<E>	sdu	LLIND_SDU100_NU1_SAPI11
(7) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(8) GRR_UNITDATA_IND_TEST		
<A>	sdu	GRRIND_DESCLIST100_NU2_SAPI3
	sdu	GRRIND_DESCLIST100_NU2_SAPI5

<C>	sdu	GRRIND_DESCLIST100_NU2_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU2_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU2_SAPI11
(9) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU2_SAPI3
	sdu	LLIND_SDU100_NU2_SAPI5
<C>	sdu	LLIND_SDU100_NU2_SAPI7
<D>	sdu	LLIND_SDU100_NU2_SAPI9
<E>	sdu	LLIND_SDU100_NU2_SAPI11
(10) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(11) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_NU3_SAPI3
	sdu	GRRIND_DESCLIST100_NU3_SAPI5
<C>	sdu	GRRIND_DESCLIST100_NU3_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU3_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU3_SAPI11
(12) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU3_SAPI3
	sdu	LLIND_SDU100_NU3_SAPI5
<C>	sdu	LLIND_SDU100_NU3_SAPI7
<D>	sdu	LLIND_SDU100_NU3_SAPI9
<E>	sdu	LLIND_SDU100_NU3_SAPI11
(13) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3

	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(14) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_NU4_SAPI3
	sdu	GRRIND_DESCLIST100_NU4_SAPI5
<C>	sdu	GRRIND_DESCLIST100_NU4_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU4_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU4_SAPI11
(15) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU4_SAPI3
	sdu	LLIND_SDU100_NU4_SAPI5
<C>	sdu	LLIND_SDU100_NU4_SAPI7
<D>	sdu	LLIND_SDU100_NU4_SAPI9
<E>	sdu	LLIND_SDU100_NU4_SAPI11

History:

07-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.6.6 LLC242: Receipt of several incorrectly numbered frames for one SAPI, layer 3 ready

Description:

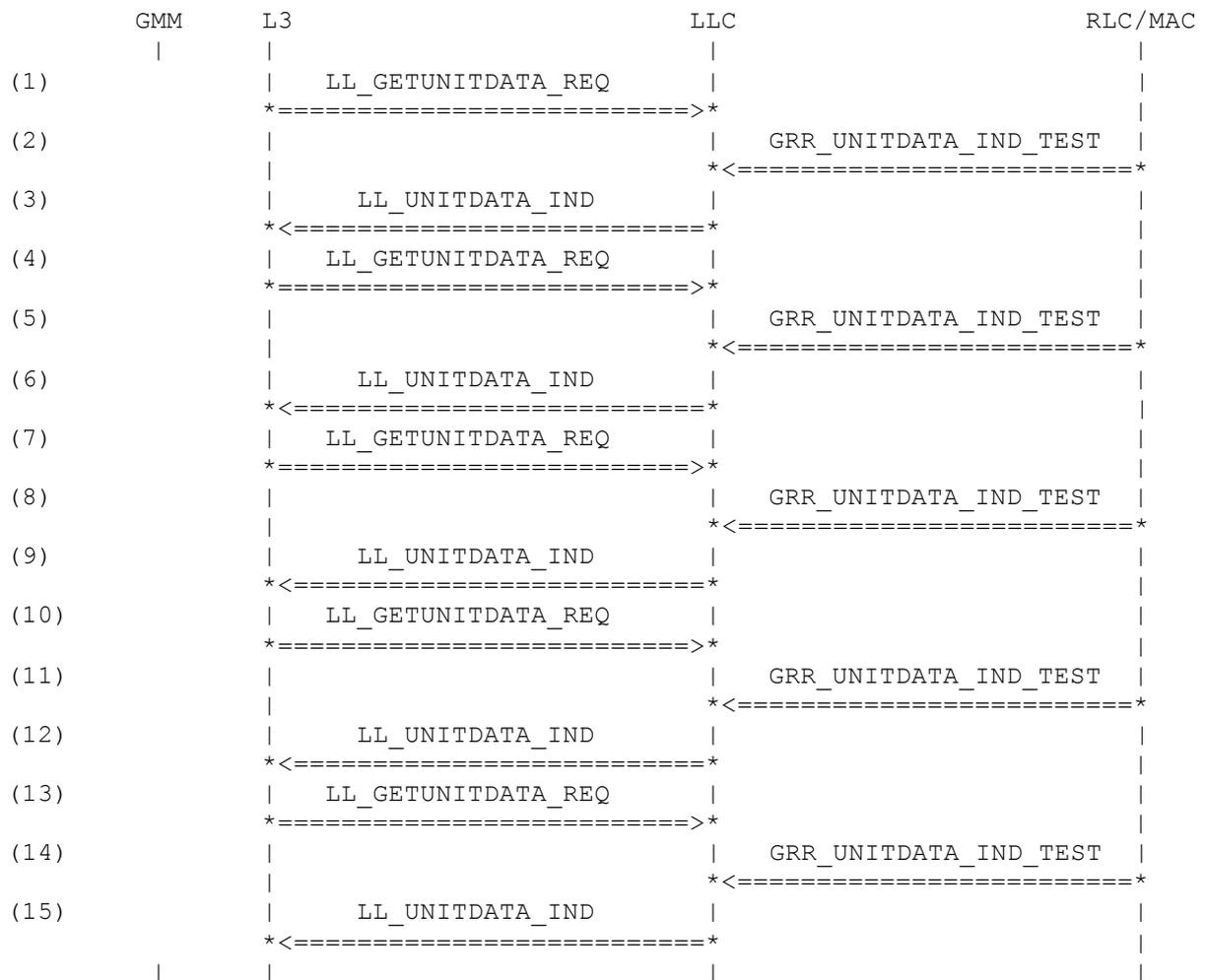
LLC receives unacknowledged data (100 octets) in UI frames with the primitive GRR_UNITDATA_IND (RLC/MAC unacknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001

Variants:

<A>....<E>



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(2) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_SAPI3
	sdu	GRRIND_DESCLIST100_SAPI5
<C>	sdu	GRRIND_DESCLIST100_SAPI7
<D>	sdu	GRRIND_DESCLIST100_SAPI9
<E>	sdu	GRRIND_DESCLIST100_SAPI11
(3) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11

	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_SAPI3
	sdu	LLIND_SDU100_SAPI5
<C>	sdu	LLIND_SDU100_SAPI7
<D>	sdu	LLIND_SDU100_SAPI9
<E>	sdu	LLIND_SDU100_SAPI11
(4) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(5) GRR_UNITDATA_IND_TEST		
<A>	sdu	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_NU1_SAPI3
<C>	sdu	GRRIND_DESCLIST100_NU1_SAPI5
<D>	sdu	GRRIND_DESCLIST100_NU1_SAPI7
<E>	sdu	GRRIND_DESCLIST100_NU1_SAPI9
	sdu	GRRIND_DESCLIST100_NU1_SAPI11
(6) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU1_SAPI3
	sdu	LLIND_SDU100_NU1_SAPI5
<C>	sdu	LLIND_SDU100_NU1_SAPI7
<D>	sdu	LLIND_SDU100_NU1_SAPI9
<E>	sdu	LLIND_SDU100_NU1_SAPI11
(7) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(8) GRR_UNITDATA_IND_TEST		
<A>	sdu	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_NU3_SAPI3
	sdu	GRRIND_DESCLIST100_NU3_SAPI5

<C>	sdu	GRRIND_DESCLIST100_NU3_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU3_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU3_SAPI11
(9) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU3_SAPI3
	sdu	LLIND_SDU100_NU3_SAPI5
<C>	sdu	LLIND_SDU100_NU3_SAPI7
<D>	sdu	LLIND_SDU100_NU3_SAPI9
<E>	sdu	LLIND_SDU100_NU3_SAPI11
(10) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(11) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_NU2_SAPI3
	sdu	GRRIND_DESCLIST100_NU2_SAPI5
<C>	sdu	GRRIND_DESCLIST100_NU2_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU2_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU2_SAPI11
(12) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU2_SAPI3
	sdu	LLIND_SDU100_NU2_SAPI5
<C>	sdu	LLIND_SDU100_NU2_SAPI7
<D>	sdu	LLIND_SDU100_NU2_SAPI9
<E>	sdu	LLIND_SDU100_NU2_SAPI11
(13) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3

	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(14) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_NU4_SAPI3
	sdu	GRRIND_DESCLIST100_NU4_SAPI5
<C>	sdu	GRRIND_DESCLIST100_NU4_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU4_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU4_SAPI11
(15) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU4_SAPI3
	sdu	LLIND_SDU100_NU4_SAPI5
<C>	sdu	LLIND_SDU100_NU4_SAPI7
<D>	sdu	LLIND_SDU100_NU4_SAPI9
<E>	sdu	LLIND_SDU100_NU4_SAPI11

History:

07-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.6.7 LLC243: Receipt of several duplicated numbered frames for one SAPI, layer 3 ready

Description:

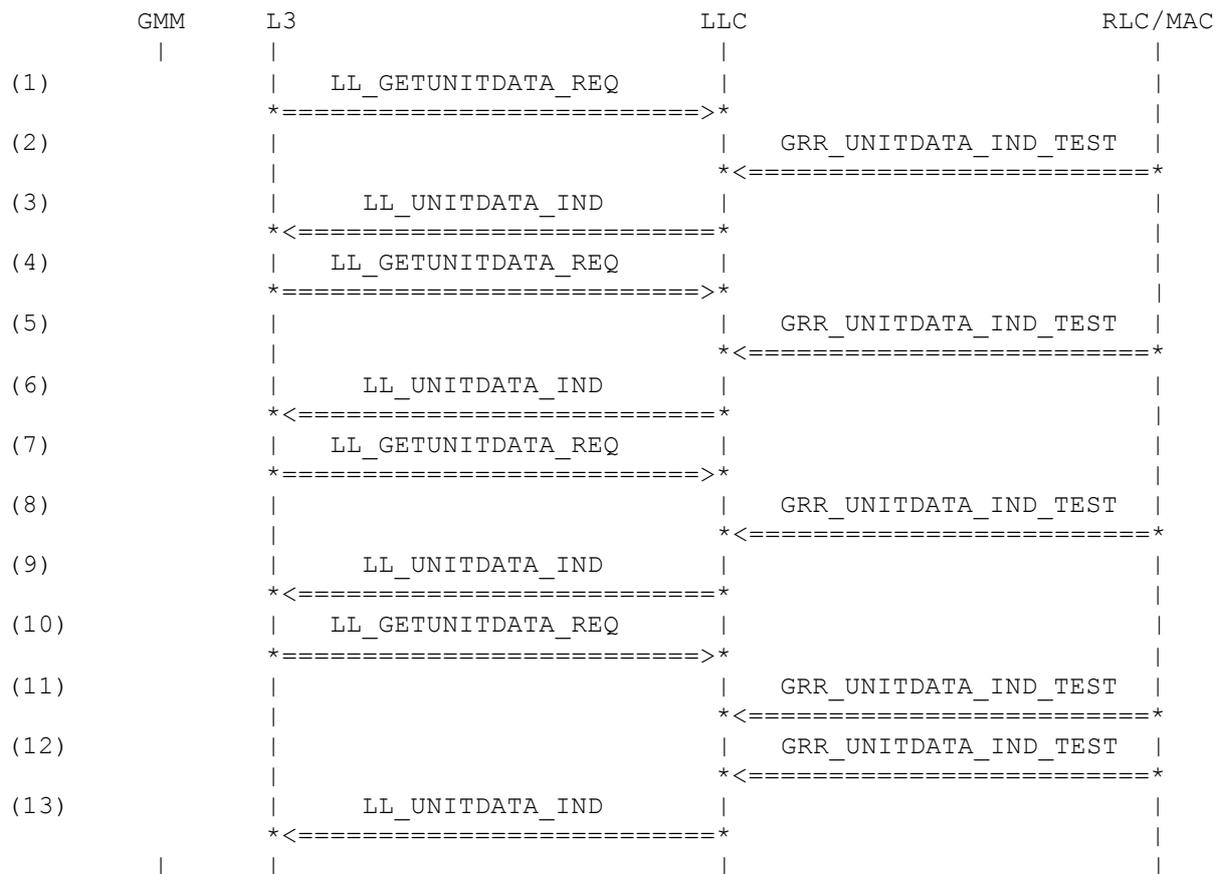
LLC receives unacknowledged data (100 octets) in UI frames with the primitive GRR_UNITDATA_IND (RLC/MAC unacknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001

Variants:

<A>....<E>



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(2) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_SAPI3
	sdu	GRRIND_DESCLIST100_SAPI5
<C>	sdu	GRRIND_DESCLIST100_SAPI7
<D>	sdu	GRRIND_DESCLIST100_SAPI9
<E>	sdu	GRRIND_DESCLIST100_SAPI11
(3) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED

	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_SAPI3
	sdu	LLIND_SDU100_SAPI5
<C>	sdu	LLIND_SDU100_SAPI7
<D>	sdu	LLIND_SDU100_SAPI9
<E>	sdu	LLIND_SDU100_SAPI11
(4) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(5) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_NU1_SAPI3
	sdu	GRRIND_DESCLIST100_NU1_SAPI5
<C>	sdu	GRRIND_DESCLIST100_NU1_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU1_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU1_SAPI11
(6) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU1_SAPI3
	sdu	LLIND_SDU100_NU1_SAPI5
<C>	sdu	LLIND_SDU100_NU1_SAPI7
<D>	sdu	LLIND_SDU100_NU1_SAPI9
<E>	sdu	LLIND_SDU100_NU1_SAPI11
(7) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(8) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_NU2_SAPI3
	sdu	GRRIND_DESCLIST100_NU2_SAPI5
<C>	sdu	GRRIND_DESCLIST100_NU2_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU2_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU2_SAPI11

(9) LL_UNITDATA_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU2_SAPI3
	sdu	LLIND_SDU100_NU2_SAPI5
<C>	sdu	LLIND_SDU100_NU2_SAPI7
<D>	sdu	LLIND_SDU100_NU2_SAPI9
<E>	sdu	LLIND_SDU100_NU2_SAPI11

(10) LL_GETUNITDATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

(11) GRR_UNITDATA_IND_TEST

	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_NU2_SAPI3
	sdu	GRRIND_DESCLIST100_NU2_SAPI5
<C>	sdu	GRRIND_DESCLIST100_NU2_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU2_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU2_SAPI11

(12) GRR_UNITDATA_IND_TEST

	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_NU3_SAPI3
	sdu	GRRIND_DESCLIST100_NU3_SAPI5
<C>	sdu	GRRIND_DESCLIST100_NU3_SAPI7
<D>	sdu	GRRIND_DESCLIST100_NU3_SAPI9
<E>	sdu	GRRIND_DESCLIST100_NU3_SAPI11

(13) LL_UNITDATA_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_NU3_SAPI3
	sdu	LLIND_SDU100_NU3_SAPI5
<C>	sdu	LLIND_SDU100_NU3_SAPI7

<D> sdu LLIND_SDU100_NU3_SAPI9
 <E> sdu LLIND_SDU100_NU3_SAPI11

History:

07-Mar-2000 DB Initial
 22-May-2000 DB Inserted reserved_* parameter(s)

3.6.8 LLC245: RLC/MAC unacknowledged mode, layer 3 not ready, RLC/MAC unacknowledged mode, layer 3 ready

Description:

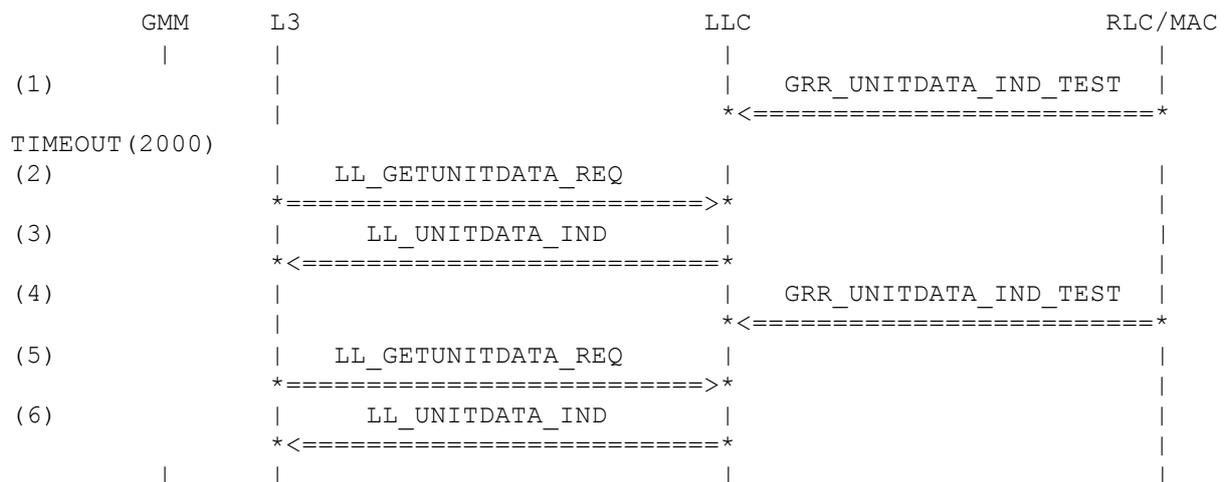
LLC receives unacknowledged data (100 octets) in an UI frame with the primitive GRR_UNITDATA_IND (RLC/MAC unacknowledged mode, N(U) = 0). Layer 3 is not ready to receive data. LLC queues the frame. LLC receives the indication, that layer 3 is now ready to receive data and forwards the queued frame to layer 3. LLC receives again unacknowledged data (100 octets) in an UI frame with the primitive GRR_DATA_IND (RLC/MAC unacknowledged mode, N(U) = 1). LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001

Variants:

<A>...<E>



Parametrization:

Primitive	Parameter	Value
(1) GRR_UNITDATA_IND_TEST		
<A>	tli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI3
<C>	sdu	GRRIND_DESCLIST100_SAPI5
<D>	sdu	GRRIND_DESCLIST100_SAPI7
<E>	sdu	GRRIND_DESCLIST100_SAPI9
	sdu	GRRIND_DESCLIST100_SAPI11
(2) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tli	TLLI_LOCAL_1

(3) LL_UNITDATA_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_SAPI3
	sdu	LLIND_SDU100_SAPI5
<C>	sdu	LLIND_SDU100_SAPI7
<D>	sdu	LLIND_SDU100_SAPI9
<E>	sdu	LLIND_SDU100_SAPI11

(4) GRR_UNITDATA_IND_TEST

	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100A_NU1_SAPI3
	sdu	GRRIND_DESCLIST100A_NU1_SAPI5
<C>	sdu	GRRIND_DESCLIST100A_NU1_SAPI7
<D>	sdu	GRRIND_DESCLIST100A_NU1_SAPI9
<E>	sdu	GRRIND_DESCLIST100A_NU1_SAPI11

(5) LL_GETUNITDATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

(6) LL_UNITDATA_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100A_SAPI3
	sdu	LLIND_SDU100A_SAPI5
<C>	sdu	LLIND_SDU100A_SAPI7
<D>	sdu	LLIND_SDU100A_SAPI9
<E>	sdu	LLIND_SDU100A_SAPI11

History:

06-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
20-Jul-2001	GS	Stack behaviour changed.

3.7 Unacknowledged receipt of large frames (LLC250 – LLC299)

3.7.1 LLC250: RLC/MAC unacknowledged mode, layer 3 ready

Description:

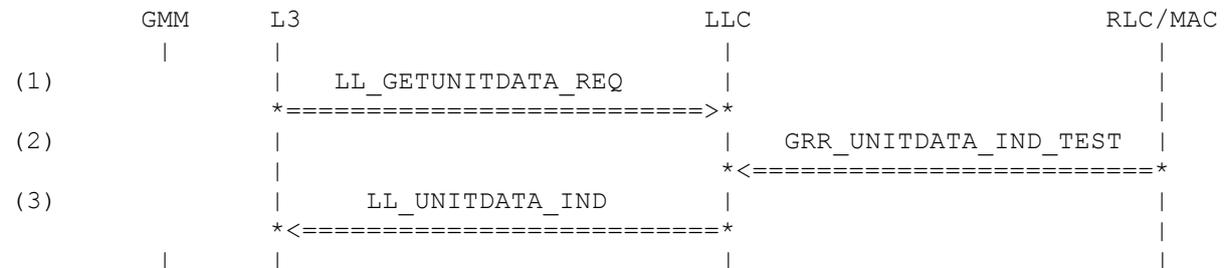
LLC receives unacknowledged data (1000 octets) in an UI frame with the primitive GRR_UNITDATA_IND (RLC/MAC unacknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001

Variants:

<A>....<E>



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(2) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST1000_SAPI3
	sdu	GRRIND_DESCLIST1000_SAPI5
<C>	sdu	GRRIND_DESCLIST1000_SAPI7
<D>	sdu	GRRIND_DESCLIST1000_SAPI9
<E>	sdu	GRRIND_DESCLIST1000_SAPI11
(3) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED

	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU1000_SAPI3
	sdu	LLIND_SDU1000_SAPI5
<C>	sdu	LLIND_SDU1000_SAPI7
<D>	sdu	LLIND_SDU1000_SAPI9
<E>	sdu	LLIND_SDU1000_SAPI11

History:

21-Sep-1999	DB	Initial
30-Nov-1999	DB	Removed SAPI 1 (see next test case)
06-Mar-2000	DB	Moved LL_GETUNITDATA_REQ to the top
22-May-2000	DB	Inserted reserved_* parameter(s)

3.7.2 LLC260: RLC/MAC acknowledged mode, layer 3 ready

Description:

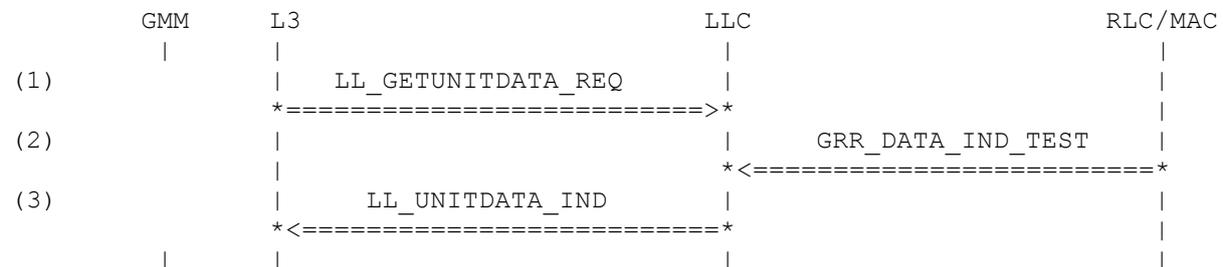
LLC receives unacknowledged data (1000 octets) in an UI frame with the primitive GRR_DATA_IND (RLC/MAC acknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001

Variants:

<A>....<E>



Parametrization:

Primitive	Parameter	Value
(1) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(2) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST1000_SAPI3
	sdu	GRRIND_DESCLIST1000_SAPI5
<C>	sdu	GRRIND_DESCLIST1000_SAPI7
<D>	sdu	GRRIND_DESCLIST1000_SAPI9
<E>	sdu	GRRIND_DESCLIST1000_SAPI11

(3) LL_UNITDATA_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU1000_SAPI3
	sdu	LLIND_SDU1000_SAPI5
<C>	sdu	LLIND_SDU1000_SAPI7
<D>	sdu	LLIND_SDU1000_SAPI9
<E>	sdu	LLIND_SDU1000_SAPI11

History:

21-Sep-1999	DB	Initial
30-Nov-1999	DB	Removed SAPI 1 (see next test case)
06-Mar-2000	DB	Moved LL_GETUNITDATA_REQ to the top
22-May-2000	DB	Inserted reserved_* parameter(s)

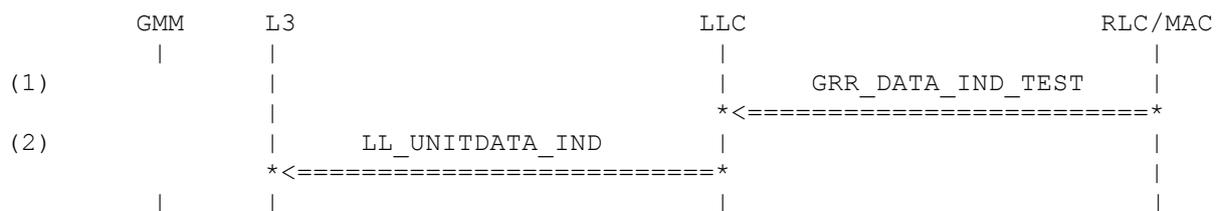
3.7.3 LLC261: SAPI 1, RLC/MAC acknowledged mode, layer 3 ready

Description:

LLC receives unacknowledged data (1000 octets) in an UI frame with the primitive GRR_DATA_IND (RLC/MAC acknowledged mode). Layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST1000_SAPI1
(2) LL_UNITDATA_IND	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
reserved_unitdata_ind5	NOT_USED	

cipher	LL_CIPHER_OFF
sdu	LLIND_SDU1000_SAPI1

History:

30-Nov-1999	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.8 Unacknowledged data transmission/receipt of small frames (LLC300 – LLC349)

3.8.1 LLC300: Transmission/Receipt of several frames for different SAPIs, RLC/MAC and layer 3 ready

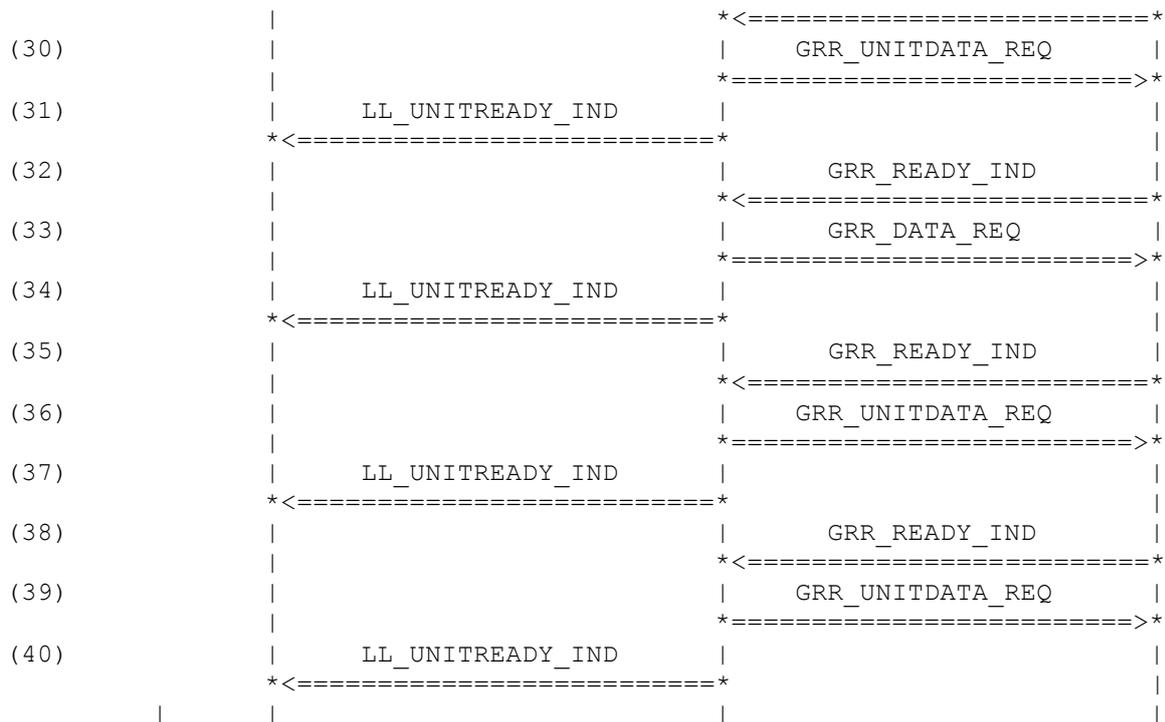
Description:

LLC receives data (100 octets) for unacknowledged transmission on every SAPI. RLC/MAC is not ready to receive data. LLC receives unacknowledged data (100 octets) in an UI frame for each SAPI with the primitive GRR_UNITDATA_IND (RLC/MAC unacknowledged mode) and layer 3 is ready to receive data. LLC passes the data to layer 3 with the primitive LL_UNITDATA_IND. After that, RLC/MAC indicates that is ready to receive data. LLC sends the initially received data for each SAPI in an UI frame to RLC/MAC using the primitive GRR_UNITDATA_REQ.

Preamble:

LLC002

	GMM	L3	LLC	RLC/MAC
(1)		LL_UNITDATA_REQ		
		=====>		
(2)		LL_UNITDATA_REQ		
		=====>		
(3)		LL_UNITDATA_REQ		
		=====>		
(4)		LL_UNITDATA_REQ		
		=====>		
(5)		LL_UNITDATA_REQ		
		=====>		
(6)		LL_UNITDATA_REQ		
		=====>		
(7)			GRR_UNITDATA_IND_TEST	
			<=====	
(8)		LL_UNITDATA_IND		
		<=====		
(9)		LL_GETUNITDATA_REQ		
		=====>		
(10)			GRR_UNITDATA_IND_TEST	
			<=====	
(11)		LL_UNITDATA_IND		
		<=====		
(12)		LL_GETUNITDATA_REQ		
		=====>		
(13)			GRR_UNITDATA_IND_TEST	
			<=====	
(14)		LL_UNITDATA_IND		
		<=====		
(15)		LL_GETUNITDATA_REQ		
		=====>		
(16)			GRR_UNITDATA_IND_TEST	
			<=====	
(17)		LL_UNITDATA_IND		
		<=====		
(18)		LL_GETUNITDATA_REQ		
		=====>		
(19)			GRR_UNITDATA_IND_TEST	
			<=====	
(20)		LL_UNITDATA_IND		
		<=====		
(21)		LL_GETUNITDATA_REQ		
		=====>		
(22)			GRR_UNITDATA_IND_TEST	
			<=====	
(23)		LL_UNITDATA_IND		
		<=====		
(24)			GRR_READY_IND	
			<=====	
(25)			GRR_DATA_REQ	
			=====>	
(26)			GRR_READY_IND	
			<=====	
(27)			GRR_UNITDATA_REQ	
			=====>	
(28)		LL_UNITREADY_IND		
		<=====		
(29)			GRR_READY_IND	



Parametrization:

Primitive	Parameter	Value	
(1) LL_UNITDATA_REQ	sapi	LL_SAPI_1	
	tli	TLLI_LOCAL_1	
	ll_qos	LLREQ_QOS_DEL4_REL3	
	radio_prio	LL_RADIO_PRIO_1	
	cipher	LL_CIPHER_OFF	
	reserved_unitdata_req1	NOT_USED	
	seg_pos	NOT_USED	
	attached_counter	LLC_NO_ATTACHE	
	reserved_unitdata_req4	NOT_USED	
	reserved_unitdata_req5	NOT_USED	
	sdu	LLREQ_SDU100	
	(2) LL_UNITDATA_REQ	sapi	LL_SAPI_3
		tli	TLLI_LOCAL_1
ll_qos		LLREQ_QOS_DEL4_REL5	
radio_prio		LL_RADIO_PRIO_3	
cipher		LL_CIPHER_OFF	
reserved_unitdata_req1		NOT_USED	
seg_pos		NOT_USED	
attached_counter		LLC_NO_ATTACHE	
reserved_unitdata_req4		NOT_USED	
reserved_unitdata_req5		NOT_USED	
sdu		LLREQ_SDU100	
(3) LL_UNITDATA_REQ		sapi	LL_SAPI_5
		tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5	
	radio_prio	LL_RADIO_PRIO_3	

	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(4) LL_UNITDATA_REQ		
	sapi	LL_SAPI_7
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(5) LL_UNITDATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(6) LL_UNITDATA_REQ		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(7) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI1
(8) LL_UNITDATA_IND		
	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED

	cipher sdu	LL_CIPHER_OFF LLIND_SDU100_SAPI1
(9) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(10) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI3
(11) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI3
(12) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1
(13) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI5
(14) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_5 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI5
(15) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_7 TLLI_LOCAL_1
(16) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI7
(17) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_7 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI7
(18) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1

(19) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI9
(20) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_9 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI9
(21) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_11 TLLI_LOCAL_1
(22) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI11
(23) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_11 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI11
(24) GRR_READY_IND		
(25) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB LL_RADIO_PRIO_1 GRR_DTACS_MOBILITY_MANAGEMENT NOT_USED GRRREQ_SDU100_SAPI1_PM
(26) GRR_READY_IND		
(27) GRR_UNITDATA_REQ	sapi tlli grr_qos radio_prio reserved_unitdata_req1 reserved_unitdata_req2 sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB LL_RADIO_PRIO_3 NOT_USED NOT_USED GRRREQ_SDU100_SAPI3
(28) LL_UNITREADY_IND	sapi tlli	LL_SAPI_3 LL_TLLI_INVALID
(29) GRR_READY_IND		

(30) GRR_UNITDATA_REQ	sapi tlli grr_qos radio_prio reserved_unitdata_req1 reserved_unitdata_req2 sdu	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB LL_RADIO_PRIO_3 NOT_USED NOT_USED GRRREQ_SDU100_SAPI5
(31) LL_UNITREADY_IND	sapi tlli	LL_SAPI_5 LL_TLLI_INVALID
(32) GRR_READY_IND		
(33) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_7 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB LL_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRRREQ_SDU100_SAPI7_PM
(34) LL_UNITREADY_IND	sapi tlli	LL_SAPI_7 LL_TLLI_INVALID
(35) GRR_READY_IND		
(36) GRR_UNITDATA_REQ	sapi tlli grr_qos radio_prio reserved_unitdata_req1 reserved_unitdata_req2 sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB LL_RADIO_PRIO_3 NOT_USED NOT_USED GRRREQ_SDU100_SAPI9
(37) LL_UNITREADY_IND	sapi tlli	LL_SAPI_9 LL_TLLI_INVALID
(38) GRR_READY_IND		
(39) GRR_UNITDATA_REQ	sapi tlli grr_qos radio_prio reserved_unitdata_req1 reserved_unitdata_req2 sdu	LL_SAPI_11 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB LL_RADIO_PRIO_3 NOT_USED NOT_USED GRRREQ_SDU100_SAPI11
(40) LL_UNITREADY_IND	sapi tlli	LL_SAPI_11 LL_TLLI_INVALID

History:

18-Oct-1999	DB	Initial
30-Nov-1999	DB	Removed flow control for SAPI 1
18-Feb-2000	DB	Changed tlli of LL_UNITREADY_IND to NOT_USED
06-Mar-2000	DB	Moved LL_GETUNITDATA_REQ
03-Apr-2000	DB	Corrected SAPI 1 behaviour: reliability class 3 -> GRR_DATA_REQ
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
12-Sep-2001	GS	Use GRR_DATA_REQ in case of SAPI 7
14-Sep-2001	GS	GRR_DATA_REQ cause changed

3.9 ABM establishment (LLC400 – LLC429)

3.9.1 LLC400: Layer-3 initiated ABM establishment, UA response

Description:

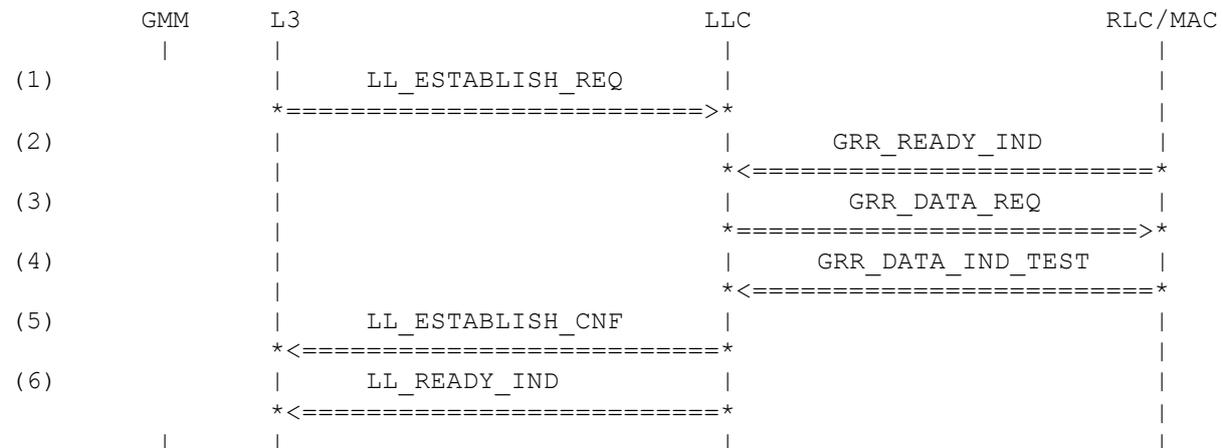
LLC receives the primitive LL_ESTABLISH_REQ from SNDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with UA, ABM is established.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5

<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(4) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI3
	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI5
<C>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI9
<D>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI11
(5) LL_ESTABLISH_CNF		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_DEF_SAPI3
	n201_u	N201_U_DEF_SAPI5
<C>	n201_u	N201_U_DEF_SAPI9
<D>	n201_u	N201_U_DEF_SAPI11
	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(6) LL_READY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

History:

18-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
11-Oct-2001	GS	Add empty L3 to sdu content

3.9.2 LLC401: Layer-3 initiated ABM establishment, N200 timeouts with retransmissions, UA response

Description:

LLC receives the primitive LL_ESTABLISH_REQ from SMDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. T200 timeout, LLC retransmits the SABM command. After N200 retransmissions responds the peer with UA, ABM is established.

NOTE:

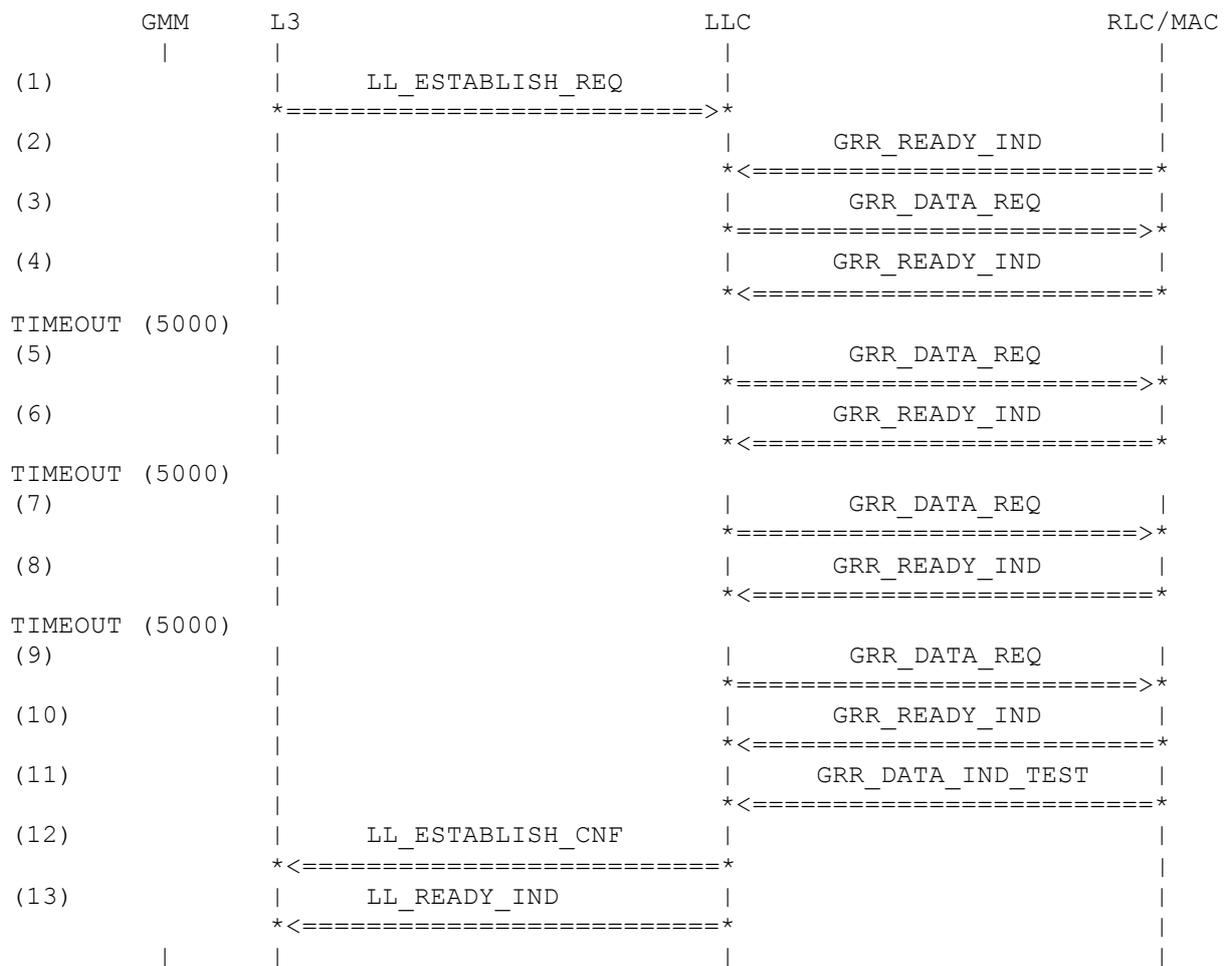
All T200 timer values are set to 5 seconds in simulation.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5

<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(4) GRR_READY_IND		
(5) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(6) GRR_READY_IND		
(7) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(8) GRR_READY_IND		
(9) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(10) GRR_READY_IND		
(11) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI3
	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI5
<C>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI9
<D>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI11

(12) LL_ESTABLISH_CNF

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_DEF_SAPI3
	n201_u	N201_U_DEF_SAPI5
<C>	n201_u	N201_U_DEF_SAPI9
<D>	n201_u	N201_U_DEF_SAPI11
	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU

(13) LL_READY_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

History:

18-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
23-Feb-2001	GS	LL_READY_IND added
11-Oct-2001	GS	Add empty L3 to sdu content

3.9.3 LLC402: Layer-3 initiated ABM establishment, N200 timeouts with retransmissions, no UA response

Description:

LLC receives the primitive LL_ESTABLISH_REQ from SNDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. T200 timeout, LLC retransmits the SABM command. No response from the peer, so ABM establishment is aborted.

NOTE:

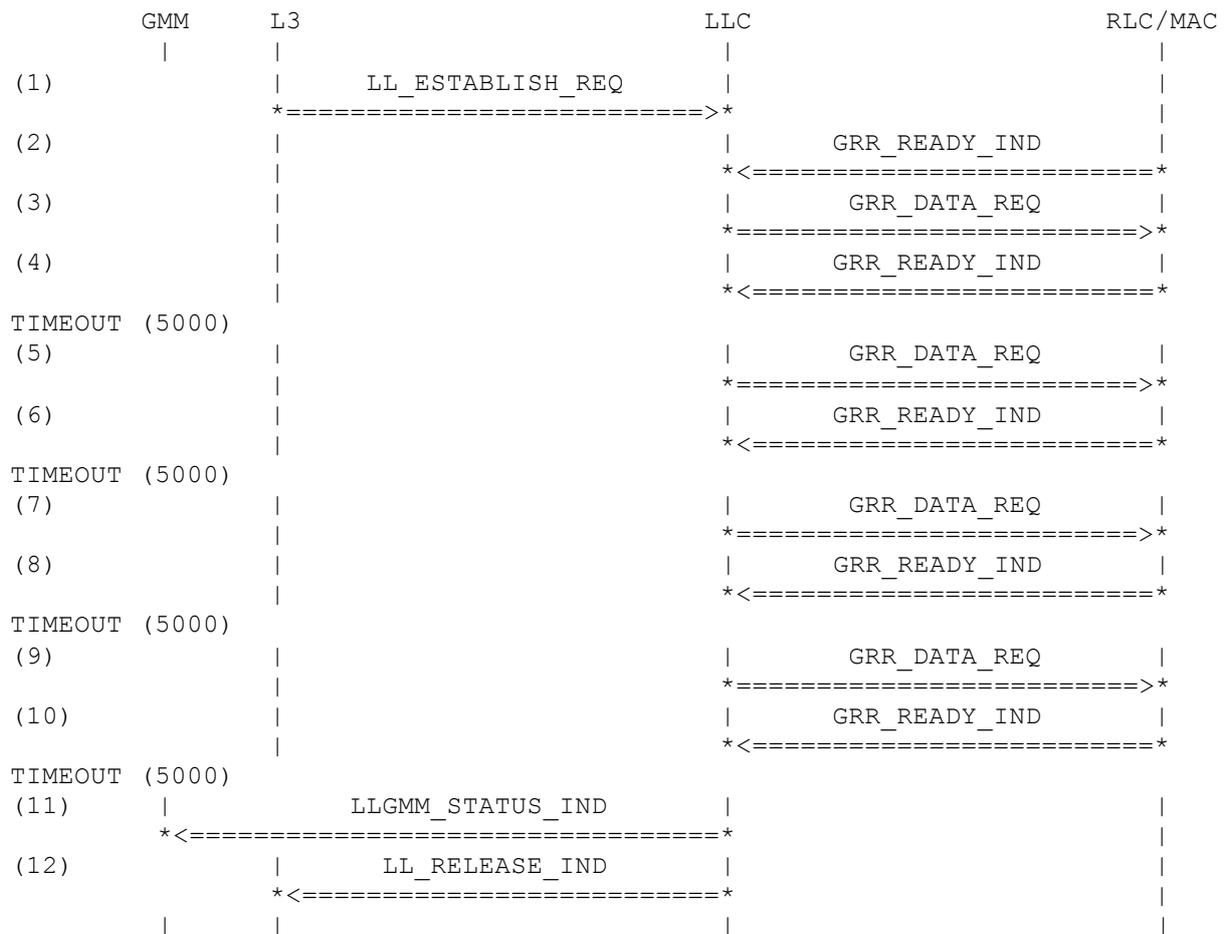
All T200 timer values are set to 5 seconds in simulation.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQ
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11

(4) GRR_READY_IND

(5) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11

(6) GRR_READY_IND

(7) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11

(8) GRR_READY_IND

(9) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11

(10) GRR_READY_IND

(11) LLGMM_STATUS_IND

error_cause
LLGMM_ERRCS_SABM_NO_PEER_RES

(12) LL_RELEASE_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11

tlli
cause
TLLI_LOCAL_1
LL_RELCS_NO_PEER_RES

History:

18-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
11-Oct-2001	GS	Add empty L3 to sdu content

3.9.4 LLC403: Layer-3 initiated ABM establishment, DM response

Description:

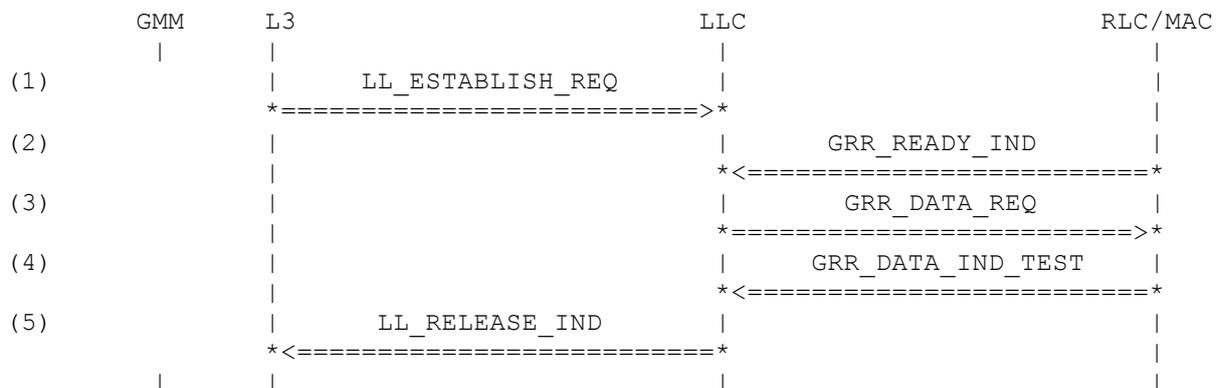
LLC receives the primitive LL_ESTABLISH_REQ from SNDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with DM, ABM establishment is aborted.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3

	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(4) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	SGSN_DM1_SAPI3
	sdu	SGSN_DM1_SAPI5
<C>	sdu	SGSN_DM1_SAPI9
<D>	sdu	SGSN_DM1_SAPI11
(5) LL_RELEASE_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	cause	LL_RELCS_DM_RECEIVED

History:

18-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
11-Oct-2001	GS	Add empty L3 to sdu content

3.9.5 LLC404: Layer-3 initiated ABM establishment, DM response (F bit 0), timeout with retransmission, DM response (F bit 1)

Description:

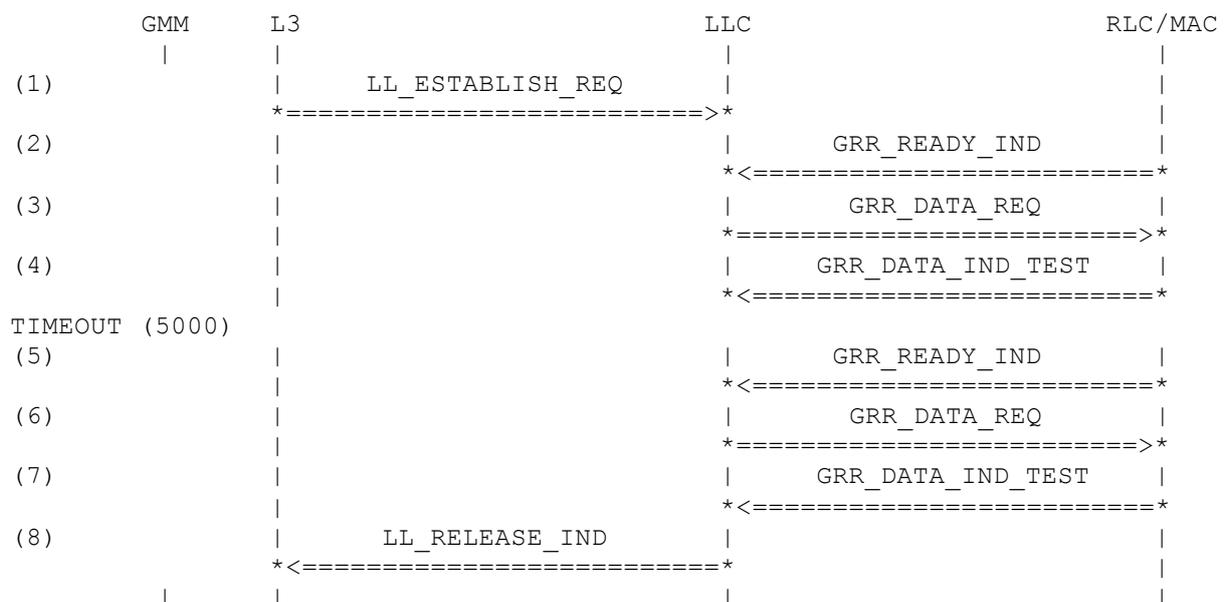
LLC receives the primitive LL_ESTABLISH_REQ from SNDSCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with DM (F bit 0) which is discarded. Timeout T200, LLC retransmits the SABM command. The peer responds with DM, ABM establishment is aborted.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

<u>Primitive</u>	<u>Parameter</u>	<u>Value</u>
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(4) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM0_SAPI3
<C>	sdu	SGSN_DM0_SAPI5
<D>	sdu	SGSN_DM0_SAPI9
	sdu	SGSN_DM0_SAPI11
(5) GRR_READY_IND		
(6) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(7) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM1_SAPI3
<C>	sdu	SGSN_DM1_SAPI5
<D>	sdu	SGSN_DM1_SAPI9
	sdu	SGSN_DM1_SAPI11
(8) LL_RELEASE_IND		
<A>	sapi	LL_SAPI_3

	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	cause	LL_RELCS_DM_RECEIVED

History:

03-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
11-Oct-2001	GS	Add empty L3 to sdu content

3.9.6 LLC410: Network initiated ABM establishment, LLC ready

Description:

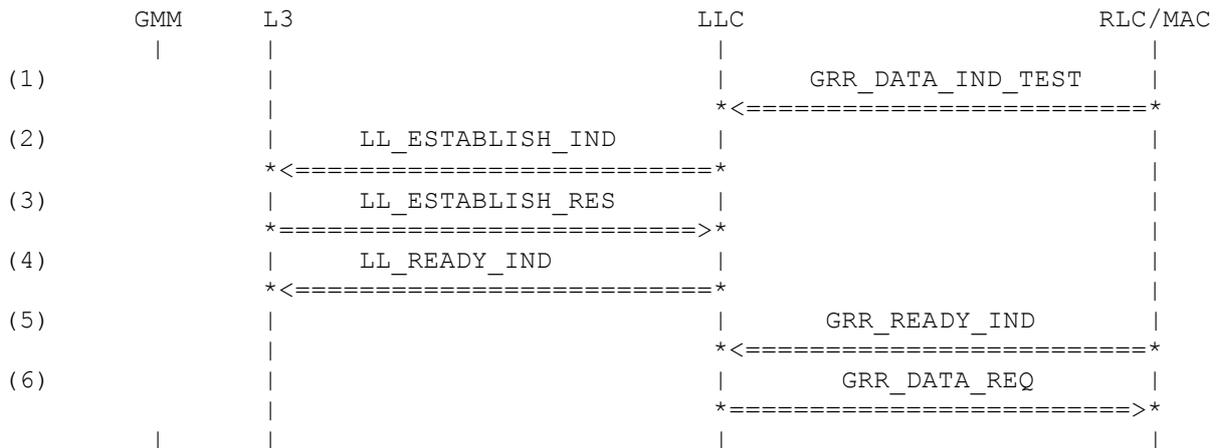
LLC receives the SABM command frame in GRR_DATA_IND_TEST from SMDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. No layer-3 XID parameters are included. LLC informs SMDCP of the establishment and transmits the UA response to its peer.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

	Primitive	Parameter	Value
(1)	GRR_DATA_IND_TEST		
		tlli	TLLI_LOCAL_1
<A>		sdu	SGSN_SABM_EMPTY_L3_XID_SAPI3
		sdu	SGSN_SABM_EMPTY_L3_XID_SAPI5
<C>		sdu	SGSN_SABM_EMPTY_L3_XID_SAPI9
<D>		sdu	SGSN_SABM_EMPTY_L3_XID_SAPI11
(2)	LL_ESTABLISH_IND		
<A>		sapi	LL_SAPI_3
		sapi	LL_SAPI_5
<C>		sapi	LL_SAPI_9
<D>		sapi	LL_SAPI_11
		tlli	TLLI_LOCAL_1
<A>		n201_u	N201_U_DEF_SAPI3
		n201_u	N201_U_DEF_SAPI5

<C>	n201_u	N201_U_DEF_SAPI9
<D>	n201_u	N201_U_DEF_SAPI11
	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(3) LL_ESTABLISH_RES		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(4) LL_READY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(5) GRR_READY_IND		
(6) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_UA1_EMPTY_L3_XID_SAPI3
	sdu	MS_UA1_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_UA1_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_UA1_EMPTY_L3_XID_SAPI11

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
23-Feb-2001	GS	LL_READY_IND added
05-Dec-2001	GS	Flag xid_valid added to LL_ESTABLISH_RES

3.9.7 LLC411: Network initiated ABM establishment for SAPI 1 or 7, establishment rejected

Description:

LLC receives the SABM command frame in GRR_DATA_IND_TEST from SDCP for SAPI 1 or 7. Establishment of ABM mode of operation is not permitted for SAPIs 1 or 7, therefore LLC rejects the establishment by sending the DM response to the peer.

Preamble:

LLC001

Variants:

<A>....

	GMM	L3	LLC	RLC/MAC
(1)			GRR_DATA_IND_TEST	
			<=====	
(2)			GRR_READY_IND	
			<=====	
(3)			GRR_DATA_REQ	
			=====>*	

Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_SABM_NOXID_SAPI1
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_1
	sapi	LL_SAPI_7
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_DM1_SAPI1
	sdu	MS_DM1_SAPI7

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.9.8 LLC420: Collision: SABM transmitted and received, both with empty layer 3 XID parameters

Description:

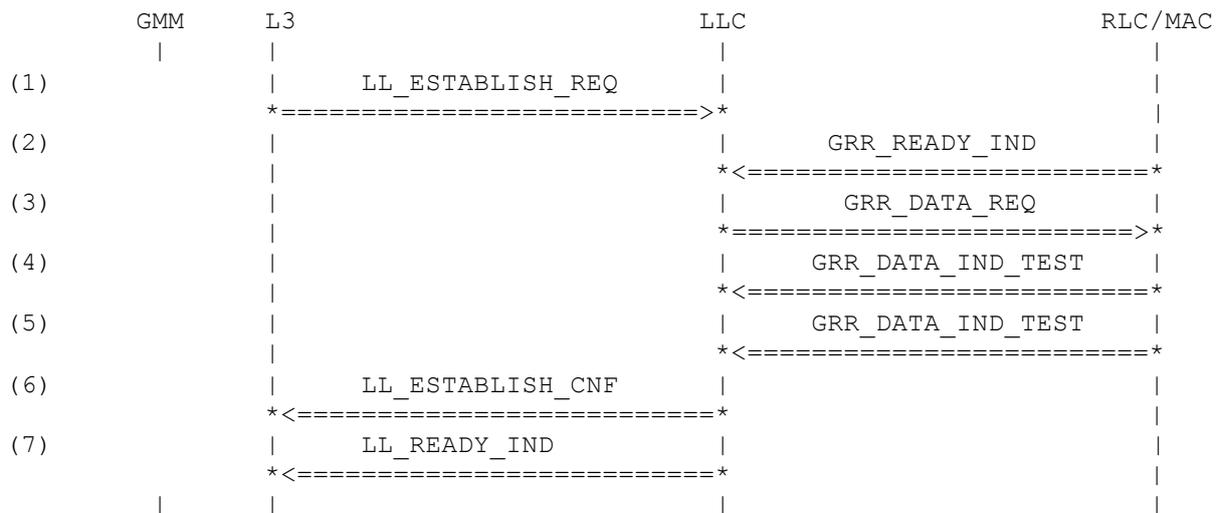
LLC receives the primitive LL_ESTABLISH_REQ from SNDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer also sent a SABM command. Both commands do contain a empty layer-3 XID parameter. The peer SABM is discarded, the peer responds with UA, ABM is established.

Preamble:

LLC001

Variants:

<A>....<D>

**Parametrization:**

Primitive	Parameter	Value
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(4) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI3
<C>	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI5
<D>	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI9
	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI11
(5) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI3
<C>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI5
<D>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI9
	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI11

(6) LL_ESTABLISH_CNF

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_DEF_SAPI3
	n201_u	N201_U_DEF_SAPI5
<C>	n201_u	N201_U_DEF_SAPI9
<D>	n201_u	N201_U_DEF_SAPI11
	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU

(7) LL_READY_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

History:

02-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.9.9 LLC425: Collision: SABM transmitted and DISC received

Description:

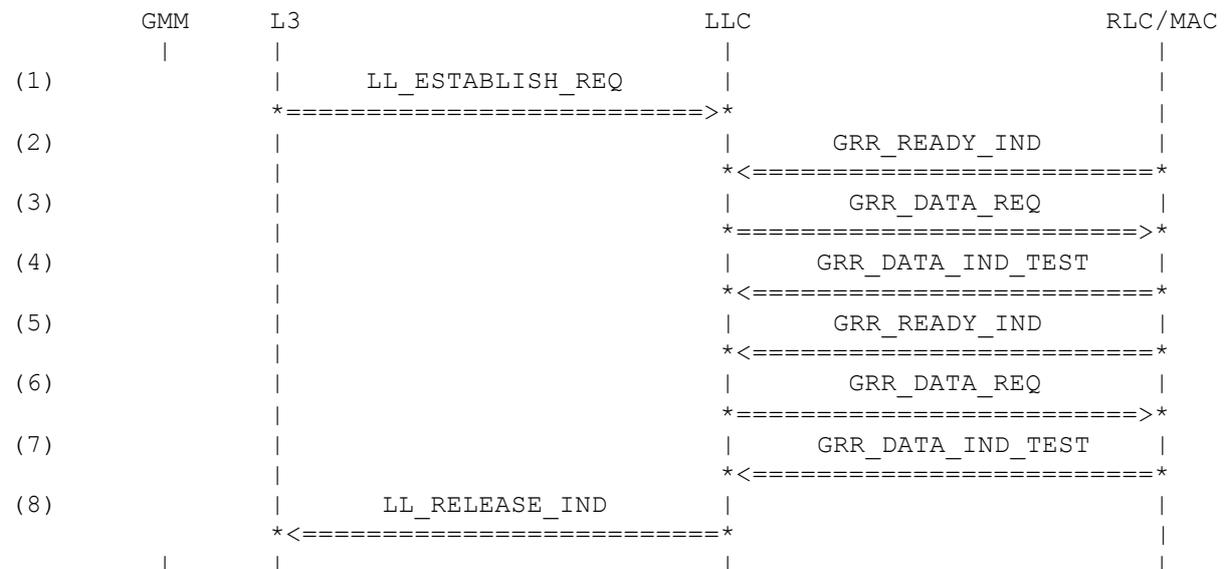
LLC receives the primitive LL_ESTABLISH_REQ from SNDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer sent a DISC command. LLC responds with DM response and waits until the DM response from the peer has been received. LLC then sends the primitive LL_RELEASE_IND to SNDCP to indicate the failed establishment.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

<u>Primitive</u>	<u>Parameter</u>	<u>Value</u>
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(4) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	SGSN_DISC_SAPI3
	sdu	SGSN_DISC_SAPI5
<C>	sdu	SGSN_DISC_SAPI9
<D>	sdu	SGSN_DISC_SAPI11
(5) GRR_READY_IND		
(6) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_DM1_SAPI3
	sdu	MS_DM1_SAPI5
<C>	sdu	MS_DM1_SAPI9
<D>	sdu	MS_DM1_SAPI11
(7) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	SGSN_DM1_SAPI3
	sdu	SGSN_DM1_SAPI5
<C>	sdu	SGSN_DM1_SAPI9
<D>	sdu	SGSN_DM1_SAPI11
(8) LL_RELEASE_IND		
<A>	sapi	LL_SAPI_3

	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	cause	LL_RELCS_DM_RECEIVED

History:

03-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
11-Oct-2001	GS	Add empty l3 sdu

3.9.10 LLC427: Collision: SABM transmitted and XID command received

Description:

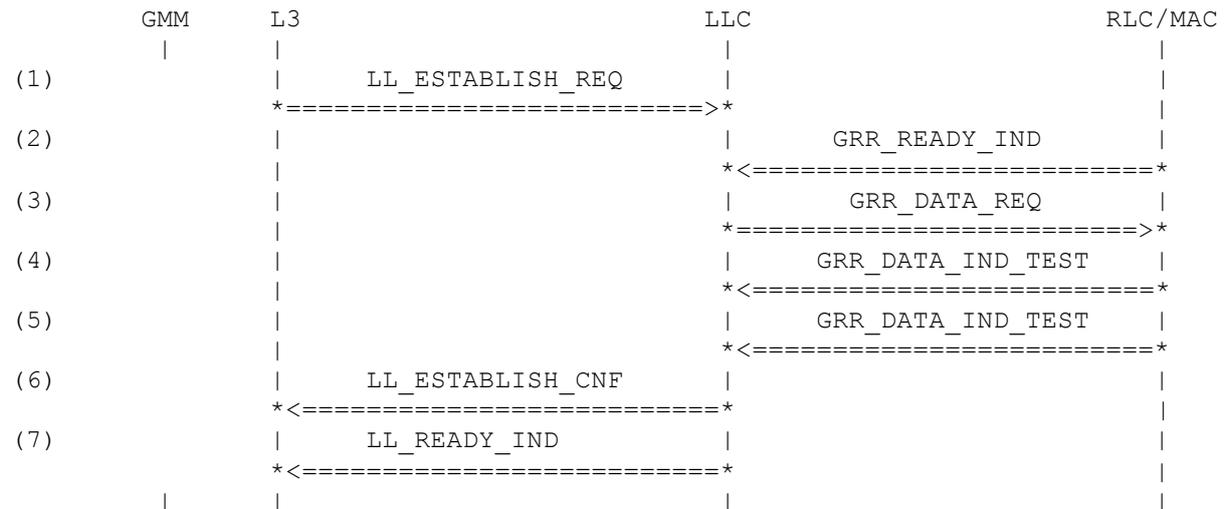
LLC receives the primitive LL_ESTABLISH_REQ from SNDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer sent the XID command, which is discarded by LLC. The peer then responds with UA, ABM is established.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9

<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(4) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	XID1_SGSN_ALLMAX_SAPI3
	sdu	XID1_SGSN_ALLMAX_SAPI5
<C>	sdu	XID1_SGSN_ALLMAX_SAPI9
<D>	sdu	XID1_SGSN_ALLMAX_SAPI11
(5) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI3
	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI5
<C>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI9
<D>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI11
(6) LL_ESTABLISH_CNF		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_DEF_SAPI3
	n201_u	N201_U_DEF_SAPI5
<C>	n201_u	N201_U_DEF_SAPI9
<D>	n201_u	N201_U_DEF_SAPI11
	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(7) LL_READY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

History:

18-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
11-Oct-2001	GS	Add empty l3 sdu

3.10 ABM re-establishment (LLC430 – LLC459)

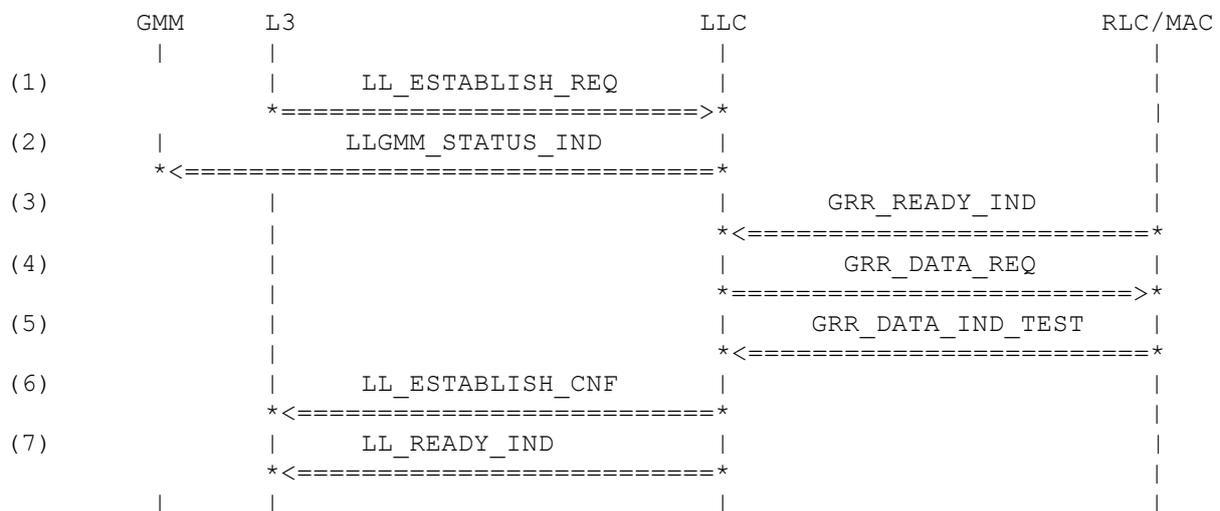
3.10.1 LLC430: Layer-3 initiated ABM re-establishment for SAPI 3, UA response

Description:

LLC receives the primitive LL_ESTABLISH_REQ from SDCP for SAPI 3. LLC is in state 'ABM' and is ready to re-establish ABM mode of operation. LLC transmits the SABM command to its peer to re-establish ABM. The peer responds with UA, ABM is re-established.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) LL_ESTABLISH_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) LLGMM_STATUS_IND	error_cause	LLGMM_ERRCS_L3_REEST
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_SABM_EMPTY_L3_XID_SAPI3

(5) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	SGSN_UA1_EMPTY_L3_XID_SAPI3

(6) LL_ESTABLISH_CNF

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
n201_u	N201_U_DEF_SAPI3
n201_i	N201_I_DEF
xid_valid	LL_XID_VALID
sdu	EMPTY_SDU

(7) LL_READY_IND

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
23-Feb-2001	GS	LL_READY_IND added
31-Jul-2001	GS	LLGMM_STATUS_IND added
11-Oct-2001	GS	Add empty L3 to sdu content

3.10.2 LLC435: LLC initiated ABM re-establishment for SAPI 3, UA response

Description:

LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC is in state 'ABM' and is ready to re-establish ABM mode of operation. LLC transmits the SABM command to its peer to re-establish ABM. The peer responds with UA, ABM is re-established. LLC informs SNDCP of the re-establishment.

Preamble:

LLC400A

	GMM	L3	LLC	RLC/MAC
(1)			GRR_DATA_IND_TEST	
			<=====	
(2)		LLGMM_STATUS_IND		
	<=====			
(3)			GRR_READY_IND	
			<=====	
(4)			GRR_DATA_REQ	
			=====	
(5)			GRR_DATA_IND_TEST	
			<=====	
(6)		LL_ESTABLISH_IND		
		<=====		
(7)		LL_ESTABLISH_RES		
		=====		
(8)		LL_READY_IND		
		<=====		
(9)			GRR_READY_IND	
			<=====	
MUTE (3000)				

Parametrization:

Primitive	Parameter	Value
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(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_DM0_SAPI3
(2) LLGMM_STATUS_IND	error_cause LLGMM_ERRCS_DM0_RECEIVED_REEST	
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED NOT_USED
(5) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_NOXID_SAPI3
(6) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_DEF_SAPI3 N201_I_DEF LL_XID_INVALID EMPTY_SDU
(7) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 LL_XID_VALID EMPTY_SDU
(8) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(9) GRR_READY_IND		

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
27-Mar-2001	GS	LL_ESTABLISH_RES added
05-Dec-2001	GS	Flag XID_valid added to LL_ESTABLISH_RES

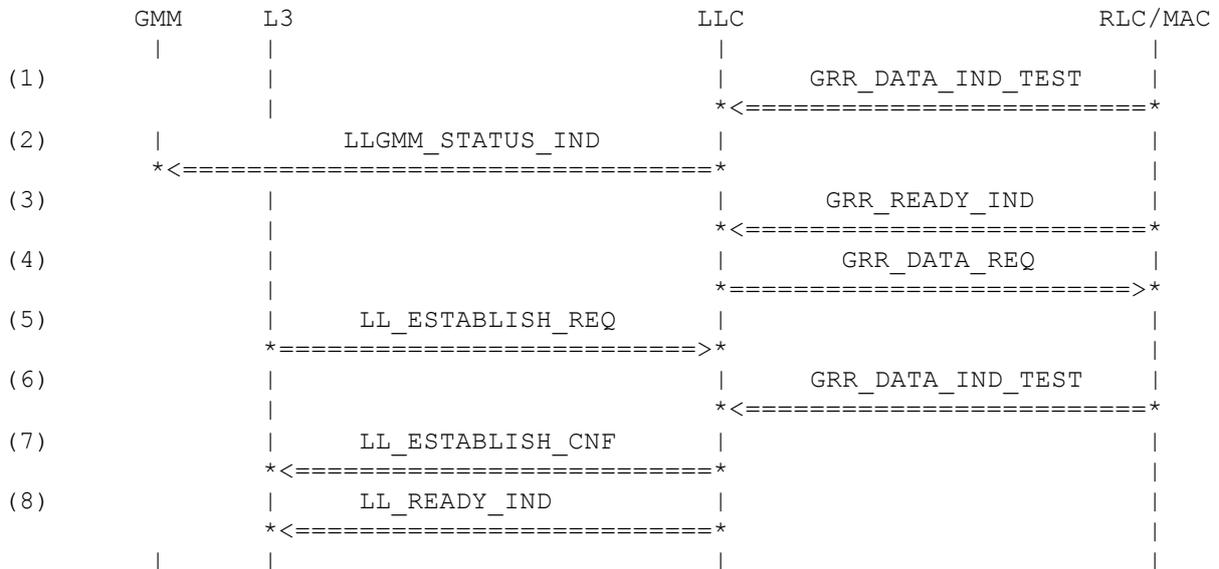
3.10.3 LLC436: LLC initiated ABM re-establishment for SAPI 3, Layer-3 re-establish request during pending re-establishment, UA response

Description:

LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC is in state 'ABM' and is ready to re-establish ABM mode of operation. LLC transmits the SABM command to its peer to re-establish ABM. Layer 3 requests re-establishment of ABM mode of operation. The peer responds with UA, ABM is re-established. LLC confirms the re-establishment to layer 3.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_DM0_SAPI3
(2) LLGMM_STATUS_IND	error_cause LLGMM_ERRCS_DM0_RECEIVED_REEST	
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF

	reserved_data_req	NOT_USED
	sdu	MS_SABM_NOXID_SAPI3
(5) LL_ESTABLISH_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(6) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_NOXID_SAPI3
(7) LL_ESTABLISH_CNF	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_DEF_SAPI3
	n201_i	N201_I_DEF
	xid_valid	LL_XID_INVALID
	sdu	EMPTY_SDU
(8) LL_READY_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
23-Feb-2001	GS	LL_READY_IND added

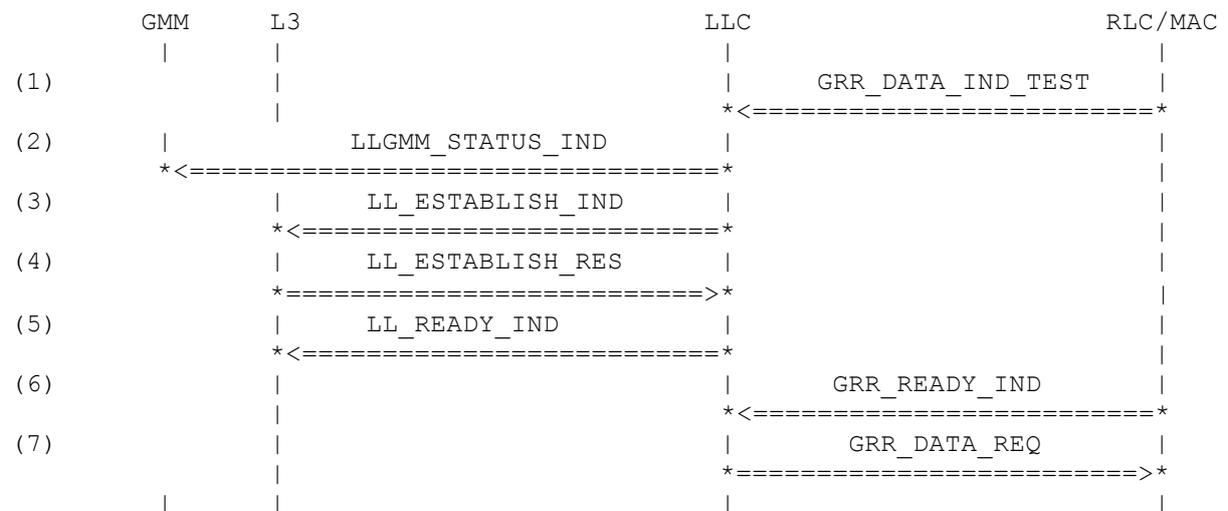
3.10.4 LLC440: Network initiated ABM re-establishment for SAPI 3

Description:

LLC receives the SABM command frame in GRR_DATA_IND_TEST from SNDCP for SAPI 3. LLC is in state 'ABM' and is ready to re-establish ABM mode of operation. No layer-3 XID parameters are included. LLC informs SNDCP of the re-establishment and transmits the UA response to its peer.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
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(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_SABM_NOXID_SAPI3
(2) LLGMM_STATUS_IND	error_cause	LLGMM_ERRCS_PEER_REEST
(3) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_DEF_SAPI3 N201_I_DEF LL_XID_INVALID EMPTY_SDU
(4) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 LL_XID_INVALID EMPTY_SDU
(5) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(6) GRR_READY_IND		
(7) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_UA1_NOXID_SAPI3

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
23-Feb-2003	GS	LL_READY_IND added
05-Dec-2001	GS	Flag xid_valid added to LL_ESTABLISH_RES

3.11 ABM termination (LLC460 – LLC499)

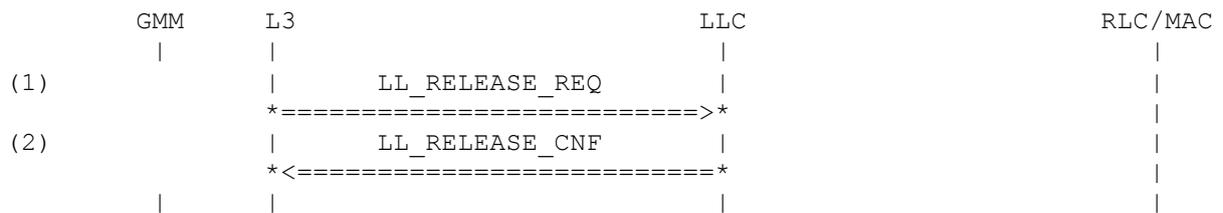
3.11.1 LLC460: Layer-3 initiated ABM termination for SAPI 3 (local release)

Description:

LLC receives the primitive LL_RELEASE_REQ (local release) from SMDCP for SAPI 3. LLC is in state 'ABM'. LLC terminates ABM mode of operation locally (i.e. without transmitting any command to the peer). Termination of ABM is confirmed to SMDCP.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_LOCAL
(2) LL_RELEASE_CNF	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

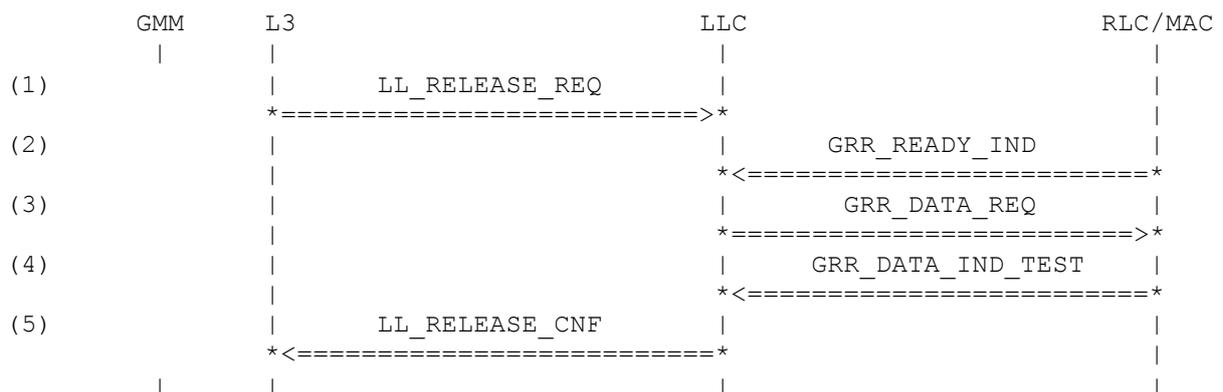
3.11.2 LLC461: Layer-3 initiated ABM termination for SAPI 3, UA response

Description:

LLC receives the primitive LL_RELEASE_REQ from SNDPCP. LLC is in state 'ABM'. LLC transmits the DISC command to its peer to terminate ABM mode of operation. The peer responds with UA, ABM is terminated. LLC confirms termination of ABM to SNDPCP.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(2) GRR_READY_IND		

(3) GRR_DATA_REQ

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAQSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	MS_DISC_SAPI3

(4) GRR_DATA_IND_TEST

tli	TLLI_LOCAL_1
sdu	SGSN_UA1_NOXID_SAPI3

(5) LL_RELEASE_CNF

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
reserved_release_cnf	NOT_USED

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

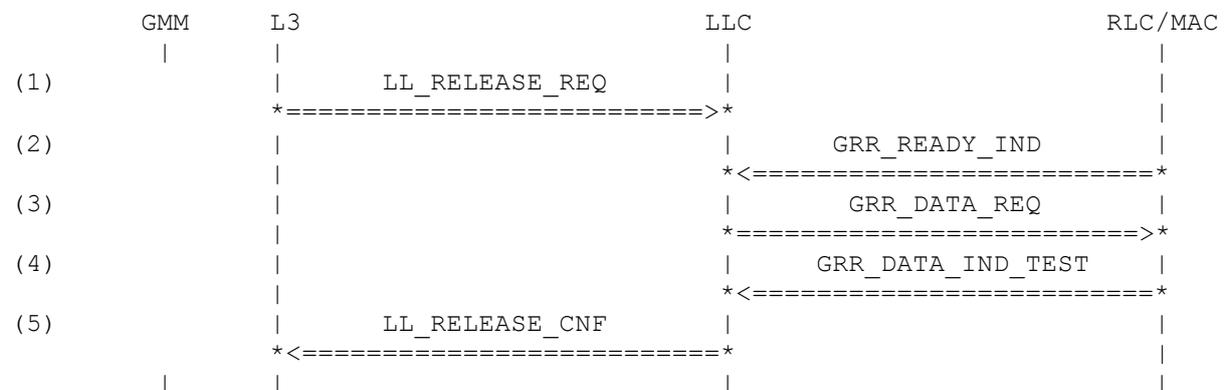
3.11.3 LLC462: Layer-3 initiated ABM termination for SAPI 3, DM response

Description:

LLC receives the primitive LL_RELEASE_REQ from SDCP. LLC is in state 'ABM'. LLC transmits the DISC command to its peer to terminate ABM mode of operation. The peer responds with DM, ABM is terminated. LLC confirms termination of ABM to SDCP.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1

	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DISC_SAPI3
(4) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM1_SAPI3
(5) LL_RELEASE_CNF		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

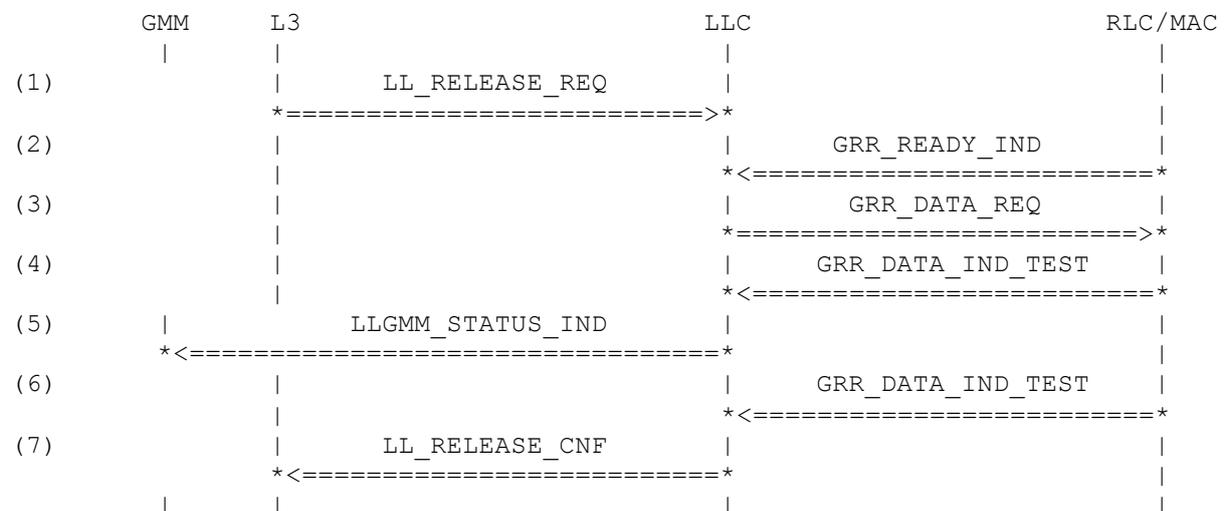
3.11.4 LLC463: Layer-3 initiated ABM termination for SAPI 3, UA response (F bit 0), UA response (F bit 1)

Description:

LLC receives the primitive LL_RELEASE_REQ from SNDSCP. LLC is in state 'ABM'. LLC transmits the DISC command to its peer to terminate ABM mode of operation. LLC receives an unsolicited UA response with the F Bit set to 0, followed by an UA response with the F bit set to 1. ABM is terminated. LLC confirms termination of ABM to SNDSCP.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PQIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DISC_SAPI3
(4) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA0_NOXID_SAPI3
(5) LLGMM_STATUS_IND		
	error_cause	LLGMM_ERRCS_MULT_ASS_TLLI
(6) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_NOXID_SAPI3
(7) LL_RELEASE_CNF		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

22-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

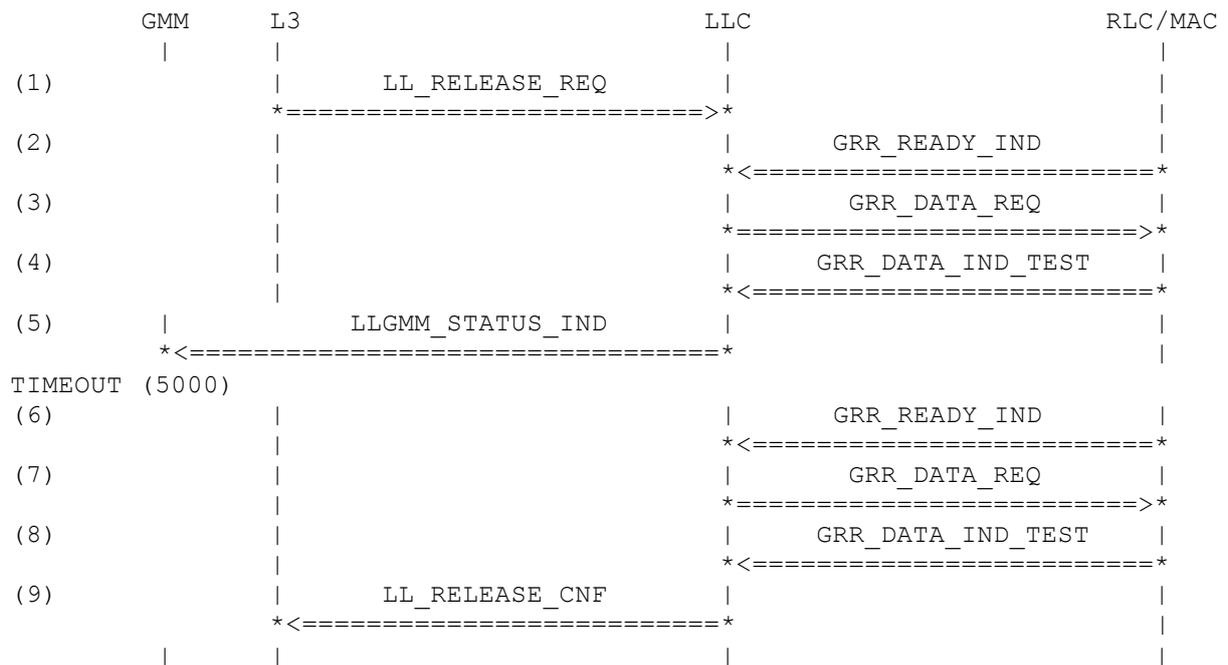
3.11.5 LLC464: Layer-3 initiated ABM termination for SAPI 3, UA response (F bit 0), timeout with retransmission, UA response (F bit 1)

Description:

LLC receives the primitive LL_RELEASE_REQ from SMDCP. LLC is in state 'ABM'. LLC transmits the DISC command to its peer to terminate ABM mode of operation. LLC receives an unsolicited UA response with the F Bit set to 0. Timeout of T200, LLC retransmits the DISC command. The peer responds with UA, ABM is terminated. LLC confirms termination of ABM to SMDCP.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DISC_SAPI3
(4) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA0_NOXID_SAPI3
(5) LLGMM_STATUS_IND	error_cause	LLGMM_ERRCS_MULT_ASS_TLLI
(6) GRR_READY_IND		
(7) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DISC_SAPI3

(8) GRR_DATA_IND_TEST

tli	TLLI_LOCAL_1
sdu	SGSN_UA1_NOXID_SAPI3

(9) LL_RELEASE_CNF

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
reserved_release_cnf	NOT_USED

History:

22-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

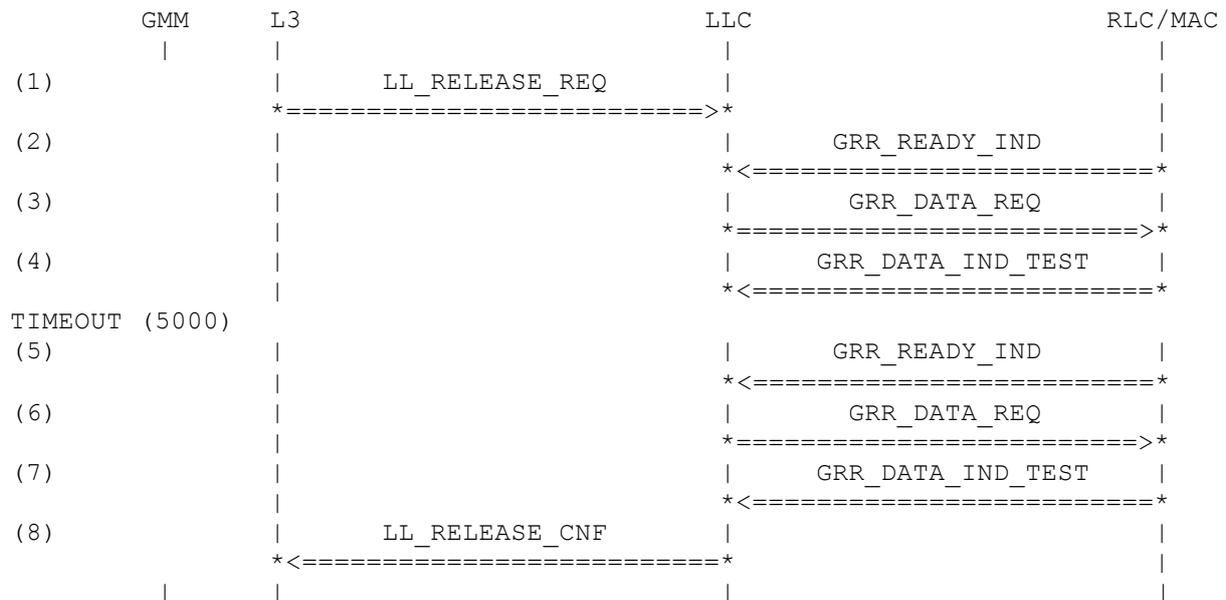
3.11.6 LLC465: Layer-3 initiated ABM termination for SAPI 3, DM response (F bit 0), timeout with retransmission, DM response (F bit 1)

Description:

LLC receives the primitive LL_RELEASE_REQ from SDCP. LLC is in state 'ABM'. LLC transmits the DISC command to its peer to terminate ABM mode of operation. LLC receives an unsolicited DM response with F bit set to 0 and discards the frame. Timeout T200, LLC retransmits the DISC command. LLC then receives a DM response with F bit set to 1, ABM is terminated. LLC confirms termination of ABM to SDCP.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1

	grr_qos	GRRREQ_QOS_PEAKE
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DISC_SAPI3
(4) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM0_SAPI3
(5) GRR_READY_IND		
(6) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKE
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DISC_SAPI3
(7) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM1_SAPI3
(8) LL_RELEASE_CNF		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

03-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.11.7 LLC470: Network initiated ABM termination for SAPI 3

Description:

LLC receives the DISC command frame in GRR_DATA_IND_TEST. LLC is in state 'ABM'. LLC informs SNDCP of the termination and transmits the UA response to its peer.

Preamble:

LLC410A

	GMM	L3	LLC	RLC/MAC
(1)				
			GRR_DATA_IND_TEST	
			<=====	
(2)				
		LL_RELEASE_IND		
		<=====		
(3)				
			GRR_READY_IND	
			<=====	
(4)				
			GRR_DATA_REQ	
			=====>	

Parametrization:

<u>Primitive</u>	<u>Parameter</u>	<u>Value</u>
------------------	------------------	--------------

(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_DISC_SAPI3
(2) LL_RELEASE_IND	sapi tlli cause	LL_SAPI_3 TLLI_LOCAL_1 LL_RELCS_NORMAL
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_UA1_NOXID_SAPI3

History:

21-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.11.8 LLC471: Network initiated ABM termination for SAPI 3, LLC is in state 'ADM'

Description:

LLC receives the DISC command frame in GRR_DATA_IND_TEST. LLC is in state 'ADM'. LLC transmits the DM response to its peer.

Preamble:

LLC001

	GMM	L3	LLC	RLC/MAC
(1)			GRR_DATA_IND_TEST	
			<=====	
(2)			GRR_READY_IND	
			<=====	
(3)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_DISC_SAPI3
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_DM1_SAPI3

History:

22-Feb-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

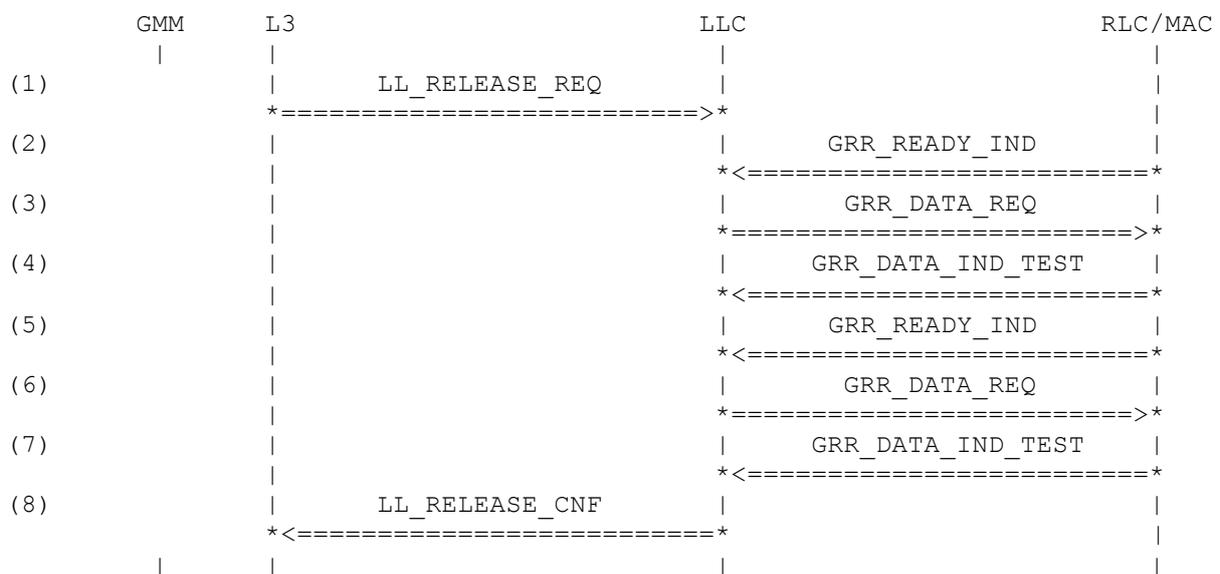
3.11.9 LLC480: Collision: DISC command transmitted and received

Description:

LLC receives the primitive LL_RELEASE_REQ from SNDSCP. LLC is in state 'ABM'. LLC transmits the DISC command to its peer to terminate ABM mode of operation. The peer also sent a DISC command, LLC responds with UA. LLC receives the UA response from the peer, ABM is terminated. LLC confirms termination of ABM to SNDSCP.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DISC_SAPI3
(4) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_DISC_SAPI3
(5) GRR_READY_IND		

(6) GRR_DATA_REQ

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAQSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	MS_UA1_NOXID_SAPI3

(7) GRR_DATA_IND_TEST

tli	TLLI_LOCAL_1
sdu	SGSN_UA1_NOXID_SAPI3

(8) LL_RELEASE_CNF

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
reserved_release_cnf	NOT_USED

History:

02-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

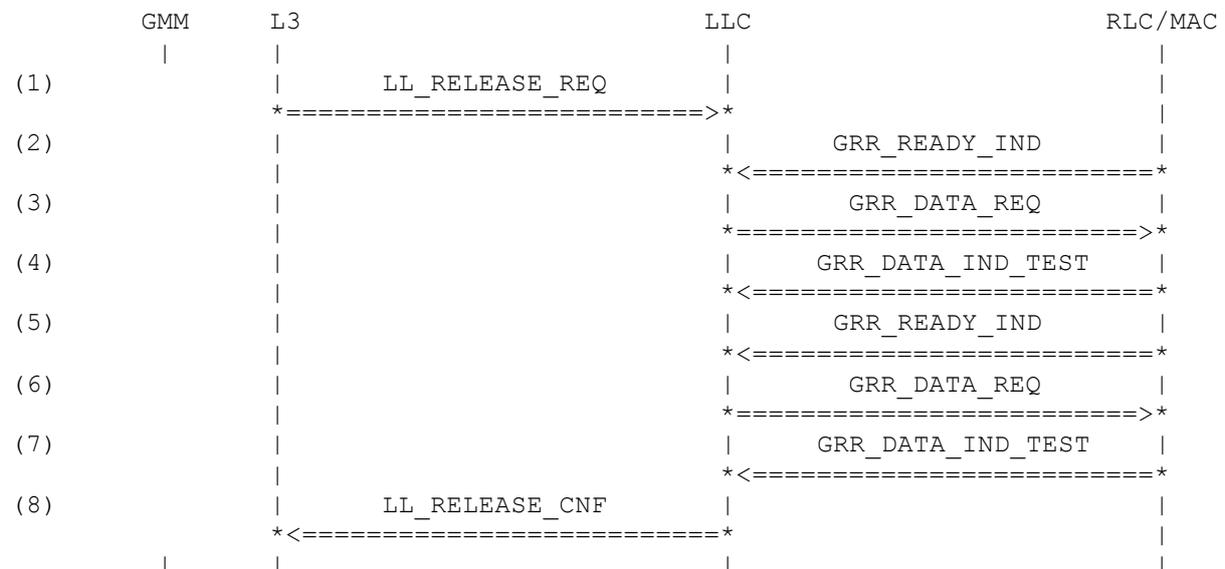
3.11.10 LLC481: Collision: DISC transmitted and SABM received

Description:

LLC receives the primitive LL_RELEASE_REQ from SMDCP. LLC is in state 'ABM'. LLC transmits the DISC command to its peer to terminate ABM mode of operation. The peer sent the SABM command. LLC responds with DM command and waits until the DM command from the peer has been received. ABM is then terminated. LLC confirms termination of ABM to SMDCP.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
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(1) LL_RELEASE_REQ

sapi	LL_SAPI_3
------	-----------

	tli local	TLLI_LOCAL_1 LL_REL_NOTLOCAL
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_DISC_SAPI3
(4) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 SGSN_SABM_NOXID_SAPI3
(5) GRR_READY_IND		
(6) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_DM1_SAPI3
(7) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 SGSN_DM1_SAPI3
(8) LL_RELEASE_CNF	sapi tli reserved_release_cnf	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED

History:

03-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.12 Frame rejection condition (LLC500 – LLC549)

3.12.1 LLC500: Receipt of a undefined command control field in ADM mode

Description:

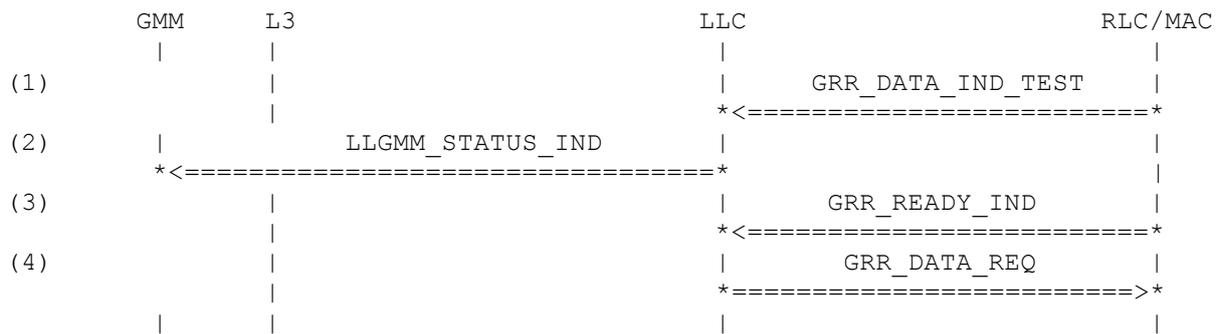
LLC receives a frame from the peer with an undefined control field. LLC discards the frame and responds with a FRMR response frame.

Preamble:

LLC001

Variants:

<A>....<F>



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_UNDEFCOMMCTRL0_SAPI1
<C>	sdu	SGSN_UNDEFCOMMCTRL0_SAPI3
<D>	sdu	SGSN_UNDEFCOMMCTRL0_SAPI5
<E>	sdu	SGSN_UNDEFCOMMCTRL0_SAPI7
<F>	sdu	SGSN_UNDEFCOMMCTRL0_SAPI9
	sdu	SGSN_UNDEFCOMMCTRL0_SAPI11
(2) LLGMM_STATUS_IND		
	error_cause	LLGMM_ERRCS_FRMR_COND
(3) GRR_READY_IND		
(4) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_1
	sapi	LL_SAPI_3
<C>	sapi	LL_SAPI_5
<D>	sapi	LL_SAPI_7
<E>	sapi	LL_SAPI_9
<F>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_FRMR_UNDEFCTRL0_SAPI1
	sdu	MS_FRMR_UNDEFCTRL0_SAPI3
<C>	sdu	MS_FRMR_UNDEFCTRL0_SAPI5
<D>	sdu	MS_FRMR_UNDEFCTRL0_SAPI7
<E>	sdu	MS_FRMR_UNDEFCTRL0_SAPI9
<F>	sdu	MS_FRMR_UNDEFCTRL0_SAPI11

History:

03-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

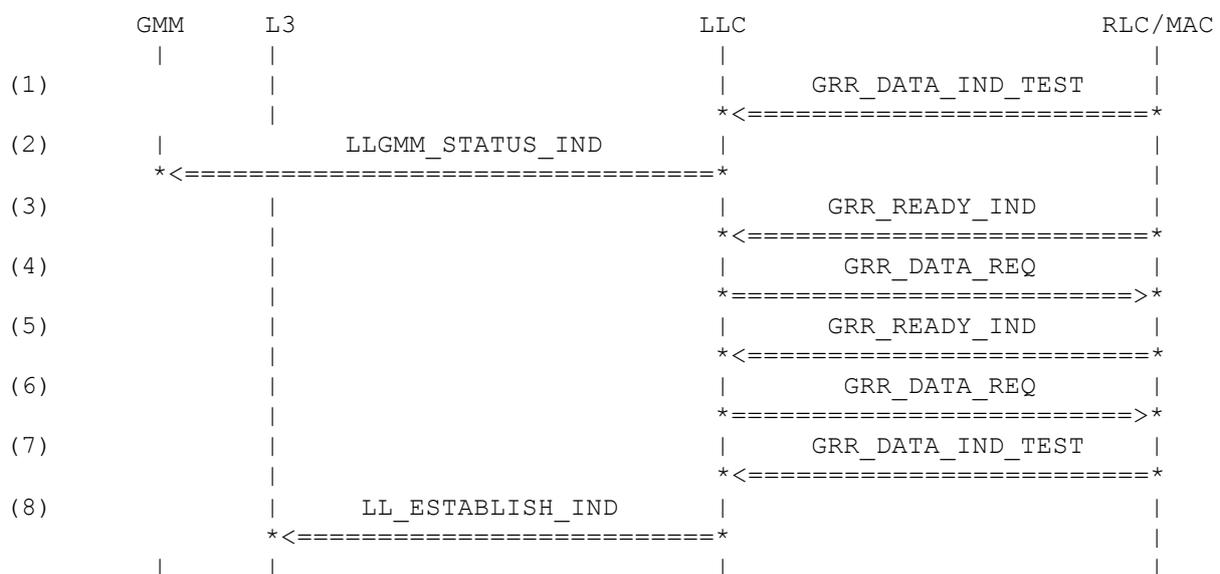
3.12.2 LLC505: Receipt of a undefined command control field for SAPI 3 in ABM mode, re-establish ABM, UA response

Description:

LLC receives a frame from the peer with an undefined control field. LLC discards the frame, responds with a FRMR response frame, and initiates re-establishment of ABM mode of operation. LLC transmits the SABM command to its peer to re-establish ABM. The peer responds with UA, ABM is re-established. LLC informs SNDCP of the re-establishment.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 SGSN_UNDEFCOMMCTRL0_SAPI3
(2) LLGMM_STATUS_IND	error_cause LLGMM_ERRCS_FRMR_COND_REEST	
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_FRMR_UNDEFCTRL0_REEST_SAPI3
(5) GRR_READY_IND		

(6) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	MS_SABM_NOXID_SAPI3

(7) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	SGSN_UA1_NOXID_SAPI3

(8) LL_ESTABLISH_IND

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
n201_u	N201_U_DEF_SAPI3
n201_i	N201_I_DEF
xid_valid	LL_XID_INVALID
sdu	EMPTY_SDU

History:

06-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)

3.12.3 LLC510: Layer-3 initiated ABM establishment, DM response (with forbidden information field), DM response

Description:

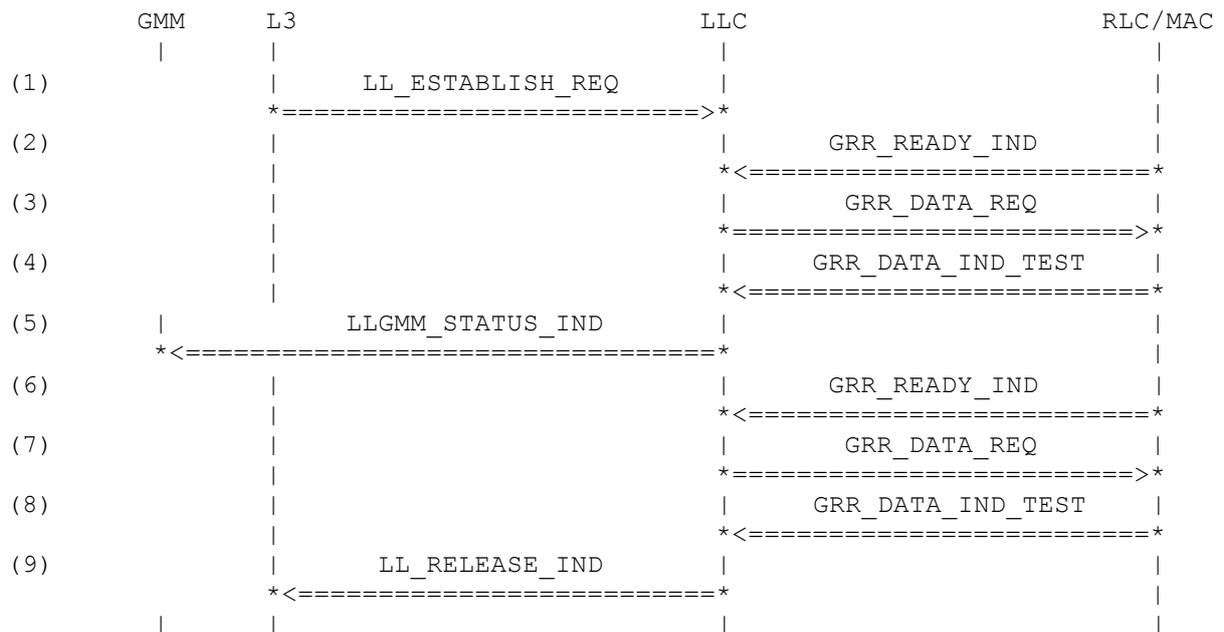
LLC receives the primitive LL_ESTABLISH_REQ from SMDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with DM, but included an information field in the response, which is forbidden. LLC sends a FRMR response. The peer sends a DM response (without information field), ABM establishment is aborted.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(4) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM1_WITHINFO_SAPI3
<C>	sdu	SGSN_DM1_WITHINFO_SAPI5
<D>	sdu	SGSN_DM1_WITHINFO_SAPI9
	sdu	SGSN_DM1_WITHINFO_SAPI11
(5) LLGMM_STATUS_IND		
	error_cause	LLGMM_ERRCS_FRMR_COND

(6) GRR_READY_IND

(7) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIQ_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_FRMR_DM1_SAPI3
	sdu	MS_FRMR_DM1_SAPI5
<C>	sdu	MS_FRMR_DM1_SAPI9
<D>	sdu	MS_FRMR_DM1_SAPI11

(8) GRR_DATA_IND_TEST

<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM1_SAPI3
<C>	sdu	SGSN_DM1_SAPI5
<D>	sdu	SGSN_DM1_SAPI9
	sdu	SGSN_DM1_SAPI11

(9) LL_RELEASE_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	cause	LL_RELCS_DM_RECEIVED

History:

07-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
11-Oct-2001	GS	Add empty I3 sdu

3.13 Trigger Request (LLC550 – LLC599)

3.13.1 LLC550: RLC/MAC unacknowledged frame waiting to be transmitted, send acknowledged UI frame

Description:

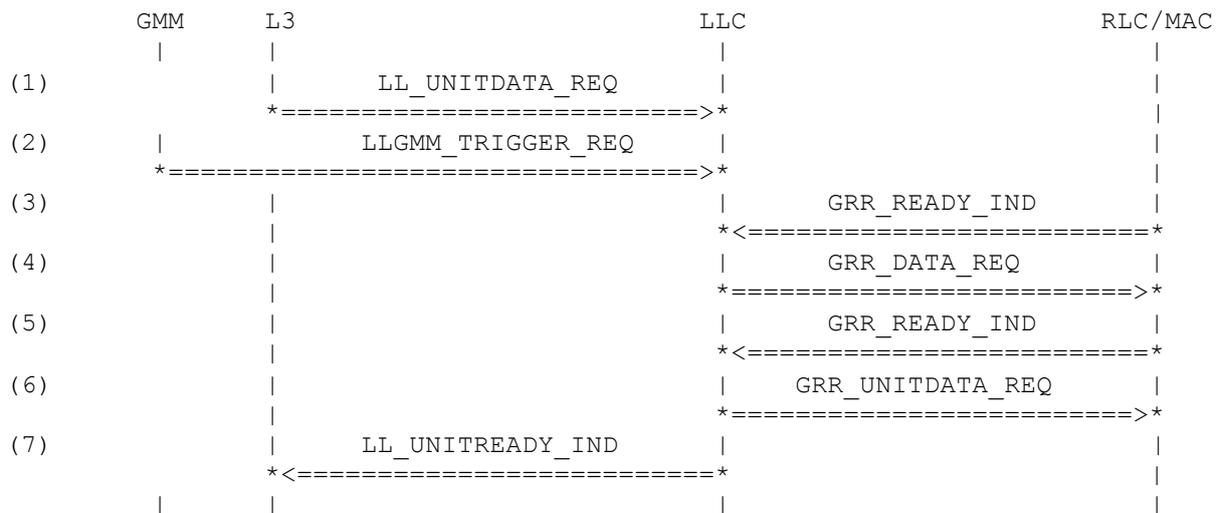
LLC receives data (100 octets) for unacknowledged transmission. RLC/MAC is not ready to receive data, so the frame is buffered. LLC receives a trigger request with cause cell update from GMM, but no acknowledged frame is buffered, so LLC sends an empty UI frame, this one acknowledged, which is inserted at the beginning of the local transmit queue due to the special primitive cause. RLC/MAC indicates ready, LLC sends the empty UI frame to RLC/MAC using the primitive GRR_DATA_REQ. RLC/MAC indicates again that it is ready to receive data, so the UI frame containing layer 3 data is sent.

Preamble:

LLC001

Variants:

<A>....<D>

**Parametrization:**

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LLGMM_TRIGGER_REQ		
	trigger_cause	LLGMM_TRICS_CELL_UPDATE
(3) GRR_READY_IND		
(4) GRR_DATA_REQ		
	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	LL_RADIO_PRIO_1
	cause	GRR_DTACS_EMPTY_FRAME
	reserved_data_req	NOT_USED
	sdu	GRRREQ_SDU0_SAPI1_PM0
(5) GRR_READY_IND		
(6) GRR_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB

	radio_prio	LL_RADIO_PRIO_3
	reserved_unitdata_req1	NOT_USED
	reserved_unitdata_req2	NOT_USED
<A>	sdu	GRRREQ_SDU100_SAPI3
	sdu	GRRREQ_SDU100_SAPI5
<C>	sdu	GRRREQ_SDU100_SAPI9
<D>	sdu	GRRREQ_SDU100_SAPI11
(7) LL_UNITREADY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

30-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
29-May-2000	DB	Exchanged GRR_*_REQ primitives due to new trigger behaviour
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
12-Sep-2001	GS	SAPI 7 variant removed.
22-May-2003	UT	Cause in GRR_DATA_REQ modified due to new Cell Update Procedure

3.13.2 LLC560: RLC/MAC acknowledged frame waiting to be transmitted, use trigger cause

Description:

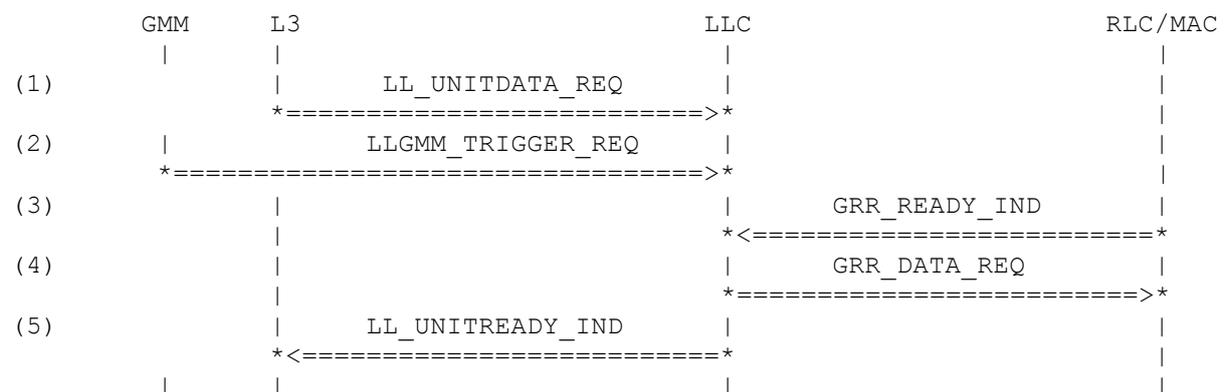
LLC receives data (100 octets) for unacknowledged transmission. RLC/MAC is not ready to receive data, so the frame is buffered. LLC receives a trigger request with cause cell update from GMM, the transmission cause of the buffered frame is modified to contain cell update. RLC/MAC indicates ready, LLC sends the buffered data in an UI frame to RLC/MAC using the primitive GRR_DATA_REQ with cause cell update.

Preamble:

LLC001

Variants:

<A>....<E>



Parametrization:

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7

<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LLGMM_TRIGGER_REQ		
	trigger_cause	LLGMM_TRICS_CELL_UPDATE
(3) GRR_READY_IND		
(4) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	LL_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	GRRREQ_SDU100_SAPI3_PM
	sdu	GRRREQ_SDU100_SAPI5_PM
<C>	sdu	GRRREQ_SDU100_SAPI7_PM
<D>	sdu	GRRREQ_SDU100_SAPI9_PM
<E>	sdu	GRRREQ_SDU100_SAPI11_PM
(5) LL_UNITREADY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_7
<D>	sapi	LL_SAPI_9
<E>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

30-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
22-May-2003	UT	Cause in GRR_DATA_REQ modified due to new Cell Update Procedure

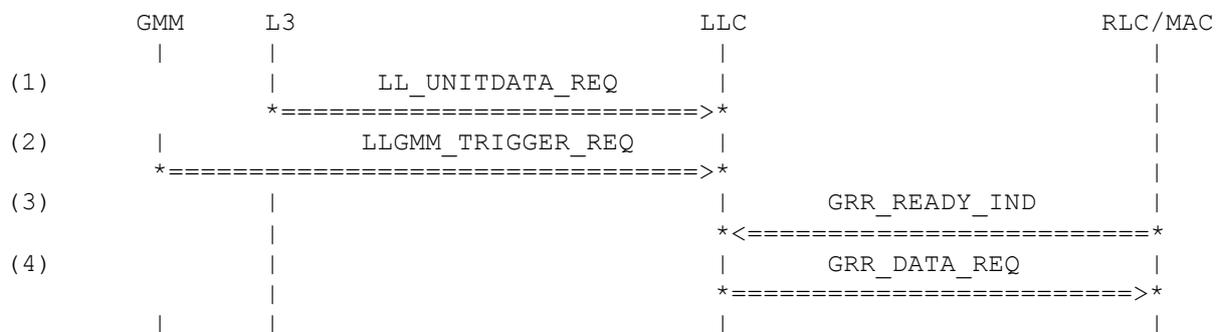
3.13.3 LLC561: RLC/MAC acknowledged frame waiting to be transmitted on SAPI 1, use trigger cause

Description:

LLC receives data (100 octets) for unacknowledged transmission. RLC/MAC is not ready to receive data, so the frame is buffered. LLC receives a trigger request with cause page response from GMM, the transmission cause of the buffered frame is modified to contain page response. RLC/MAC indicates ready, LLC sends the buffered data in an UI frame to RLC/MAC using the primitive GRR_DATA_REQ with cause page response.

Preamble:

LLC001



Parametrization:

Primitive	Parameter	Value	
(1) LL_UNITDATA_REQ	sapi	LL_SAPI_1	
	tlli	TLLI_LOCAL_1	
	ll_qos	LLREQ_QOS_DEL4_REL3	
	radio_prio	LL_RADIO_PRIO_1	
	cipher	LL_CIPHER_OFF	
	reserved_unitdata_req1	NOT_USED	
	seg_pos	NOT_USED	
	attached_counter	LLC_NO_ATTACHE	
	reserved_unitdata_req4	NOT_USED	
	reserved_unitdata_req5	NOT_USED	
	sdu	LLREQ_SDU100	
	(2) LLGMM_TRIGGER_REQ	trigger_cause	LLGMM_TRICS_PAGE_RESPONSE
	(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_1	
	tlli	TLLI_LOCAL_1	
	grr_qos	GRRREQ_QOS_PEAKSUB	
	radio_prio	LL_RADIO_PRIO_1	
	cause	GRR_DTACS_PAGE_RESPONSE	
	reserved_data_req	NOT_USED	
	sdu	GRRREQ_SDU100_SAPI1_PM	

History:

30-Mar-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs

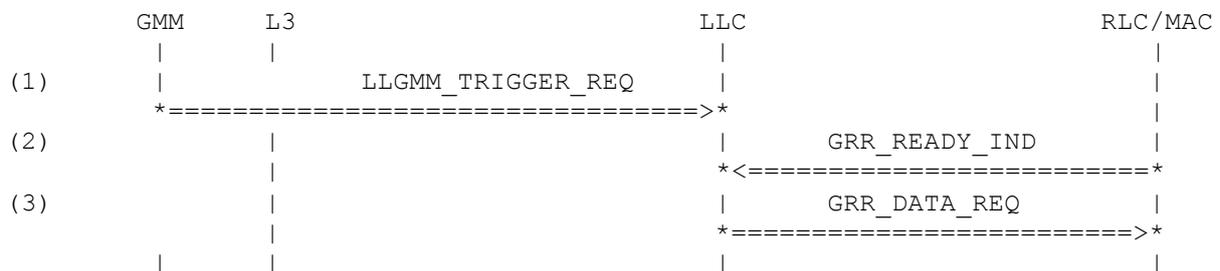
3.13.4 LLC570: No frame waiting to be transmitted, transmit UI frame with no information field

Description:

LLC receives a trigger request with cause page response from GMM, no frame is waiting to be transmitted. LLC builds a UI frame with no information field and buffers it until RLC/MAC indicates ready. RLC/MAC indicates ready, LLC sends the buffered UI frame to RLC/MAC using the primitive GRR_DATA_REQ with cause page response.

Preamble:

LLC001

**Parametrization:**

Primitive	Parameter	Value
(1) LLGMM_TRIGGER_REQ	trigger_cause	LLGMM_TRICS_CELL_UPDATE
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKESSUB
	radio_prio	LL_RADIO_PRIO_1
	cause	GRR_DTACS_EMPTY_FRAME
	reserved_data_req	NOT_USED
	sdu	GRRREQ_SDU0_SAPI1_PM0

History:

03-Apr-2000	DB	Initial
22-May-2000	DB	Inserted reserved_* parameter(s)
22-May-2003	UT	Cause in GRR_DATA_REQ modified due to new Cell Update Procedure

3.14 Suspended Mode (LLC600 – LLC649)

3.14.1 LLC600: LLC suspended, unacknowledged frame transmission requested, LLC resumed, send buffered frame

Description:

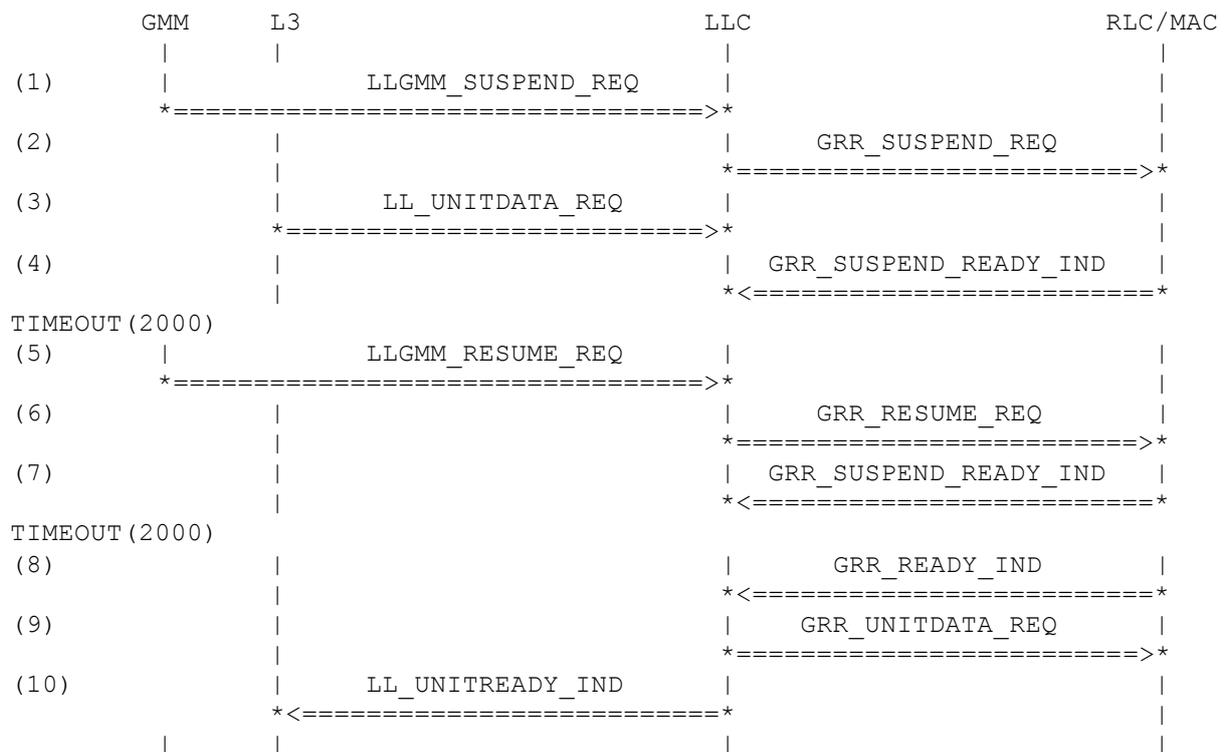
LLC is being suspended by GMM. LLC receives data (100 octets) for unacknowledged transmission. RLC/MAC is not ready to receive data, so the frame is buffered. RLC/MAC indicates ready, LLC sends the buffered UI frame containing the layer 3 data.

Preamble:

LLC001

Variants:

<A>...<D>



Parametrization:

Primitive	Parameter	Value
(1) LLGMM_SUSPEND_REQ		
<A>	susp_cause	LLGMM_RAU
	susp_cause	LLGMM_RAU
<C>	susp_cause	LLGMM_RAU
<D>	susp_cause	LLGMM_RAU
(2) GRR_SUSPEND_REQ		

(3) LL_UNITDATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100

(4) GRR_SUSPEND_READY_IND

(5) LLGMM_RESUME_REQ

(6) GRR_RESUME_REQ

(7) GRR_SUSPEND_READY_IND

(8) GRR_READY_IND

(9) GRR_UNITDATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKESSUB
	radio_prio	LL_RADIO_PRIO_3
	reserved_unitdata_req1	NOT_USED
	reserved_unitdata_req2	NOT_USED
<A>	sdu	GRRREQ_SDU100_SAPI3
	sdu	GRRREQ_SDU100_SAPI5
<C>	sdu	GRRREQ_SDU100_SAPI9
<D>	sdu	GRRREQ_SDU100_SAPI11

(10) LL_UNITREADY_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

29-May-2000	DB	Initial
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
12-Sep-2001	GS	SAPI 7 variant removed
18-Oct-2001	GS	Suspension cause added
30-Jan-2002	GS	GRR suspend/resume handling added.
22-Mar-2002	GS	Change susp_cause to call

3.14.2 LLC601: Transmission of several frames for different SAPIs, LLC suspended/resumed with frame transmission on SAPI 1

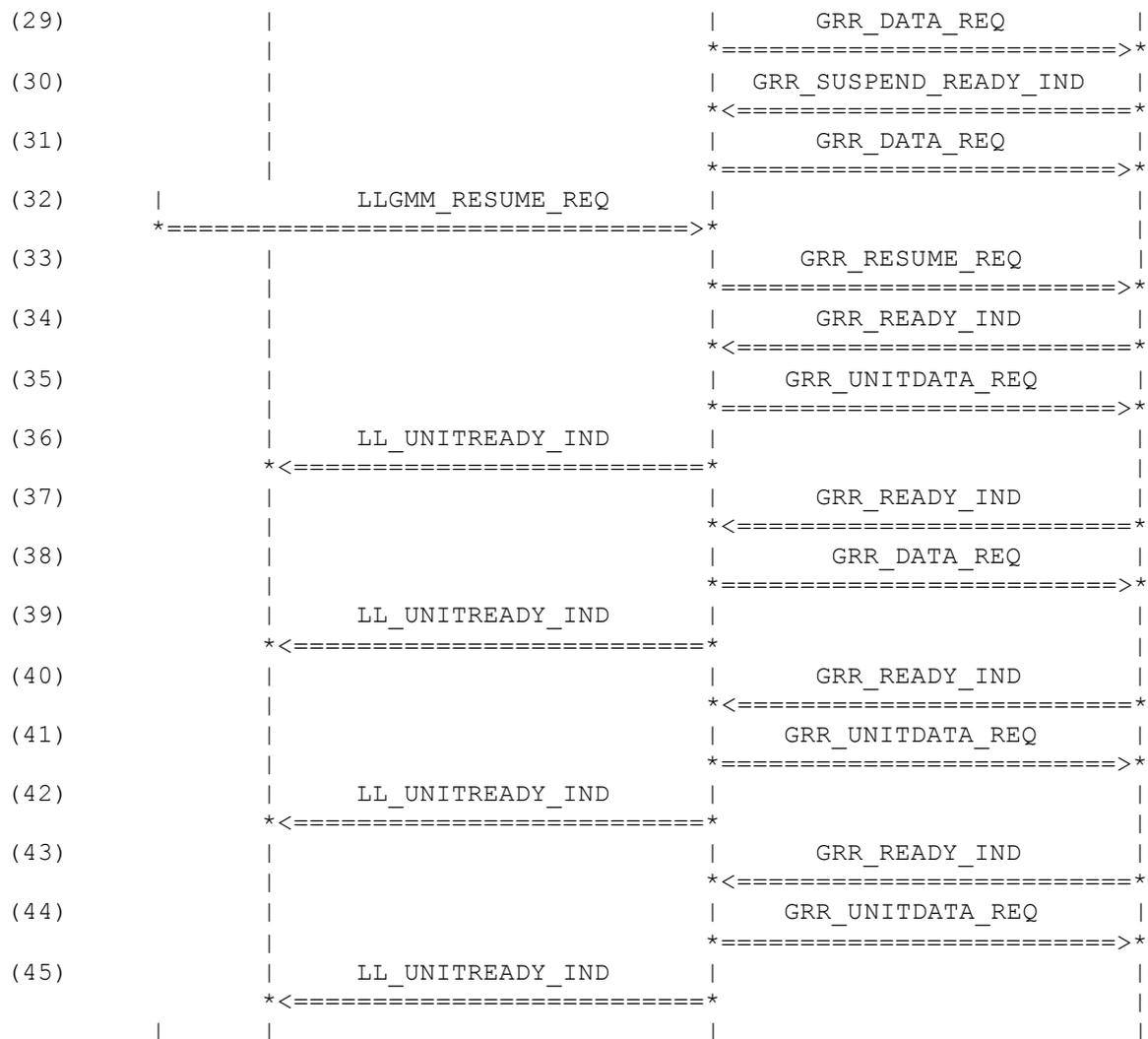
Description:

LLC receives data (100 octets) for unacknowledged transmission on every SAPI. RLC/MAC is not ready to receive data. RLC/MAC indicates ready and LLC sends data on SAPIs 1 and 3 before GMM suspends LLC. LLC receives six requests for unacknowledged data transmission on SAPI 1 from GMM. RLC/MAC already indicated ready, so the first frame is sent. RLC/MAC indicates ready and LLC is able to send all requested data for SAPI 1. GMM resumes LLC, and RLC/MAC indicates ready. LLC sends the buffered frames on SAPIs 5, 7, 9, and 11.

Preamble:

LLC001

	GMM	L3	LLC	RLC/MAC
(1)		LL_UNITDATA_REQ		
		=====>		
(2)		LL_UNITDATA_REQ		
		=====>		
(3)		LL_UNITDATA_REQ		
		=====>		
(4)		LL_UNITDATA_REQ		
		=====>		
(5)		LL_UNITDATA_REQ		
		=====>		
(6)		LL_UNITDATA_REQ		
		=====>		
(7)			GRR_READY_IND	
			<=====	
(8)			GRR_DATA_REQ	
			=====>	
(9)			GRR_READY_IND	
			<=====	
(10)			GRR_UNITDATA_REQ	
			=====>	
(11)		LL_UNITREADY_IND		
		<=====		
(12)		LLGMM_SUSPEND_REQ		
		=====>		
(13)			GRR_SUSPEND_REQ	
			=====>	
(14)			GRR_SUSPEND_READY_IND	
			<=====	
TIMEOUT (1000)				
(15)		LL_UNITDATA_REQ		
		=====>		
(16)			GRR_DATA_REQ	
			=====>	
(17)		LL_UNITDATA_REQ		
		=====>		
(18)		LL_UNITDATA_REQ		
		=====>		
(19)		LL_UNITDATA_REQ		
		=====>		
(20)		LL_UNITDATA_REQ		
		=====>		
(21)		LL_UNITDATA_REQ		
		=====>		
(22)			GRR_SUSPEND_READY_IND	
			<=====	
(23)			GRR_DATA_REQ	
			=====>	
(24)			GRR_SUSPEND_READY_IND	
			<=====	
(25)			GRR_DATA_REQ	
			=====>	
(26)			GRR_SUSPEND_READY_IND	
			<=====	
(27)			GRR_DATA_REQ	
			=====>	
(28)			GRR_SUSPEND_READY_IND	
			<=====	



Parametrization:

Primitive	Parameter	Value
(1) LL_UNITDATA_REQ	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_1
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LL_UNITDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF

	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(3) LL_UNITDATA_REQ		
	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(4) LL_UNITDATA_REQ		
	sapi	LL_SAPI_7
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(5) LL_UNITDATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(6) LL_UNITDATA_REQ		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(7) GRR_READY_IND		

(8) GRR_DATA_REQ

sapi	LL_SAPI_1
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	LL_RADIO_PRIO_1
cause	GRR_DTACS_MOBILITY_MANAGEMENT
reserved_data_req	NOT_USED
sdu	GRRREQ_SDU100_SAPI1_PM

(9) GRR_READY_IND

(10) GRR_UNITDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	LL_RADIO_PRIO_3
reserved_unitdata_req1	NOT_USED
reserved_unitdata_req2	NOT_USED
sdu	GRRREQ_SDU100_SAPI3

(11) LL_UNITREADY_IND

sapi	LL_SAPI_3
tlli	LL_TLLI_INVALID

(12) LLGMM_SUSPEND_REQ

susp_cause	LLGMM_RAU
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(13) GRR_SUSPEND_REQ

(14) GRR_SUSPEND_READY_IND

(15) LL_UNITDATA_REQ

sapi	LL_SAPI_1
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL3
radio_prio	LL_RADIO_PRIO_1
cipher	LL_CIPHER_OFF
reserved_unitdata_req1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE
reserved_unitdata_req4	NOT_USED
reserved_unitdata_req5	NOT_USED
sdu	LLREQ_SDU100

(16) GRR_DATA_REQ

sapi	LL_SAPI_1
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	LL_RADIO_PRIO_1
cause	GRR_DTACS_MOBILITY_MANAGEMENT
reserved_data_req	NOT_USED
sdu	GRRREQ_SDU100_SAPI1_NU1_PM

(17) LL_UNITDATA_REQ

sapi	LL_SAPI_1
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL3
radio_prio	LL_RADIO_PRIO_1
cipher	LL_CIPHER_OFF
reserved_unitdata_req1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE

	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(18) LL_UNITDATA_REQ		
	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_1
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(19) LL_UNITDATA_REQ		
	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_1
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(20) LL_UNITDATA_REQ		
	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_1
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(21) LL_UNITDATA_REQ		
	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL3
	radio_prio	LL_RADIO_PRIO_1
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(22) GRR_SUSPEND_READY_IND		
(23) GRR_DATA_REQ		
	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1

	grr_qos radio_prio cause reserved_data_req sdu	GRRREQ_QOS_PEAKE LL_RADIO_PRIO_1 GRR_DTACS_MOBILITY_MANAGEMENT NOT_USED GRRREQ_SDU100_SAPI1_NU2_PM
(24) GRR_SUSPEND_READY_IND		
(25) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 GRRREQ_QOS_PEAKE LL_RADIO_PRIO_1 GRR_DTACS_MOBILITY_MANAGEMENT NOT_USED GRRREQ_SDU100_SAPI1_NU3_PM
(26) GRR_SUSPEND_READY_IND		
(27) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 GRRREQ_QOS_PEAKE LL_RADIO_PRIO_1 GRR_DTACS_MOBILITY_MANAGEMENT NOT_USED GRRREQ_SDU100_SAPI1_NU4_PM
(28) GRR_SUSPEND_READY_IND		
(29) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 GRRREQ_QOS_PEAKE LL_RADIO_PRIO_1 GRR_DTACS_MOBILITY_MANAGEMENT NOT_USED GRRREQ_SDU100_SAPI1_NU5_PM
(30) GRR_SUSPEND_READY_IND		
(31) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_1 TLLI_LOCAL_1 GRRREQ_QOS_PEAKE LL_RADIO_PRIO_1 GRR_DTACS_MOBILITY_MANAGEMENT NOT_USED GRRREQ_SDU100_SAPI1_NU6_PM
(32) LLGMM_RESUME_REQ		
(33) GRR_RESUME_REQ		
(34) GRR_READY_IND		
(35) GRR_UNITDATA_REQ	sapi tlli grr_qos radio_prio reserved_unitdata_req1 reserved_unitdata_req2 sdu	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAKE LL_RADIO_PRIO_3 NOT_USED NOT_USED GRRREQ_SDU100_SAPI5

(36) LL_UNITREADY_IND	sapi tlli	LL_SAPI_5 LL_TLLI_INVALID
(37) GRR_READY_IND		
(38) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_7 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB LL_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRRREQ_SDU100_SAPI7_PM
(39) LL_UNITREADY_IND	sapi tlli	LL_SAPI_7 LL_TLLI_INVALID
(40) GRR_READY_IND		
(41) GRR_UNITDATA_REQ	sapi tlli grr_qos radio_prio reserved_unitdata_req1 reserved_unitdata_req2 sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB LL_RADIO_PRIO_3 NOT_USED NOT_USED GRRREQ_SDU100_SAPI9
(42) LL_UNITREADY_IND	sapi tlli	LL_SAPI_9 LL_TLLI_INVALID
(43) GRR_READY_IND		
(44) GRR_UNITDATA_REQ	sapi tlli grr_qos radio_prio reserved_unitdata_req1 reserved_unitdata_req2 sdu	LL_SAPI_11 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB LL_RADIO_PRIO_3 NOT_USED NOT_USED GRRREQ_SDU100_SAPI11
(45) LL_UNITREADY_IND	sapi tlli	LL_SAPI_11 LL_TLLI_INVALID

History:

30-May-2000	DB	Initial
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
12-Sep-2001	GS	Use GRR_DATA_REQ in case of SAPI 7
14-Sep-2001	GS	GRR_DATA_REQ cause changed
18-Oct-2001	GS	Suspension cause added
30-Jan-2002	GS	GRR suspend / resume handling added

3.14.3 LLC602: Receipt of several frames for different SAPIs while LLC is suspended/resumed

Description:

LLC receives unacknowledged data (100 octets) on every SAPI from RLC/MAC. Layer 3 already indicated that it is ready to receive data. LLC sends the received data to Layer 3, while GMM suspends LLC, requests an unacknowledged frame transmission on SAPI 1, and resumes LLC.. RLC/MAC indicates ready and LLC is able to send the requested data for SAPI 1, while the reception of frames continues.

Preamble:

LLC001

	GMM	L3	LLC	RLC/MAC
(1)		LL_GETUNITDATA_REQ		
		=====>		
(2)		LL_GETUNITDATA_REQ		
		=====>		
(3)		LL_GETUNITDATA_REQ		
		=====>		
(4)		LL_GETUNITDATA_REQ		
		=====>		
(5)		LL_GETUNITDATA_REQ		
		=====>		
(6)			GRR_UNITDATA_IND_TEST	
			<=====	
(7)		LL_UNITDATA_IND		
		<=====		
(8)			GRR_UNITDATA_IND_TEST	
			<=====	
(9)		LL_UNITDATA_IND		
		<=====		
(10)		LLGMM_SUSPEND_REQ		
		=====>		
(11)			GRR_SUSPEND_REQ	
			=====>	
(12)		LL_UNITDATA_REQ		
		=====>		
(13)			GRR_UNITDATA_IND_TEST	
			<=====	
(14)		LL_UNITDATA_IND		
		<=====		
(15)			GRR_SUSPEND_READY_IND	
			<=====	
(16)			GRR_DATA_REQ	
			=====>	
(17)			GRR_UNITDATA_IND_TEST	
			<=====	
(18)		LL_UNITDATA_IND		
		<=====		
(19)		LLGMM_RESUME_REQ		
		=====>		
(20)			GRR_RESUME_REQ	
			=====>	
(21)			GRR_READY_IND	
			<=====	
(22)			GRR_UNITDATA_IND_TEST	
			<=====	
(23)		LL_UNITDATA_IND		
		<=====		
(24)			GRR_UNITDATA_IND_TEST	
			<=====	
(25)		LL_UNITDATA_IND		
		<=====		

Parametrization:

Primitive	Parameter	Value
-----------	-----------	-------

(1) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(2) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1
(3) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_7 TLLI_LOCAL_1
(4) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(5) LL_GETUNITDATA_REQ	sapi tlli	LL_SAPI_11 TLLI_LOCAL_1
(6) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI1
(7) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_1 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI1
(8) GRR_UNITDATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_DESCLIST100_SAPI3
(9) LL_UNITDATA_IND	sapi tlli reserved_unitdata_ind1 reserved_unitdata_ind2 reserved_unitdata_ind3 reserved_unitdata_ind4 reserved_unitdata_ind5 cipher sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LL_CIPHER_OFF LLIND_SDU100_SAPI3
(10) LLGMM_SUSPEND_REQ	susp_cause	LLGMM_RAU
(11) GRR_SUSPEND_REQ		
(12) LL_UNITDATA_REQ	sapi tlli ll_qos radio_prio cipher reserved_unitdata_req1 seg_pos	LL_SAPI_1 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL3 LL_RADIO_PRIO_1 LL_CIPHER_OFF NOT_USED NOT_USED

	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(13) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI5
(14) LL_UNITDATA_IND		
	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_SAPI5
(15) GRR_SUSPEND_READY_IND		
(16) GRR_DATA_REQ		
	sapi	LL_SAPI_1
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	LL_RADIO_PRIO_1
	cause	GRR_DTACS_MOBILITY_MANAGEMENT
	reserved_data_req	NOT_USED
	sdu	GRRREQ_SDU100_SAPI1_PM
(17) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI7
(18) LL_UNITDATA_IND		
	sapi	LL_SAPI_7
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_SAPI7
(19) LLGMM_RESUME_REQ		
(20) GRR_RESUME_REQ		
(21) GRR_READY_IND		
(22) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI9
(23) LL_UNITDATA_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED

	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_SAPI9
(24) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_DESCLIST100_SAPI11
(25) LL_UNITDATA_IND		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
	sdu	LLIND_SDU100_SAPI11

History:

30-May-2000	DB	Initial
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
14-Sep-2001	GS	GRR_DATA_REQ cause changed
18-Oct-2001	GS	Suspension cause added
30-Jan-2002	GS	GRR suspend/resume handling added

3.14.4 LLC605: LLC suspended due to a call, unacknowledged frame transmission requested, LLC resumed, send buffered frame

Description:

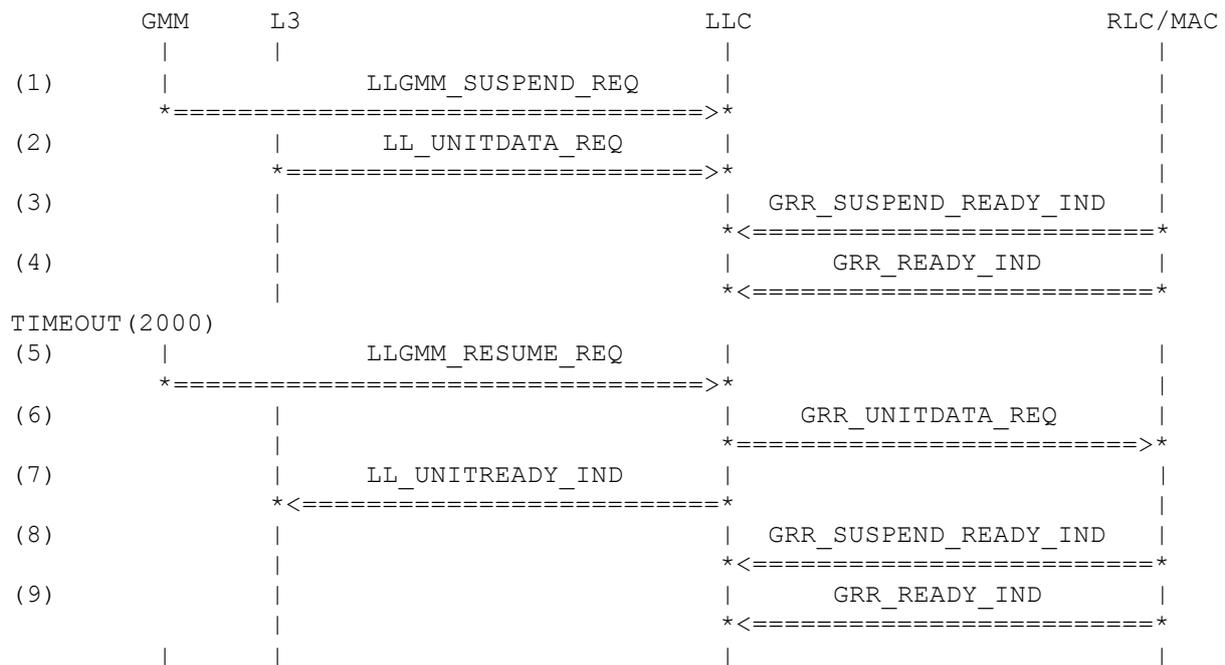
LLC is being suspended by GMM. LLC receives data (100 octets) for unacknowledged transmission. RLC/MAC is not ready to receive data, so the frame is buffered. RLC/MAC indicates a suspend ready and a normal which both is ignored. GMM resumes LLC. GRR sends an additional suspend ready, which is also ignored. After receiving a normal ready from GRR LLC sends the buffered UI frame containing the layer 3 data.

Preamble:

LLC001

Variants:

<A>....<D>

**Parametrization:**

Primitive	Parameter	Value
(1) LLGMM_SUSPEND_REQ		
<A>	susp_cause	LLGMM_CALL
	susp_cause	LLGMM_CALL
<C>	susp_cause	LLGMM_CALL
<D>	susp_cause	LLGMM_CALL
(2) LL_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(3) GRR_SUSPEND_READY_IND		
(4) GRR_READY_IND		
(5) LLGMM_RESUME_REQ		
(6) GRR_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	LL_RADIO_PRIO_3
	reserved_unitdata_req1	NOT_USED
	reserved_unitdata_req2	NOT_USED
<A>	sdu	GRRREQ_SDU100_SAPI3
	sdu	GRRREQ_SDU100_SAPI5
<C>	sdu	GRRREQ_SDU100_SAPI9
<D>	sdu	GRRREQ_SDU100_SAPI11
(7) LL_UNITREADY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(8) GRR_SUSPEND_READY_IND		
(9) GRR_READY_IND		

History:

22-Mar-2002	GS	Initial
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3.14.5 LLC606: LLC suspended tree times (call, rau, call), unacknowledged frame transmission requested, LLC resumed, send buffered frame

Description:

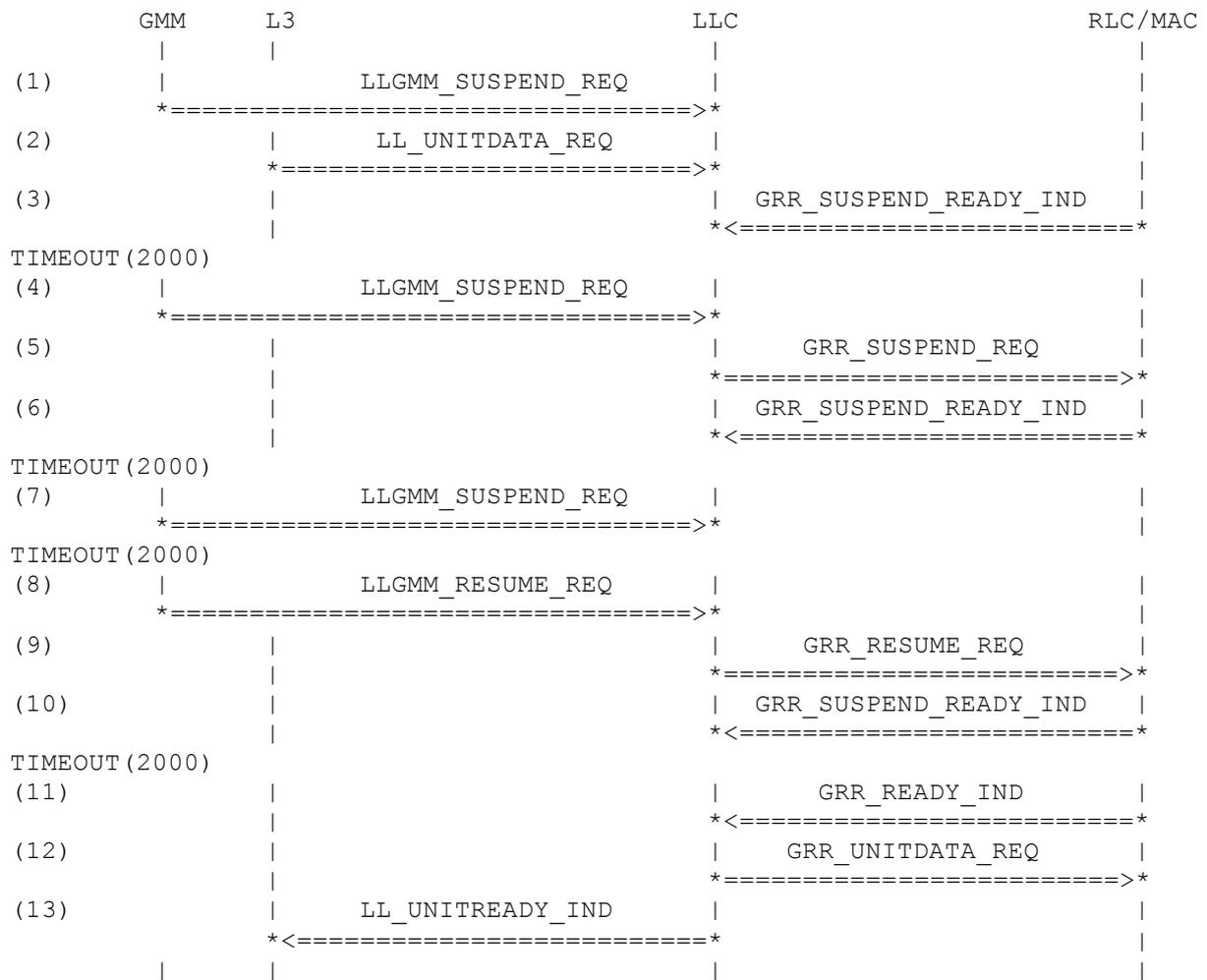
LLC is being suspended by GMM due to a call. LLC receives data (100 octets) for unacknowledged transmission. RLC/MAC is not ready to receive data, so the frame is buffered. RLC/MAC indicates a suspend ready, which is ignored. GMM sends an additional suspend, this due to RAU. LLC forwards the suspend to GRR. GMM sends a third suspend, this due to a call again. This is ignored. After receiving a resume from GMM LLC resumes GRR and sends the buffered UI frame containing the layer 3 data after receiving a normal redy indication.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) LLGMM_SUSPEND_REQ	susp_cause	LLGMM_CALL
(2) LL_UNITDATA_REQ	sapi	LL_SAPI_3
<A>	sapi	LL_SAPI_5
	sapi	LL_SAPI_9
<C>	sapi	LL_SAPI_11
<D>	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(3) GRR_SUSPEND_READY_IND		

(4) LLGMM_SUSPEND_REQ		
<A>	susp_cause	LLGMM_RAU
	susp_cause	LLGMM_RAU
<C>	susp_cause	LLGMM_RAU
<D>	susp_cause	LLGMM_RAU
(5) GRR_SUSPEND_REQ		
(6) GRR_SUSPEND_READY_IND		
(7) LLGMM_SUSPEND_REQ		
	susp_cause	LLGMM_CALL
(8) LLGMM_RESUME_REQ		
(9) GRR_RESUME_REQ		
(10) GRR_SUSPEND_READY_IND		
(11) GRR_READY_IND		
(12) GRR_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	LL_RADIO_PRIO_3
	reserved_unitdata_req1	NOT_USED
	reserved_unitdata_req2	NOT_USED
<A>	sdu	GRRREQ_SDU100_SAPI3
	sdu	GRRREQ_SDU100_SAPI5
<C>	sdu	GRRREQ_SDU100_SAPI9
<D>	sdu	GRRREQ_SDU100_SAPI11
(13) LL_UNITREADY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

22-Mar-2002	GS	Initial
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3.14.6 LLC607: LLC suspended due to RAU, unacknowledged frame transmission requested, LLC un assigned and then assigned, send buffered frame

Description:

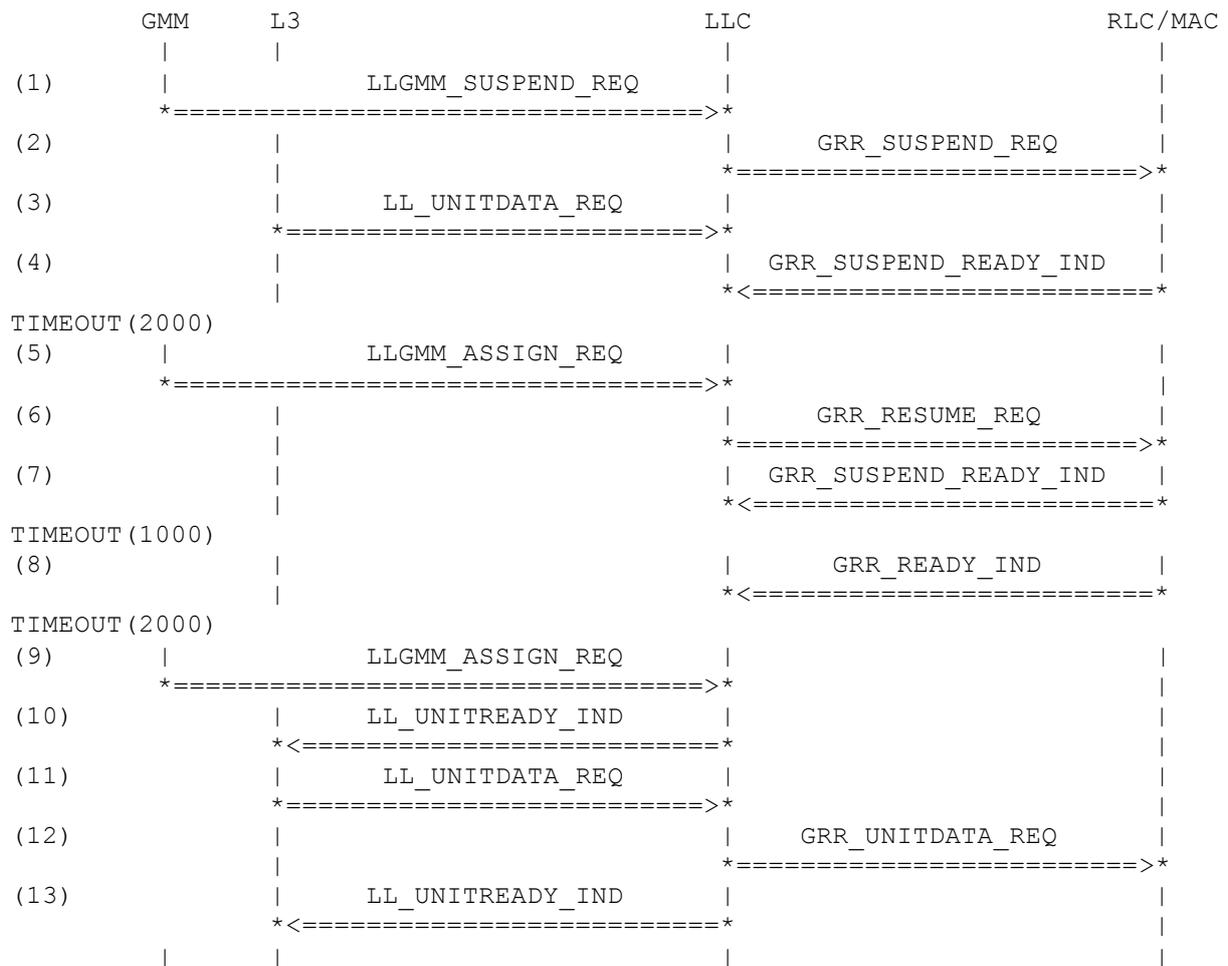
LLC is being suspended by GMM due to RAU. LLC receives data (100 octets) for unacknowledged transmission. GMM unassigns LLC, and therefore LLC resumes GRR. After new assignment LLC receives new data for transmission and sends the buffered UI frame containing the layer 3 data. This shows, that the flow control is not resetted in unassigned state.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) LLGMM_SUSPEND_REQ		
<A>	susp_cause	LLGMM_RAU
	susp_cause	LLGMM_RAU
<C>	susp_cause	LLGMM_RAU
<D>	susp_cause	LLGMM_RAU
(2) GRR_SUSPEND_REQ		
(3) LL_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100

(4) GRR_SUSPEND_READY_IND

(5) LLGMM_ASSIGN_REQ

old_tlli	TLLI_LOCAL_1
new_tlli	LLGMM_TLLI_INVALID
llgmm_kc	LLGMM_KC_NULL
ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM

(6) GRR_RESUME_REQ

(7) GRR_SUSPEND_READY_IND

(8) GRR_READY_IND

(9) LLGMM_ASSIGN_REQ

old_tlli	LLGMM_TLLI_INVALID
new_tlli	TLLI_LOCAL_1
llgmm_kc	LLGMM_KC_NULL
ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM

(10) LL_UNITREADY_IND

<A>

sapi LL_SAPI_3

sapi LL_SAPI_5

<C>

sapi LL_SAPI_9

<D>

sapi LL_SAPI_11

tlli LL_TLLI_INVALID

(11) LL_UNITDATA_REQ

<A>

sapi LL_SAPI_3

sapi LL_SAPI_5

<C>

sapi LL_SAPI_9

<D>

sapi LL_SAPI_11

tlli TLLI_LOCAL_1

ll_qos LLREQ_QOS_DEL4_REL5

radio_prio LL_RADIO_PRIO_3

cipher LL_CIPHER_OFF

reserved_unitdata_req1 NOT_USED

seg_pos NOT_USED

attached_counter LLC_NO_ATTACHE

reserved_unitdata_req4 NOT_USED

reserved_unitdata_req5 NOT_USED

sdu LLREQ_SDU100

(12) GRR_UNITDATA_REQ

<A>

sapi LL_SAPI_3

sapi LL_SAPI_5

<C>

sapi LL_SAPI_9

<D>

sapi LL_SAPI_11

tlli TLLI_LOCAL_1

grr_qos GRRREQ_QOS_PEAKSUB

radio_prio LL_RADIO_PRIO_3

reserved_unitdata_req1 NOT_USED

reserved_unitdata_req2 NOT_USED

<A>

sdu GRRREQ_SDU100_SAPI3

sdu GRRREQ_SDU100_SAPI5

<C>

sdu GRRREQ_SDU100_SAPI9

<D>

sdu GRRREQ_SDU100_SAPI11

(13) LL_UNITREADY_IND

<A>

sapi LL_SAPI_3

sapi LL_SAPI_5

<C>

sapi LL_SAPI_9

<D> sapi LL_SAPI_11
 tli LL_TLLI_INVALID

History: 29-May-2000 GS Initial

3.14.7 LLC610: LLC suspended, Layer-3 initiated ABM establishment, UA response

Description:

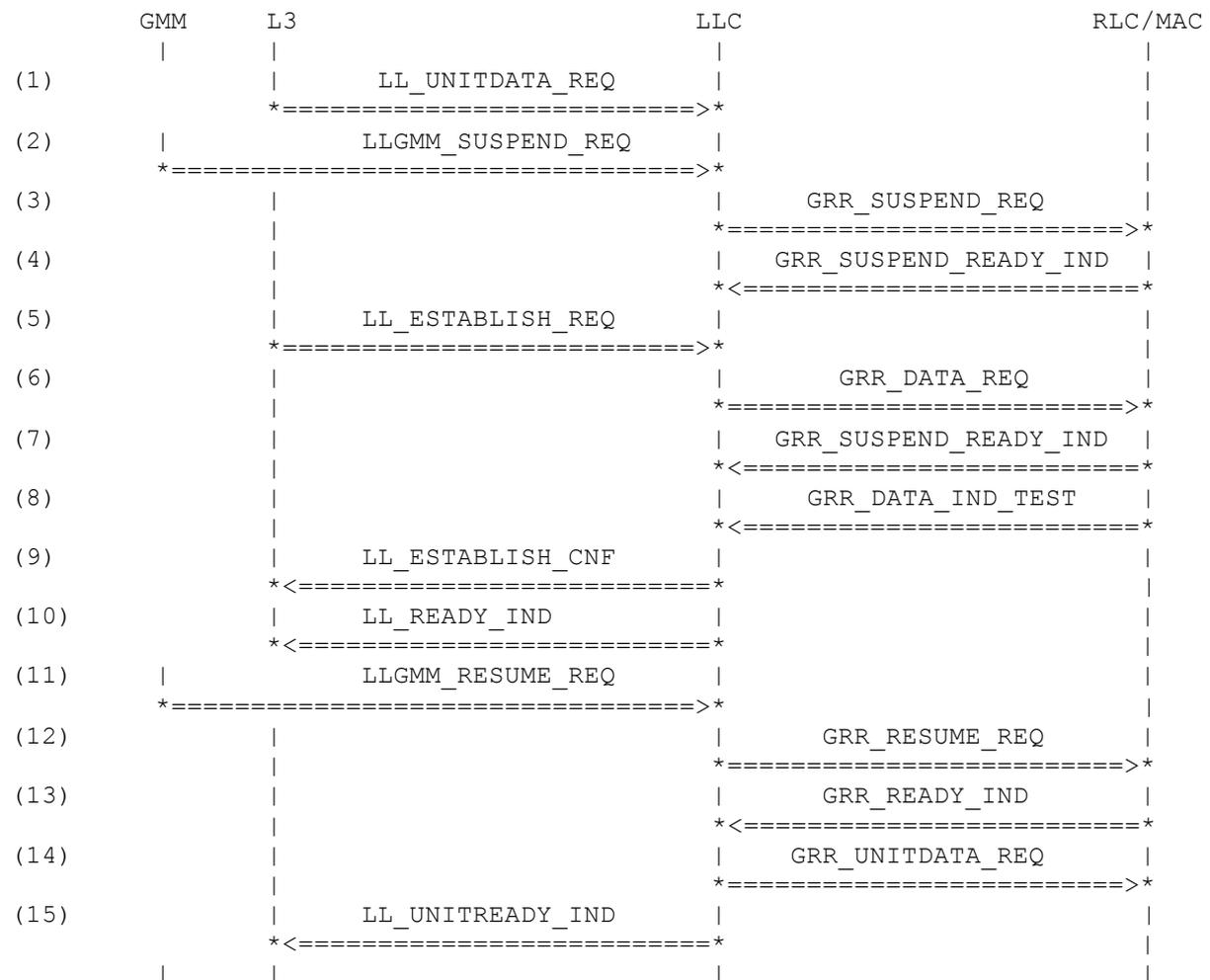
LLC is requested to transmit an unacknowledged frame, RLC/MAC is not ready. GMM suspends LLC. LLC receives the primitive LL_ESTABLISH_REQ from SMDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with UA, ABM is established. GMM resumes LLC. RLC/MAC already indicated that it is ready to received data, LLC sends the buffered unacknowledged frame.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
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(1) LL_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	cipher	LL_CIPHER_OFF
	reserved_unitdata_req1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_unitdata_req4	NOT_USED
	reserved_unitdata_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LLGMM_SUSPEND_REQ		
	susp_cause	LLGMM_RAU
(3) GRR_SUSPEND_REQ		
(4) GRR_SUSPEND_READY_IND		
(5) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(6) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_EMPTY_L3_XID_SAPI3
	sdu	MS_SABM_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_SABM_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_SABM_EMPTY_L3_XID_SAPI11
(7) GRR_SUSPEND_READY_IND		
(8) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI3
	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI5
<C>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI9
<D>	sdu	SGSN_UA1_EMPTY_L3_XID_SAPI11
(9) LL_ESTABLISH_CNF		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_DEF_SAPI3

	n201_u	N201_U_DEF_SAPI5
<C>	n201_u	N201_U_DEF_SAPI9
<D>	n201_u	N201_U_DEF_SAPI11
	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(10) LL_READY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(11) LLGMM_RESUME_REQ		
(12) GRR_RESUME_REQ		
(13) GRR_READY_IND		
(14) GRR_UNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	LL_RADIO_PRIO_3
	reserved_unitdata_req1	NOT_USED
	reserved_unitdata_req2	NOT_USED
<A>	sdu	GRRREQ_SDU100_SAPI3
	sdu	GRRREQ_SDU100_SAPI5
<C>	sdu	GRRREQ_SDU100_SAPI9
<D>	sdu	GRRREQ_SDU100_SAPI11
(15) LL_UNITREADY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

History:

30-May-2000	DB	Initial
21-Jun-2000	DB	Modified due to changed LL/GRR SAPs
07-Nov-2000	GS	LL_READY_IND added
11-Oct-2001	GS	Add empty I3 sdu
18-Oct-2001	GS	Suspension cause added
30-Jan-2002	GS	GRR suspend/resume handling added

3.15 Acknowledged transmission of frames (LLC700-LC799)

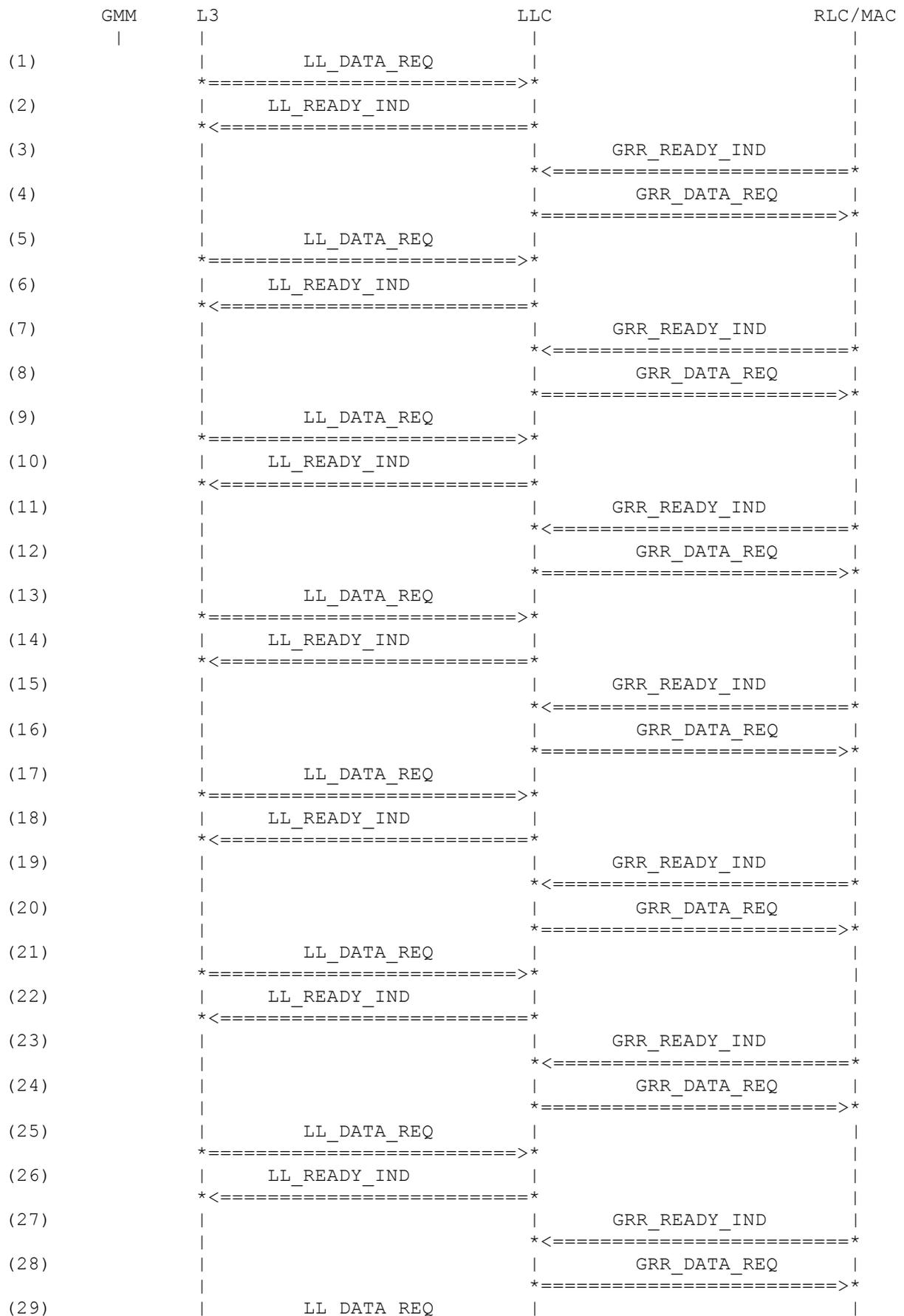
3.15.1 LLC700: ABM - L3 data request on SAPI 3 forwarded to GRR after ready indication

Description:

LLC receives data (100 octets) for acknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an I frame to RLC/MAC using the primitive GRR_DATA_REQ. This test is only applicable to SAPI3

Preamble:

LLC400A



```

(30)      *=====>*
          | LL_READY_IND |
(31)      *<=====*
```

|

```

          | GRR_READY_IND |
(32)      | GRR_READY_IND |
          | GRR_DATA_REQ |
          *=====>*
(33)      | LL_DATA_REQ |
          *=====>*
(34)      | LL_READY_IND |
          *<=====*
```

|

```

          | GRR_READY_IND |
(35)      | GRR_READY_IND |
          | GRR_DATA_REQ |
          *=====>*
(36)      | LL_DATA_REQ |
          *=====>*
(37)      | LL_READY_IND |
          *<=====*
```

|

```

          | GRR_READY_IND |
(38)      | GRR_READY_IND |
          | GRR_DATA_REQ |
          *=====>*
(39)      | LL_DATA_REQ |
          *=====>*
(40)      | LL_READY_IND |
          *<=====*
```

|

```

          | GRR_READY_IND |
(41)      | GRR_READY_IND |
          | GRR_DATA_REQ |
          *=====>*
(42)      | LL_DATA_REQ |
          *=====>*
(43)      | LL_READY_IND |
          *<=====*
```

|

```

          | GRR_READY_IND |
(44)      | GRR_READY_IND |
          | GRR_DATA_REQ |
          *=====>*
(45)      | LL_DATA_REQ |
          *=====>*
(46)      | LL_READY_IND |
          *<=====*
```

|

```

          | GRR_READY_IND |
(47)      | GRR_READY_IND |
          | GRR_DATA_REQ |
          *=====>*
(48)      | LL_DATA_REQ |
          *=====>*
(49)      | LL_READY_IND |
          *<=====*
```

|

```

          | GRR_READY_IND |
(50)      | GRR_READY_IND |
          | GRR_DATA_REQ |
          *=====>*
(51)      | LL_DATA_REQ |
          *=====>*
(52)      | LL_READY_IND |
          *<=====*
```

|

```

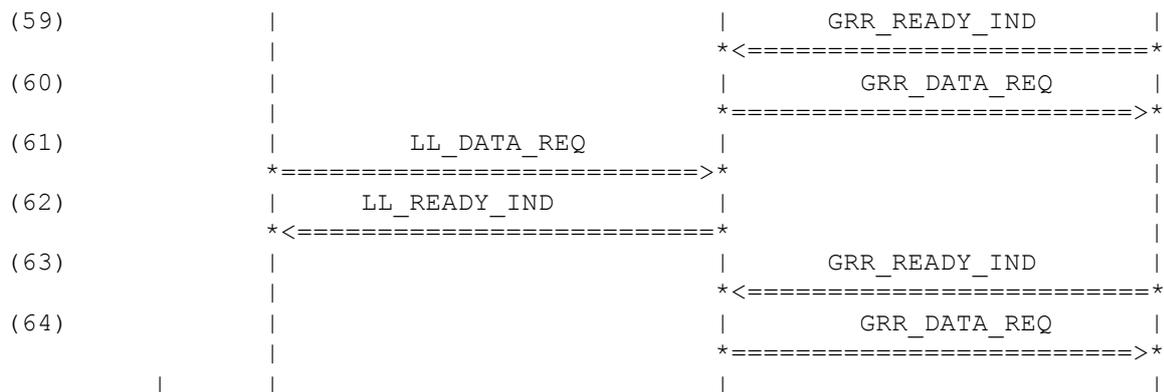
          | GRR_READY_IND |
(53)      | GRR_READY_IND |
          | GRR_DATA_REQ |
          *=====>*
(54)      | LL_DATA_REQ |
          *=====>*
(55)      | LL_READY_IND |
          *<=====*
```

|

```

          | GRR_READY_IND |
(56)      | GRR_READY_IND |
          | GRR_DATA_REQ |
          *=====>*
(57)      | LL_DATA_REQ |
          *=====>*
(58)      | LL_READY_IND |
          *<=====*
```

|



Parametrization:

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
(2) LL_READY_IND	sdu	LLREQ_SDU100
	sapi	LL_SAPI_3
(3) GRR_READY_IND	tli	TLLI_LOCAL_1
	sapi	LL_SAPI_3
(4) GRR_DATA_REQ	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKESSUB
(5) LL_DATA_REQ	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS0_NR0_SAPI3
	sapi	LL_SAPI_3
(5) LL_DATA_REQ	tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100

(6) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(7) GRR_READY_IND		
(8) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS1_NR0_SAPI3
(9) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DELE4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(10) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS2_NR0_A1_SAPI3
(13) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DELE4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(14) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(15) GRR_READY_IND		

(16) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS3_NR0_SAPI3
(17) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(18) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(19) GRR_READY_IND		
(20) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS4_NR0_SAPI3
(21) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(22) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(23) GRR_READY_IND		
(24) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF

	reserved_data_req sdu	NOT_USED GRR_DATAREQ_NS5_NR0_SAPI3
(25) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(26) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(27) GRR_READY_IND		
(28) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS6_NR0_A1_SAPI3
(29) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(30) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(31) GRR_READY_IND		
(32) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS7_NR0_SAPI3
(33) LL_DATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1

	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(34) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(35) GRR_READY_IND		
(36) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS8_NR0_A1_SAPI3
(37) LL_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(38) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(39) GRR_READY_IND		
(40) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS9_NR0_SAPI3
(41) LL_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE

	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(42) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(43) GRR_READY_IND		
(44) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS10_NR0_SAPI3
(45) LL_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(46) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(47) GRR_READY_IND		
(48) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS11_NR0_SAPI3
(49) LL_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100

(50) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(51) GRR_READY_IND		
(52) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS12_NR0_SAPI3
(53) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(54) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(55) GRR_READY_IND		
(56) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS13_NR0_SAPI3
(57) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(58) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(59) GRR_READY_IND		

(60) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_NS14_NRO_SAPI3

(61) LL_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL5
radio_prio	LL_RADIO_PRIO_3
reserved_data_req1	NOT_USED
reference1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE
reserved_data_req4	NOT_USED
reserved_data_req5	NOT_USED
sdu	LLREQ_SDU100

(62) LL_READY_IND

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1

(63) GRR_READY_IND

(64) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_NS15_NRO_SAPI3

History:

01-Sep-2000	SLM	Initial
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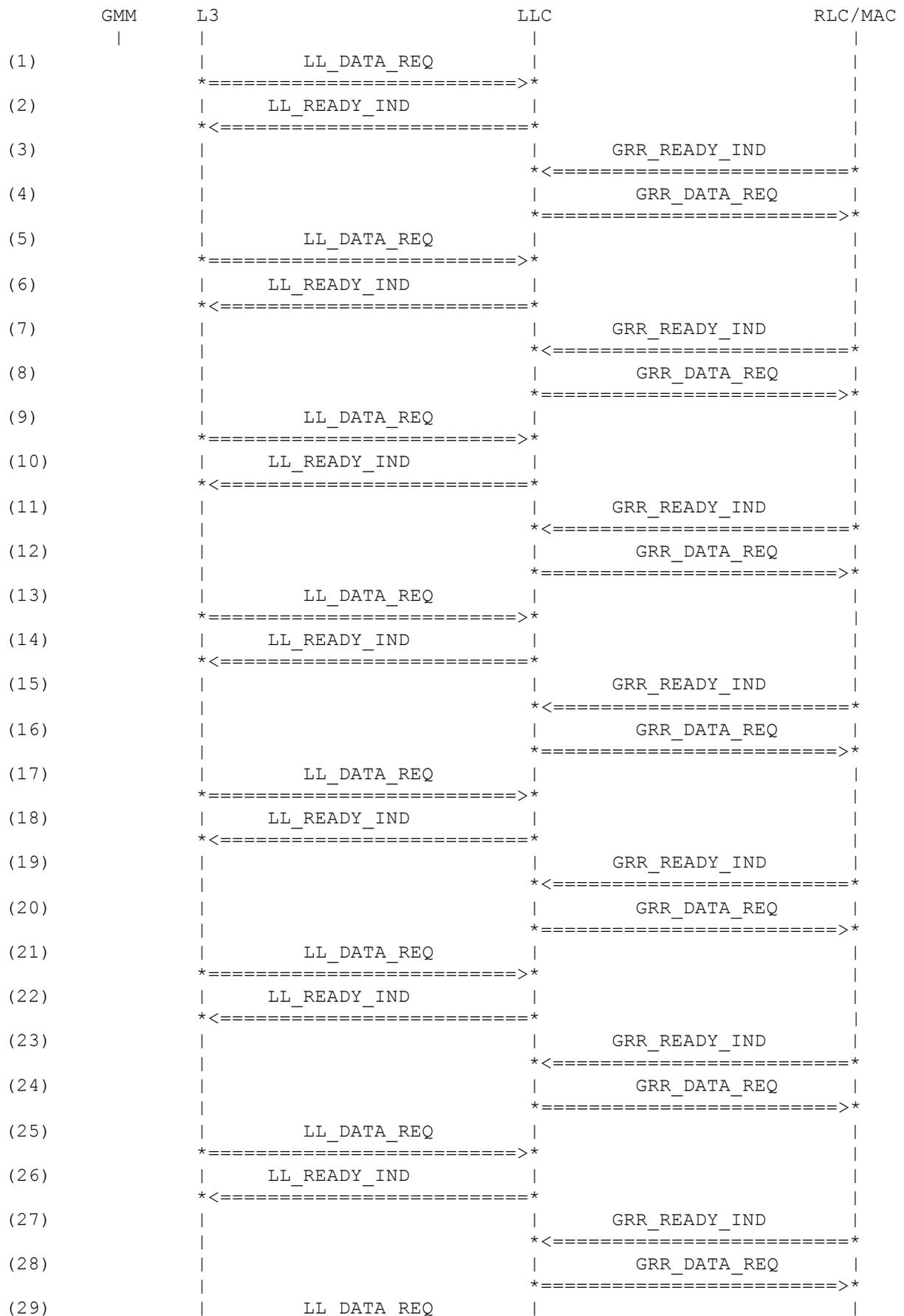
3.15.2 LLC701: ABM - L3 data request on SAPI 5 forwarded to GRR after ready indication

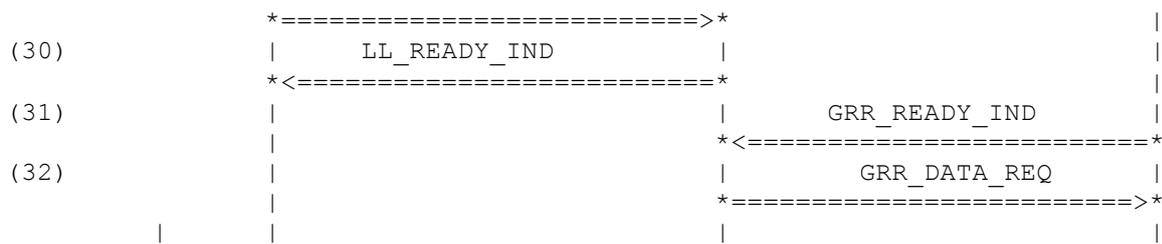
Description:

LLC receives data (100 octets) for acknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an I frame to RLC/MAC using the primitive GRR_DATA_REQ. This test is only applicable to SAPI5

Preamble:

LLC400B



**Parametrization:**

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LL_READY_IND	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS0_NR0_SAPI5
(5) LL_DATA_REQ	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(6) LL_READY_IND	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
(7) GRR_READY_IND		

(8) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS1_NR0_SAPI5
(9) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_5 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(10) LL_READY_IND	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS2_NR0_A1_SAPI5
(13) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_5 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(14) LL_READY_IND	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1
(15) GRR_READY_IND		
(16) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF

	reserved_data_req sdu	NOT_USED GRR_DATAREQ_NS3_NR0_SAPI5
(17) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_5 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(18) LL_READY_IND	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1
(19) GRR_READY_IND		
(20) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS4_NR0_SAPI5
(21) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_5 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 GRR_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(22) LL_READY_IND	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1
(23) GRR_READY_IND		
(24) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS5_NR0_SAPI5
(25) LL_DATA_REQ	sapi tlli	LL_SAPI_5 TLLI_LOCAL_1

	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(26) LL_READY_IND		
	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
(27) GRR_READY_IND		
(28) GRR_DATA_REQ		
	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS6_NR0_A1_SAPI5
(29) LL_DATA_REQ		
	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(30) LL_READY_IND		
	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
(31) GRR_READY_IND		
(32) GRR_DATA_REQ		
	sapi	LL_SAPI_5
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS7_NR0_SAPI5

History:

01-Sep-2000 SLM Initial

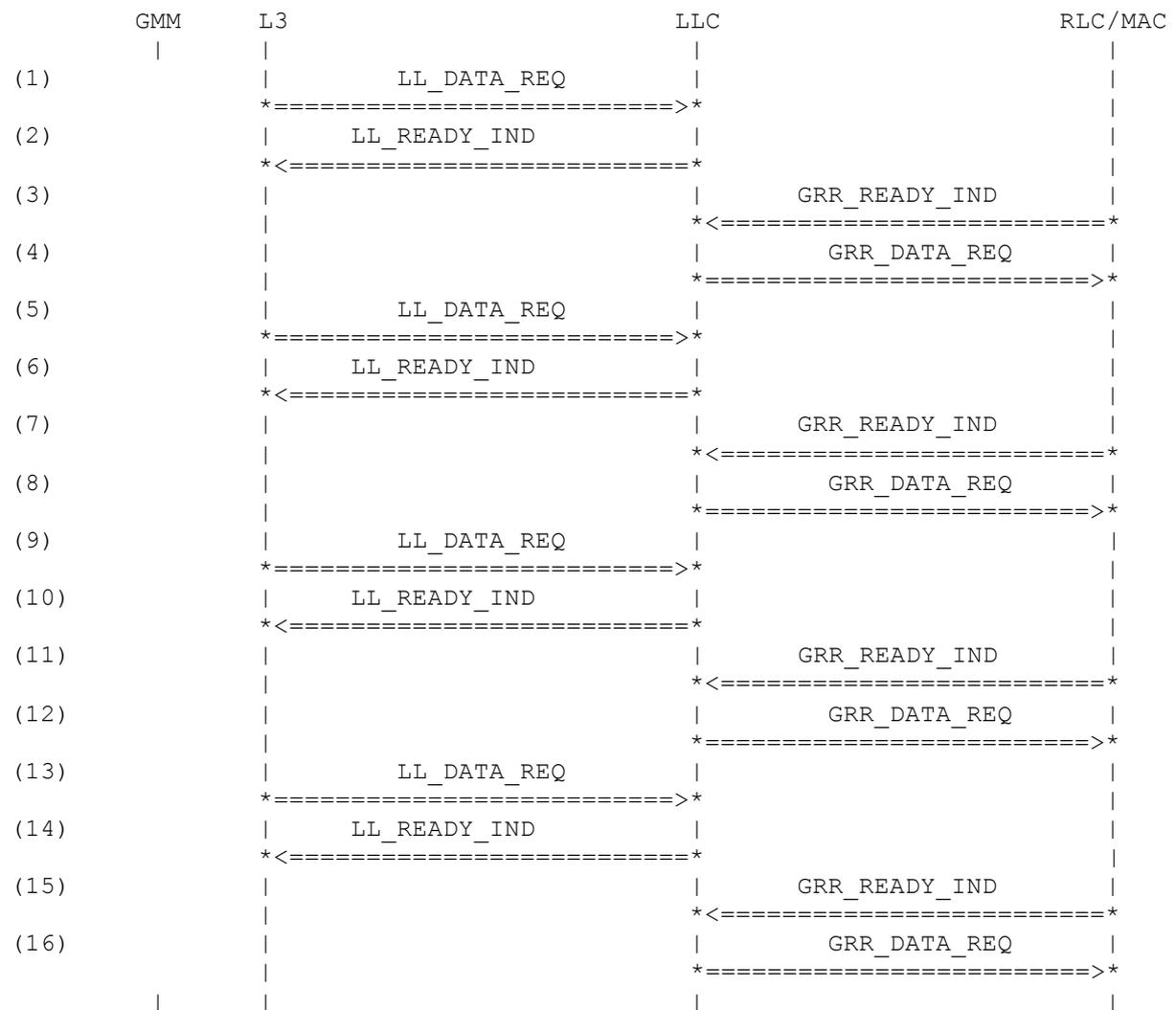
3.15.3 LLC702: ABM - L3 data request on SAPI 9 forwarded to GRR after ready indication

Description:

LLC receives data (100 octets) for acknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an I frame to RLC/MAC using the primitive GRR_DATA_REQ. This test is only applicable to SAPI9

Preamble:

LLC400C



Parametrization:

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED

	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS0_NR0_SAPI9
(5) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(6) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(7) GRR_READY_IND		
(8) GRR_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS1_NR0_SAPI9
(9) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100

(10) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS2_NR0_A1_SAPI9
(13) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_9 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU100
(14) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(15) GRR_READY_IND		
(16) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS3_NR0_SAPI9

History:

01-Sep-2000	SLM	Initial
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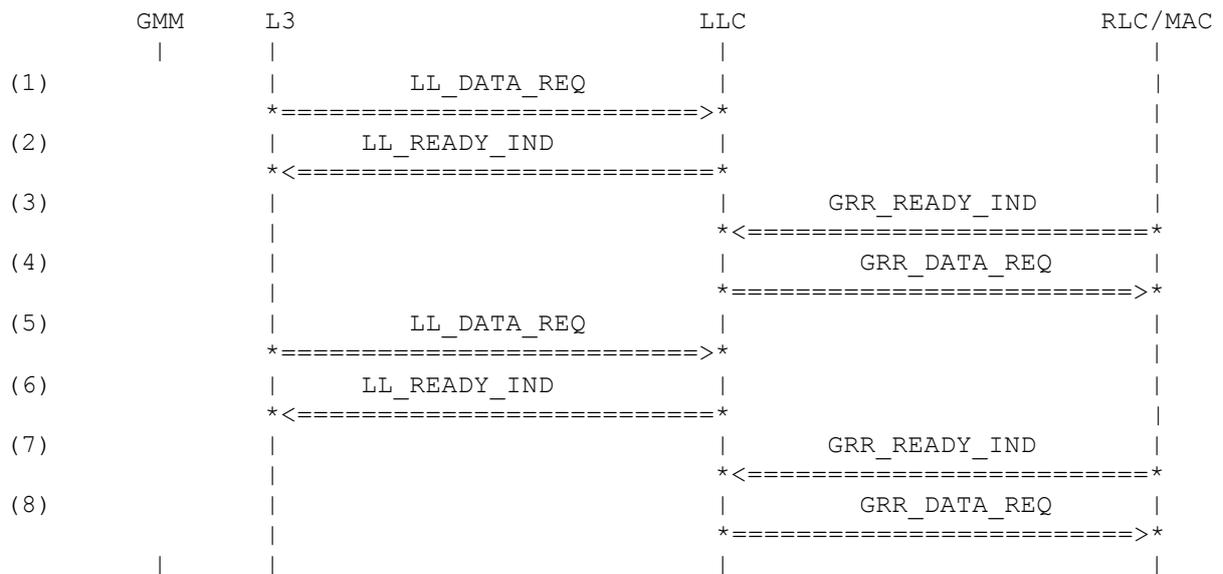
3.15.4 LLC703: ABM - L3 data request on SAPI 11 forwarded to GRR after ready indication

Description:

LLC receives data (100 octets) for acknowledged transmission. RLC/MAC is ready to receive data. LLC sends the received data in an I frame to RLC/MAC using the primitive GRR_DATA_REQ. This test is only applicable to SAPI11

Preamble:

LLC400D



Parametrization:

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
sdu	LLREQ_SDU100	
(2) LL_READY_IND	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS0_NR0_SAPI11
(5) LL_DATA_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED

	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(6) LL_READY_IND		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(7) GRR_READY_IND		
(8) GRR_DATA_REQ		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS1_NR0_SAPI11

History:

01-Sep-2000	SLM	Initial
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3.15.5 LLC705: ABM - Several L3 data request on SAPI 9 not forwarded to GRR after all are received

Description:

LLC receives several data (100 octets) for acknowledged transmission. RLC/MAC is not ready to receive data. LLC stores the frames. This test is only applicable to SAPI9

Preamble:

LLC400C

GMM	L3	LLC	RLC/MAC
(1)	LL_DATA_REQ		
	=====>		
(2)	LL_READY_IND		
	<=====		
(3)	LL_DATA_REQ		
	=====>		
(4)	LL_READY_IND		
	<=====		
(5)	LL_DATA_REQ		
	=====>		
(6)	LL_READY_IND		
	<=====		
(7)	LL_DATA_REQ		
	=====>		
(8)	LL_READY_IND		
	<=====		
(9)		GRR_READY_IND	
		<=====	
(10)		GRR_DATA_REQ	
		=====>	
(11)		GRR_READY_IND	
		<=====	
(12)		GRR_DATA_REQ	
		=====>	
(13)		GRR_READY_IND	
		<=====	
(14)		GRR_DATA_REQ	
		=====>	
(15)		GRR_READY_IND	
		<=====	
(16)		GRR_DATA_REQ	
		=====>	

Parametrization:

Primitive	Parameter	Value	
(1) LL_DATA_REQ	sapi	LL_SAPI_9	
	tlli	TLLI_LOCAL_1	
	ll_qos	LLREQ_QOS_DEL4_REL5	
	radio_prio	LL_RADIO_PRIO_3	
	reserved_data_req1	NOT_USED	
	reference1	NOT_USED	
	seg_pos	NOT_USED	
	attached_counter	LLC_NO_ATTACHE	
	reserved_data_req4	NOT_USED	
	reserved_data_req5	NOT_USED	
	sdu	LLREQ_SDU100	
	(2) LL_READY_IND	sapi	LL_SAPI_9
		tlli	TLLI_LOCAL_1
(3) LL_DATA_REQ	sapi	LL_SAPI_9	

	tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(4) LL_READY_IND		
	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
(5) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(6) LL_READY_IND		
	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
(7) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(8) LL_READY_IND		
	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
(9) GRR_READY_IND		
(10) GRR_DATA_REQ		
	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS0_NR0_SAPI9
(11) GRR_READY_IND		

(12) GRR_DATA_REQ

sapi	LL_SAPI_9
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_NS1_NR0_A0_SAPI9

(13) GRR_READY_IND

(14) GRR_DATA_REQ

sapi	LL_SAPI_9
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_NS2_NR0_A0_SAPI9

(15) GRR_READY_IND

(16) GRR_DATA_REQ

sapi	LL_SAPI_9
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_NS3_NR0_SAPI9

History:

01-Sep-2000 SLM Initial

3.15.6 LLC710: ABM - First Timeout of T201 after sending maximum window of data requests to GRR

Description:

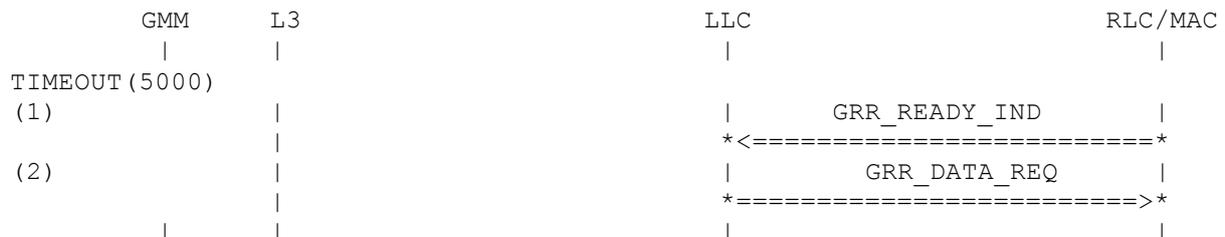
In LL700..703 test cases specific numbers of frames are sent in acknowledged mode. In this test case the behaviour of LLC which doesn't get acknowledgment is tested. After timeout LLC has to resent the last frame.

Variants:

<A>....<D>

Preamble:

<A>LLC700
 LLC701
 <C>LLC702
 <D>LLC703



Parametrization:

Primitive	Parameter	Value
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(1) GRR_READY_IND

(2) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	GRR_DATAREQ_NS15_NR0_SAPI3
	sdu	GRR_DATAREQ_NS7_NR0_SAPI5
<C>	sdu	GRR_DATAREQ_NS3_NR0_SAPI9
<D>	sdu	GRR_DATAREQ_NS1_NR0_SAPI11

History:

04-Sep-2000 SLM Initial

3.15.7 LLC711: ABM - Retransmission of 2 frames after receive of a SACK

Description:

LLC receives a SACK command which identifies the first frame and one additional that has been received wrong. The illegal frames must be retransmitted.

Variants:

<A>....<C>

Preamble:

<A>LLC700
 LLC701
 <C>LLC702

	GMM	L3	LLC	RLC/MAC
(1)			GRR_DATA_IND_TEST	
			<=====	
(2)			GRR_READY_IND	
			<=====	
(3)			GRR_DATA_REQ	
			=====>*	
(4)			GRR_READY_IND	
			<=====	
(5)			GRR_DATA_REQ	
			=====>*	

Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
<A>	sdu	GRRIND_SACK_R8_SAPI3
	sdu	GRRIND_SACK_R6_SAPI5
<C>	sdu	GRRIND_SACK_R2_SAPI9
(2) GRR_READY_IND		

(3) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	GRR_DATAREQ_NS0_NR0_A0_SAPI3
	sdu	GRR_DATAREQ_NS0_NR0_A0_SAPI5
<C>	sdu	GRR_DATAREQ_NS0_NR0_A0_SAPI9

(4) GRR_READY_IND

(5) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	GRR_DATAREQ_NS8_NR0_A1_SAPI3
	sdu	GRR_DATAREQ_NS6_NR0_A1_SAPI5
<C>	sdu	GRR_DATAREQ_NS2_NR0_A1_SAPI9

History:

05-Sep-2000 SLM Initial

3.15.8 LLC712: ABM - Retransmission of 3 frames after receive of a SACK; L3 data request while window full

Description:

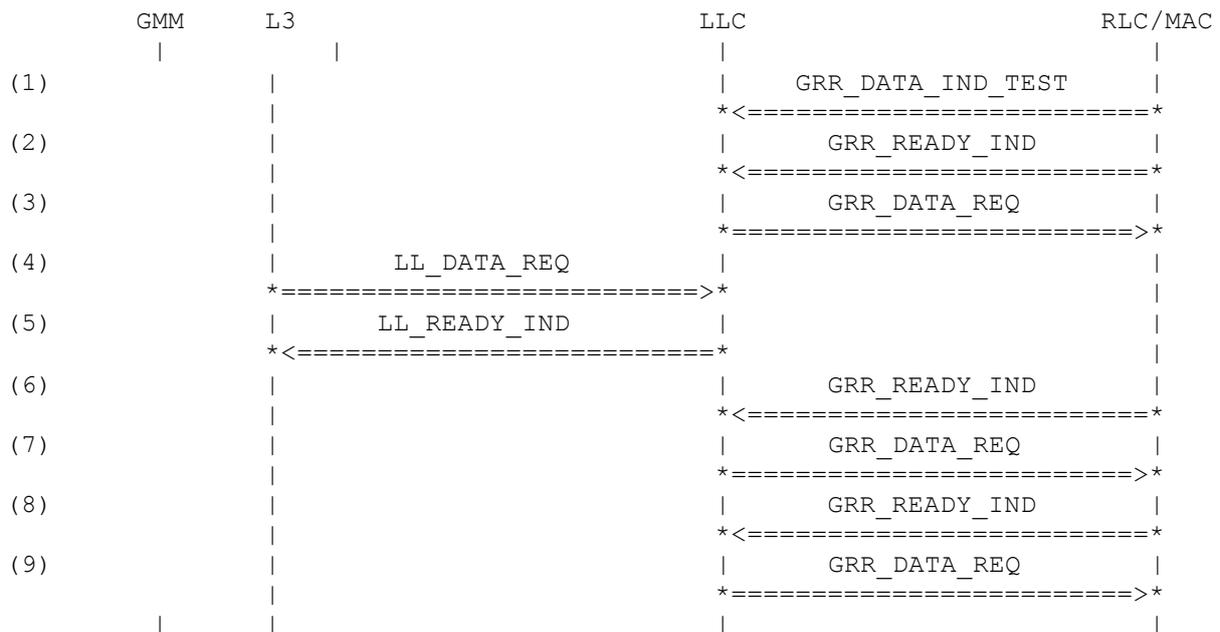
LLC receives a SACK command which identifies the frames that has been received wrong. The frames are retransmitted and the next new L3 frame will be send. A L3 data request is queued and not forwarded to GRR, because the transmit window is full.

Variants:

<A>....<C>

Preamble:

<A>LLC700
 LLC701
 <C>LLC702



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	GRRIND_SACK_R4_R8_SAPI3
<C>	sdu	GRRIND_SACK_R2_R6_SAPI5
	sdu	GRRIND_SACK_R1_R2_SAPI9
(2) GRR_READY_IND		
(3) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	GRR_DATAREQ_NS0_NR0_A0_SAPI3
	sdu	GRR_DATAREQ_NS0_NR0_A0_SAPI5
<C>	sdu	GRR_DATAREQ_NS0_NR0_A0_SAPI9
(4) LL_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED

	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(5) LL_READY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(6) GRR_READY_IND		
(7) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	GRR_DATAREQ_NS4_NR0_A0_SAPI3
	sdu	GRR_DATAREQ_NS2_NR0_A0_SAPI5
<C>	sdu	GRR_DATAREQ_NS1_NR0_A0_SAPI9
(8) GRR_READY_IND		
(9) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	GRR_DATAREQ_NS8_NR0_A1_SAPI3
	sdu	GRR_DATAREQ_NS6_NR0_A1_SAPI5
<C>	sdu	GRR_DATAREQ_NS2_NR0_A1_SAPI9

History:

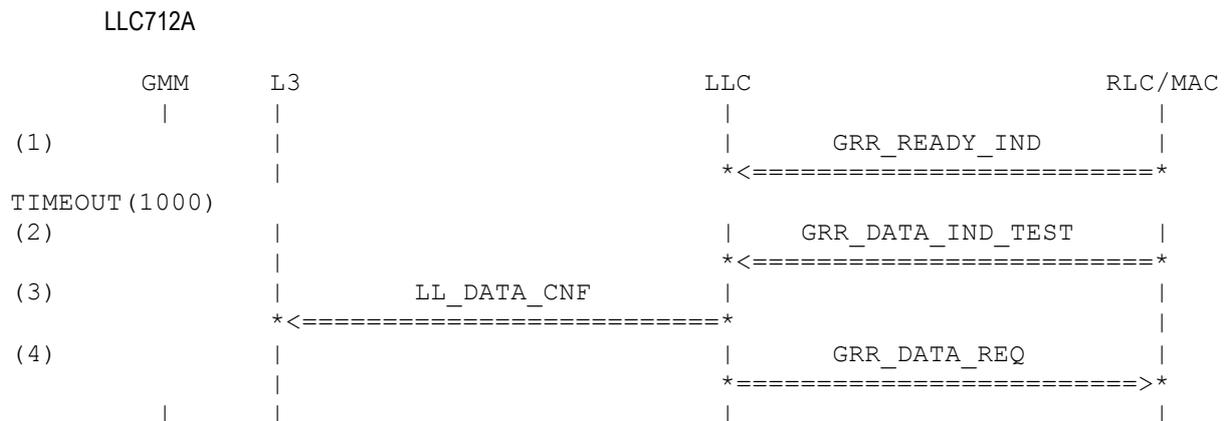
05-Sep-2000 SLM Initial

3.15.9 LLC713: ABM - Receiving an frame ack with RR; Sending data to GRR after transmit window full

Description:

LLC receives an GRR ready indication, but will not send any new frames in state "transmit window full". Then LLC receives a RR command for SAPI 3 which confirms the first transmitted frame. The L3 confirmation for all at this time in sequence acknowledged frames are send. The next following L3 PDU is send to GRR including an acknowledgement request.

Preamble:



Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_RR_NR2_A0_SAPI3
(3) LL_DATA_CNF	sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS16_NR0_A1_SAPI3

History:

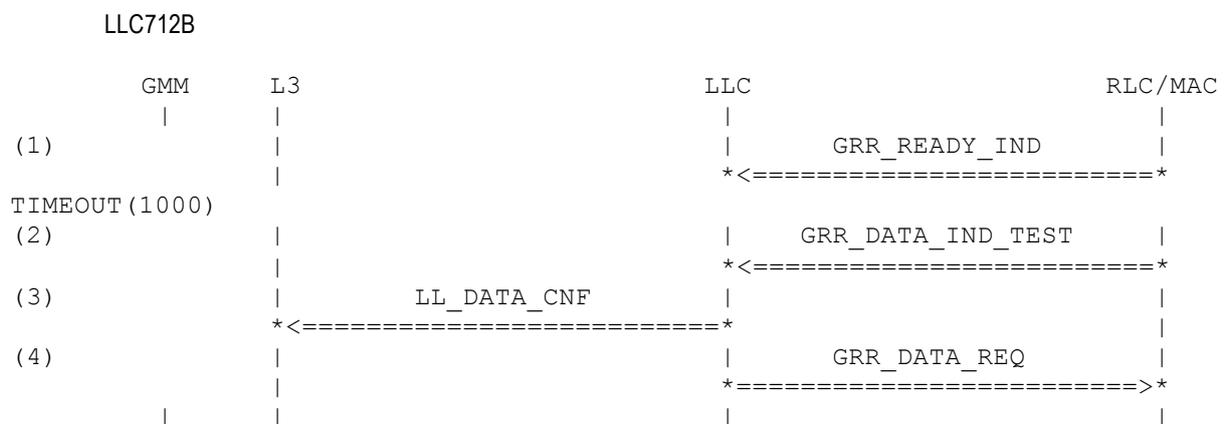
03-Nov-2000	GS	Initial
08-Dec-2000	GS	LL_DATA_CNFs joined together to one

3.15.10 LLC714: ABM - Receiving an frame ack with RR; Sending data to GRR after transmit window full

Description:

LLC receives an GRR ready indication, but will not send any new frames in state "transmit window full". Then LLC receives a RR command for SAPI 5 which confirms the first transmitted frame. The L3 confirmation for all at this time in sequence acknowledged frames are send. The next following L3 PDU is send to GRR including an acknowledgement request.

Preamble:



Parametrization:

<u>Primitive</u>	<u>Parameter</u>	<u>Value</u>
(1) GRR_READY_IND		
(2) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_RR_NR2_A0_SAPI5
(3) LL_DATA_CNF	sapi tlli reference1	LL_SAPI_5 TLLI_LOCAL_1 NOT_USED
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_5 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS8_NR0_A1_SAPI5

History:

03-Nov-2000	GS	Initial
08-Dec-2000	GS	LL_DATA_CNFs joined together to one

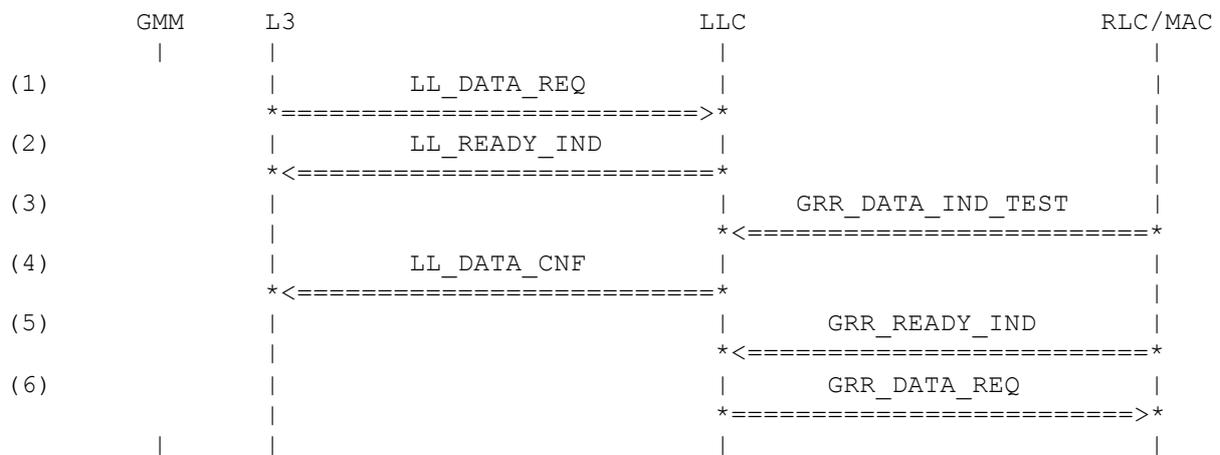
3.15.11 LLC715: ABM - Receiving frame ack with RR for SAPI 9; Sending new frames to GRR

Description:

LLC receives an RR frame that identifies that all frames previously sent have been acknowledged later, it sends frame with N(R)=1.

Preamble:

LLC702



Parametrization:

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LL_READY_IND	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
(3) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	GRRIND_RR_NR4_SAPI9
(4) LL_DATA_CNF	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	reference1	NOT_USED

(5) GRR_READY_IND

(6) GRR_DATA_REQ

sapi	LL_SAPI_9
tli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_NS4_NR0_A1_SAPI9

History:

05-Sep-2000	SLM	Initial
08-Dec-2000	GS	LL_DATA_CNFs joined together to one

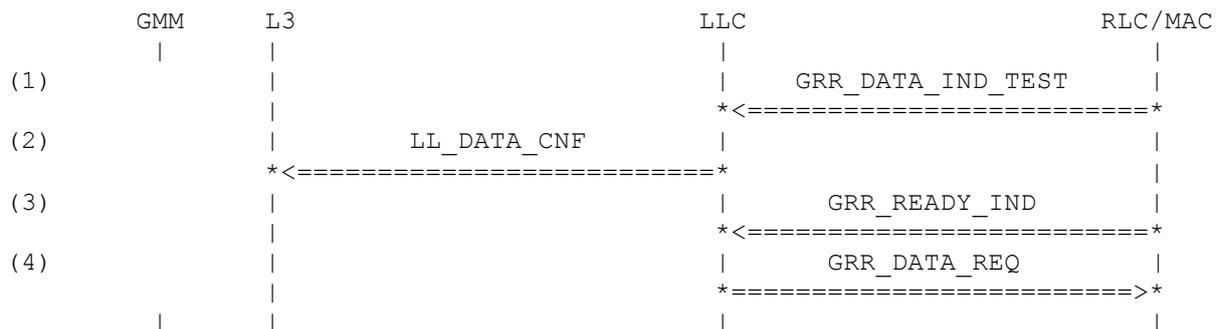
3.15.12 LLC716: ABM - Receiving an ACK on SAPI 3; Retransmit erroneous frame

Description:

In test case LLC700 16 frames with N(S) = 0..15 are send to the lower layer. An ACK command comes with N(R) set to 9. The L3 data confirmation is send and the erroneous frame is retransmitted.

Preamble:

LLC700



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	GRRIND_ACK_NR9_SAPI3
(2) LL_DATA_CNF	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	reference1	NOT_USED
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS9_NR0_SAPI3

History:

05-Nov-2000	GS	Initial
08-Dec-2000	GS	LL_DATA_CNFs joined together to one

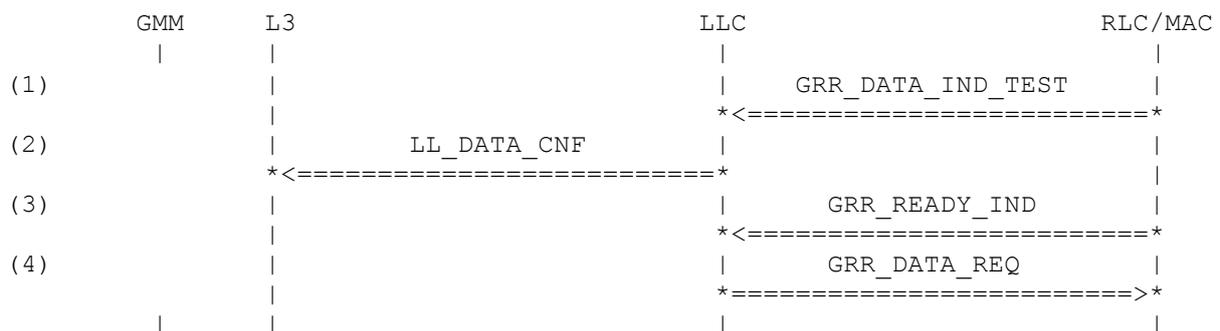
3.15.13 LLC717: ABM - Receiving an ACK on SAPI 5; Retransmit erroneous frame

Description:

In test case LLC701 eight frames with N(S) = 0..7 are send to the lower layer. An ACK command comes with N(R) set to 3. The L3 data confirmation is send and the erroneous frame is retransmitted.

Preamble:

LLC701

**Parametrization:**

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	GRRIND_ACK_NR3_SAPI5
(2) LL_DATA_CNF	sapi	LL_SAPI_5
	tli	TLLI_LOCAL_1
	reference1	NOT_USED
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_5
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS3_NR0_SAPI5

History:

05-Sep-2000	SLM	Initial
08-Dec-2000	GS	LL_DATA_CNFs joined together to one

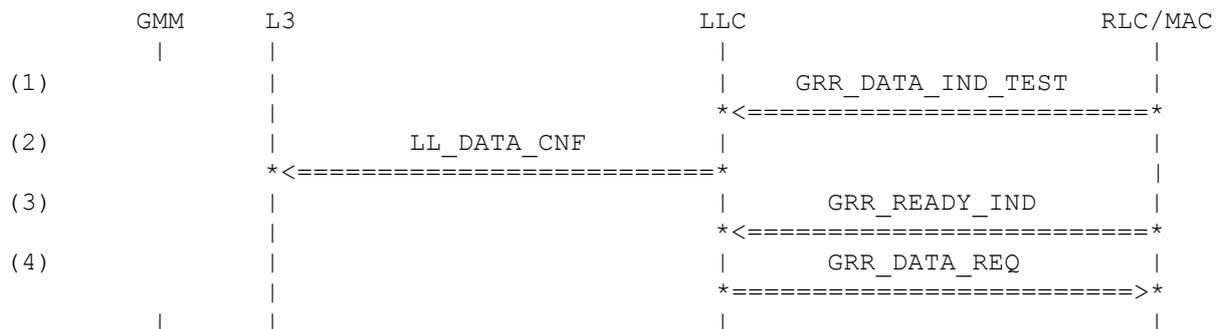
3.15.14 LLC718: ABM - Receiving an ACK on SAPI 9; Retransmit erroneous frame

Description:

In test case LLC702 four frames with N(S) = 0..3 are send to the lower layer. An ACK command comes with N(R) set to 1. The L3 data confirmation is send and the erroneous frame is retransmitted.

Preamble:

LLC702

**Parametrization:**

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	GRRIND_ACK_NR1_SAPI9
(2) LL_DATA_CNF	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	reference1	NOT_USED
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS1_NR0_SAPI9

History:

05-Sep-2000 SLM Initial

3.15.15 LLC719: ABM - Receiving a RNR, 2 times expiry of T201 and restart of transmission after RR

Description:

LLC receives a RNR frame. The timer T201 expires two times and then LLC receive a RR. Then LLC should continue I frame transmission.

Preamble:

LLC703

	GMM	L3	LLC	RLC/MAC
(1)			GRR_DATA_IND_TEST	
			<=====	
(2)		LL_DATA_CNF		
		<=====		
TIMEOUT (5000)				
(3)			GRR_READY_IND	
			<=====	
(4)			GRR_DATA_REQ	
			=====>	
(5)			GRR_DATA_IND_TEST	
			<=====	
TIMEOUT (5000)				
(6)			GRR_READY_IND	
			<=====	
(7)			GRR_DATA_REQ	
			=====>	
(8)			GRR_DATA_IND_TEST	
			<=====	
(9)		LL_DATA_REQ		
		=====>		
(10)		LL_READY_IND		
		<=====		
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	GRRIND_RNR_NR2_SAPI11
(2) LL_DATA_CNF	sapi	LL_SAPI_11
	tli	TLLI_LOCAL_1
	reference1	NOT_USED
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_11

	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRRIND_RR_NR0_A1_SAPI11
(5) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_RNR_NR2_SAPI11
(6) GRR_READY_IND		
(7) GRR_DATA_REQ		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRRIND_RR_NR0_A1_SAPI11
(8) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_RR_NR2_SAPI11
(9) LL_DATA_REQ		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(10) LL_READY_IND		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS2_NR0_A1_SAPI11

History:

07-Sep-2000	SLM	Initial
08-Dec-2000	GS	LL_DATA_CNFs joined together to one

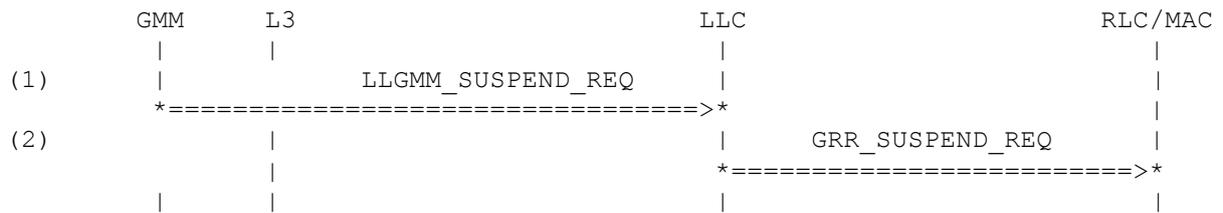
3.15.16 LLC720: ABM - Suspend Request after ABM Establishment

Description:

LLC is just established. GMM suspends operation.

Preamble:

LLC400C



Parametrization:

Primitive	Parameter	Value
(1) LLGMM_SUSPEND_REQ	susp_cause	LLGMM_RAU
(2) GRR_SUSPEND_REQ		

History:

07-Nov-2000	GS	Initial
18-Oct-2001	GS	Suspension cause added
30-Jan-2002	GS	GRR_SUSPEND_REQ added.

3.15.17 LLC721: ABM - Suspended; L3 data requests received until Transmit Buffer full

Description:

LLC receives 10 frames each with 500 octet PDUs for acknowledged transmission. RLC/MAC is not ready. The Transmitbuffer is full with the last request. No ready indication is send until the operation is resumed and the first frame is acknowledged from the peer.

Preamble:

LLC720

	GMM	L3	LLC	RLC/MAC
(1)		LL_DATA_REQ		
		=====>		
(2)		LL_READY_IND		
		<=====		
(3)		LL_DATA_REQ		
		=====>		
(4)		LL_READY_IND		
		<=====		
(5)		LL_DATA_REQ		
		=====>		
(6)		LL_READY_IND		
		<=====		
(7)		LL_DATA_REQ		
		=====>		
(8)		LL_READY_IND		
		<=====		
(9)		LL_DATA_REQ		
		=====>		
(10)		LL_READY_IND		
		<=====		
(11)		LL_DATA_REQ		
		=====>		
(12)		LL_READY_IND		
		<=====		
(13)		LL_DATA_REQ		
		=====>		
(14)		LL_READY_IND		
		<=====		
(15)		LL_DATA_REQ		
		=====>		
(16)		LL_READY_IND		
		<=====		
(17)		LL_DATA_REQ		
		=====>		
(18)		LL_READY_IND		
		<=====		
(19)		LL_DATA_REQ		
		=====>		

Parametrization:

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500

(2) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(3) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_9 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU500
(4) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(5) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_9 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU500
(6) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(7) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_9 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU500
(8) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(9) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1	LL_SAPI_9 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED

	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(10) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(11) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(12) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(13) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(14) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(15) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(16) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1

(17) LL_DATA_REQ

sapi	LL_SAPI_9
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL5
radio_prio	LL_RADIO_PRIO_3
reserved_data_req1	NOT_USED
reference1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE
reserved_data_req4	NOT_USED
reserved_data_req5	NOT_USED
sdu	LLREQ_SDU500

(18) LL_READY_IND

sapi	LL_SAPI_9
tlli	TLLI_LOCAL_1

(19) LL_DATA_REQ

sapi	LL_SAPI_9
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL5
radio_prio	LL_RADIO_PRIO_3
reserved_data_req1	NOT_USED
reference1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE
reserved_data_req4	NOT_USED
reserved_data_req5	NOT_USED
sdu	LLREQ_SDU500

History:

08-Sep-2000	SLM	Initial
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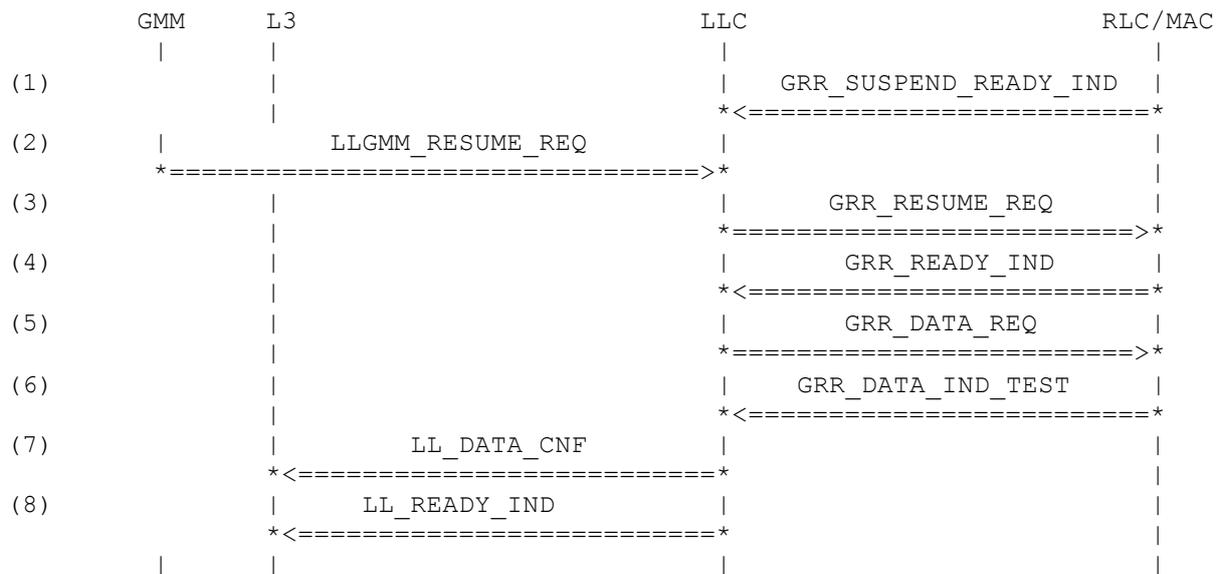
3.15.18 LLC722: ABM - Resume of operation, acknowledge of first frame, send of L3 ready indication

Description:

LLC receives a Resume request from GMM and starts transmission on frames to the lower layer. After the first frame is acknowledged from the peer, LLC sends a LL_READY_IND.

Preamble:

LLC721



Parametrization:

Primitive	Parameter	Value
(1) GRR_SUSPEND_READY_IND		
(2) LLGMM_RESUME_REQ		
(3) GRR_RESUME_REQ		
(4) GRR_READY_IND		
(5) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS0_NR0_500_SAPI9
(6) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_RR_NR1_A0_SAPI9
(7) LL_DATA_CNF	sapi tlli reference1	LL_SAPI_9 TLLI_LOCAL_1 NOT_USED
(8) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1

History:

06-Nov-2000	GS	Initial
31-Jan-2002	GS	GRR suspend/resume handling added

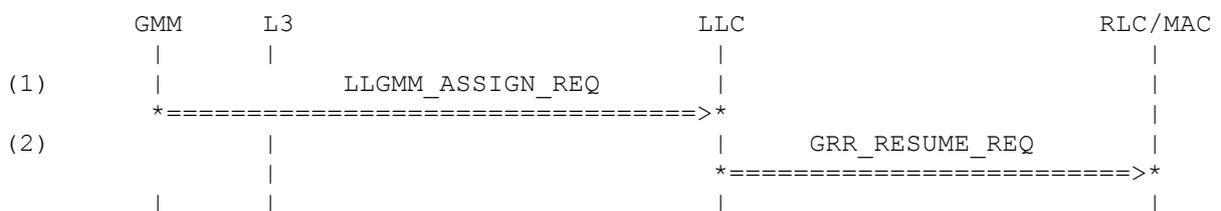
3.15.19 LLC724: ABM - Suspended; GMM unassigns LLC

Description:

LLC is in state suspended and receives from GMM a request to unassign itself. LLC sends a resume request to GRR to indicate resume of normal LLC operation.

Preamble:

LLC720



Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	TLLI_LOCAL_1
	new_tlli	LLGMM_TLLI_INVALID
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) GRR_RESUME_REQ		

History:

01-Feb-2002	DB	Initial
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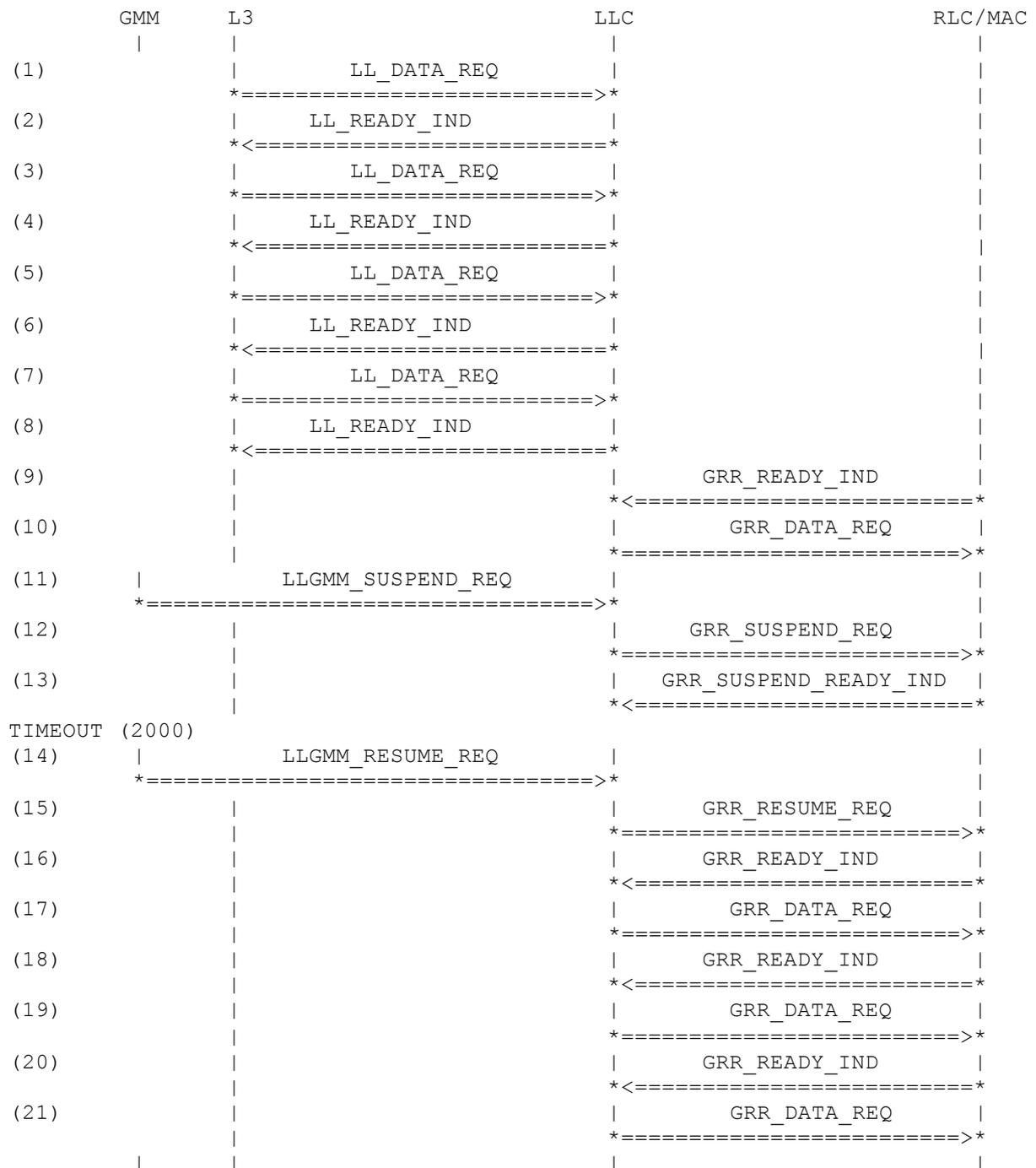
3.15.20 LLC725: ABM - Suspend and resume of L3 PDU transmission

Description:

LLC receives several data requests for acknowledged transmission. RLC/MAC is not ready to receive data. LLC stores the frames and forwards them later with each ready indication from GRR. After a suspend request was received from GMM, forwarding of frames is stopped until the corresponding resume request was received.

Preamble:

LLC400C



Parametrization:

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED

	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(3) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(4) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(5) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(6) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(7) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(8) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(9) GRR_READY_IND		

(10) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS0_NR0_SAPI9
(11) LLGMM_SUSPEND_REQ	susp_cause	LLGMM_RAU
(12) GRR_SUSPEND_REQ		
(13) GRR_SUSPEND_READY_IND		
(14) LLGMM_RESUME_REQ		
(15) GRR_RESUME_REQ		
(16) GRR_READY_IND		
(17) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS1_NR0_A0_SAPI9
(18) GRR_READY_IND		
(19) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS2_NR0_A0_SAPI9
(20) GRR_READY_IND		
(21) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED GRR_DATAREQ_NS3_NR0_SAPI9

History:

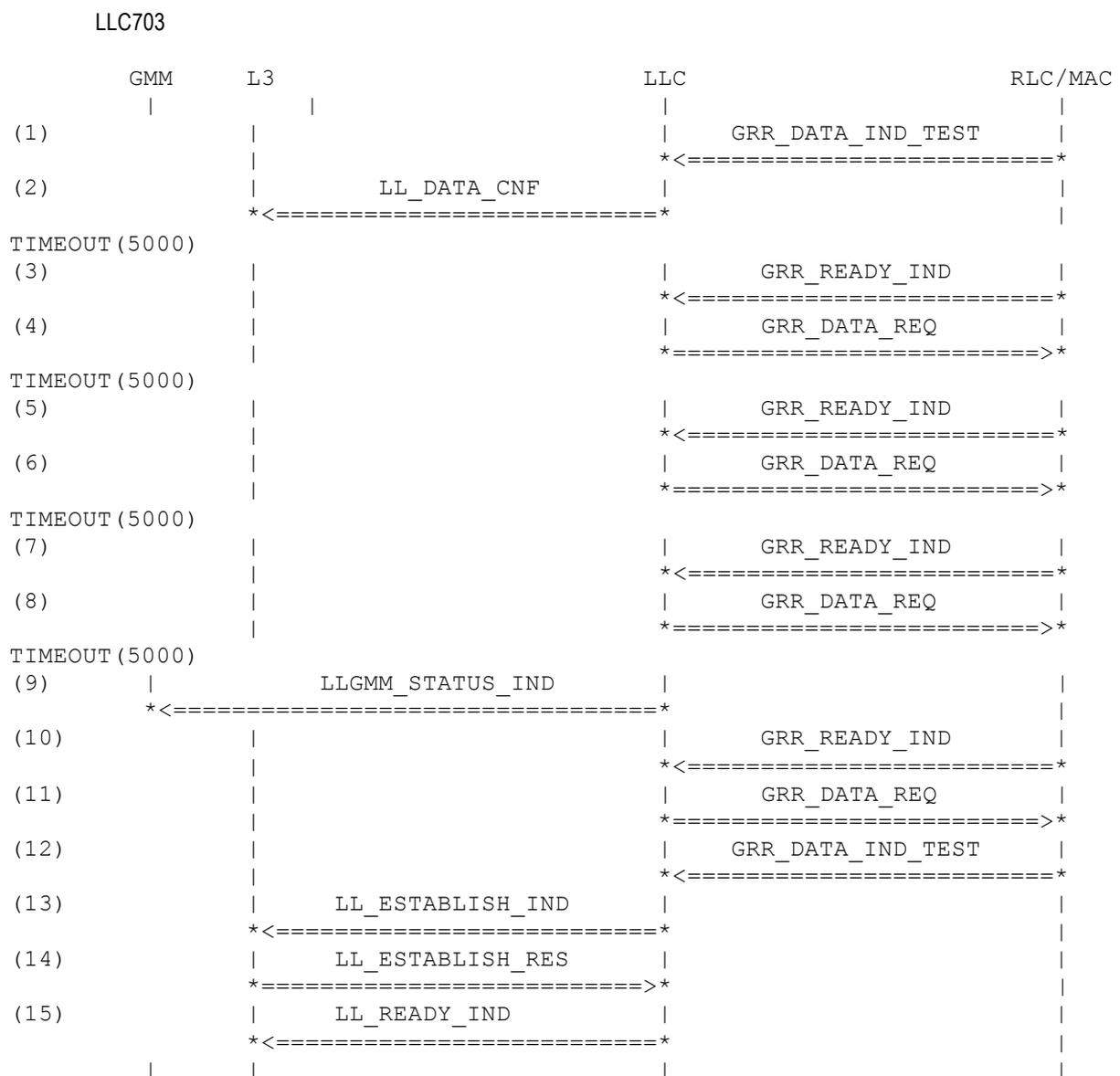
06-Nov-2000	GS	Initial
18-Oct-2001	GS	Suspension cause added
30-Jan-2002	GS	GRR suspend/resume handling added.

3.15.21 LLC730: ABM - T201 expires N200 times in state Peer Busy; ABM operation reestablished

Description:

LLC receives An RNR frame. T201 expires N200 times. Finally LLC initiate an re-establishment of the connection and sends an SABM command. An status indication including the re-establish cause is send to GMM. After receiving the UA the re-establish is indicated to L3. After receiving the response LLC is back in ABM.

Preamble:



Parametrization:

Primitive	Parameter	Value
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(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_RNR_NR2_SAPI11
(2) LL_DATA_CNF	sapi tlli reference1	LL_SAPI_11 TLLI_LOCAL_1 NOT_USED
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_11 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR0_A1_SAPI11
(5) GRR_READY_IND		
(6) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_11 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR0_A1_SAPI11
(7) GRR_READY_IND		
(8) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_11 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR0_A1_SAPI11
(9) LLGMM_STATUS_IND	error_cause LLGMM_ERRCS_PEER_BUSY_REEST	
(10) GRR_READY_IND		
(11) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_11 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_SABM_NOXID_SAPI11
(12) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_NOXID_SAPI11
(13) LL_ESTABLISH_IND	sapi tlli	LL_SAPI_11 TLLI_LOCAL_1

	n201_u	N201_U_DEF_SAPI11
	n201_i	N201_I_DEF
	xid_valid	LL_XID_INVALID
	sdu	EMPTY_SDU
(14) LL_ESTABLISH_RES		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	xid_valid	LL_XID_INVALID
	sdu	EMPTY_SDU
(15) LL_READY_IND		
	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

History:

07-Sep-2000	SLM	Initial
08-Dec-2000	GS	LL_DATA_CNFs joined together to one
27-Jun-2001	GS	Behaviour changed.
25-Jul-2001	GS	Status indication and completion of reestablish added.
05-Dec-2001	GS	Flag xid_valid added to LL_ESTABLISH_RES

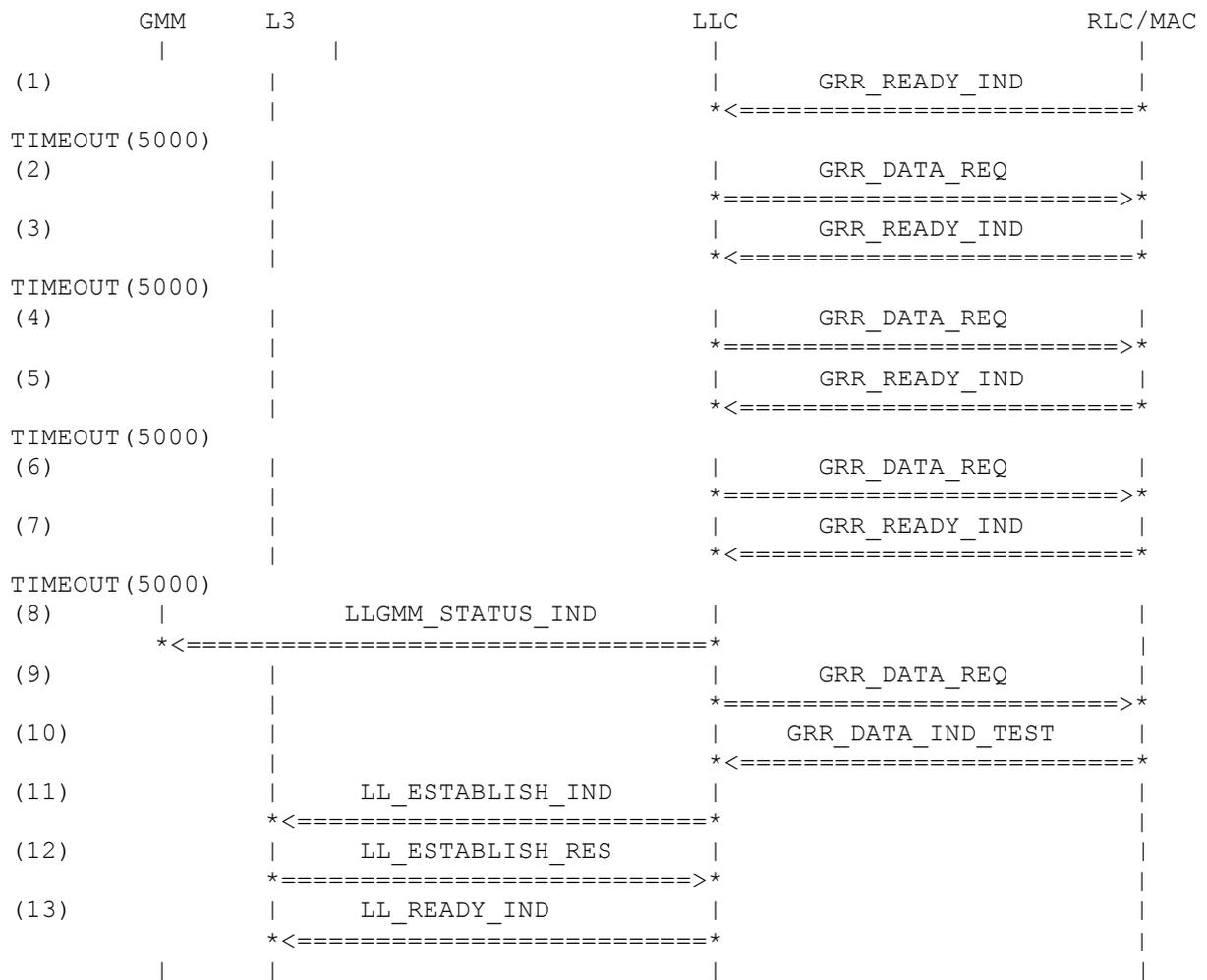
3.15.22 LLC731: ABM - T201 expires N200 times while waiting for an acknowledge; ABM operation reestablished

Description:

T201 expires N200 times while LLC was waiting for an frame acknowledge. Finally LLC initiate an re-establishment of the connection and sends an SABM command. An status indication including the re-establish cause is send to GMM. After receiving the UA the re-establish is initiated to L3. After receiving the response LLC is back in ABM.

Preamble:

LLC703



Parametrization:

<u>Primitive</u>	<u>Parameter</u>	<u>Value</u>
(1) GRR_READY_IND		
(2) GRR_DATA_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS1_NR0_SAPI11
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS1_NR0_SAPI11
(5) GRR_READY_IND		

(6) GRR_DATA_REQ

sapi	LL_SAPI_11
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEQSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_NS1_NR0_SAPI11

(7) GRR_READY_IND

(8) LLGMM_STATUS_IND

error_cause	LLGMM_ERRCS_ACK_NO_PEER_RES_REEST
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(9) GRR_DATA_REQ

sapi	LL_SAPI_11
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEQSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	MS_SABM_NOXID_SAPI11

(10) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	SGSN_UA1_NOXID_SAPI11

(11) LL_ESTABLISH_IND

sapi	LL_SAPI_11
tlli	TLLI_LOCAL_1
n201_u	N201_U_DEF_SAPI11
n201_i	N201_I_DEF
xid_valid	LL_XID_INVALID
sdu	EMPTY_SDU

(12) LL_ESTABLISH_RES

sapi	LL_SAPI_11
tlli	TLLI_LOCAL_1
xid_valid	LL_XID_INVALID
sdu	EMPTY_SDU

(13) LL_READY_IND

sapi	LL_SAPI_11
tlli	TLLI_LOCAL_1

History:

25-Jul-2001	GS	Initial.
05-Dec-2001	GS	Flag xid_valid added to LL_ESTABLISH_RES

3.15.23 LLC740: ABM - LLC sends 31 I-Frames per Variant, A-Bit set, get an RR for each

Description:

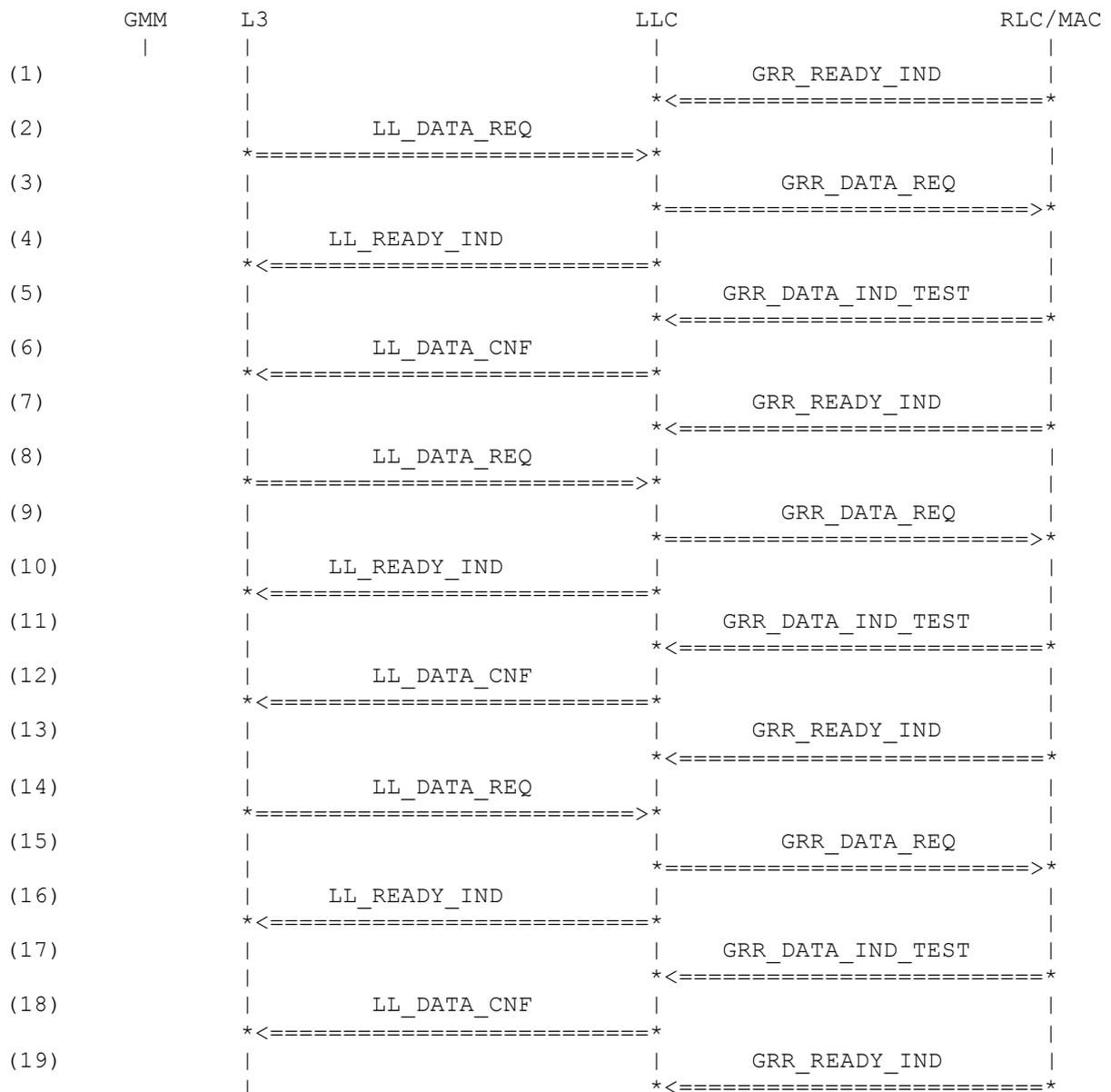
LLC receives between 31 (variant A) to 496 (variant P) L3 SDUs and sends them to the peer entity including in I-frames with the A-Bit set to 1. The Tests starts with N(S)=0. For each I-frame an RR is received from the peer entity.

Variants:

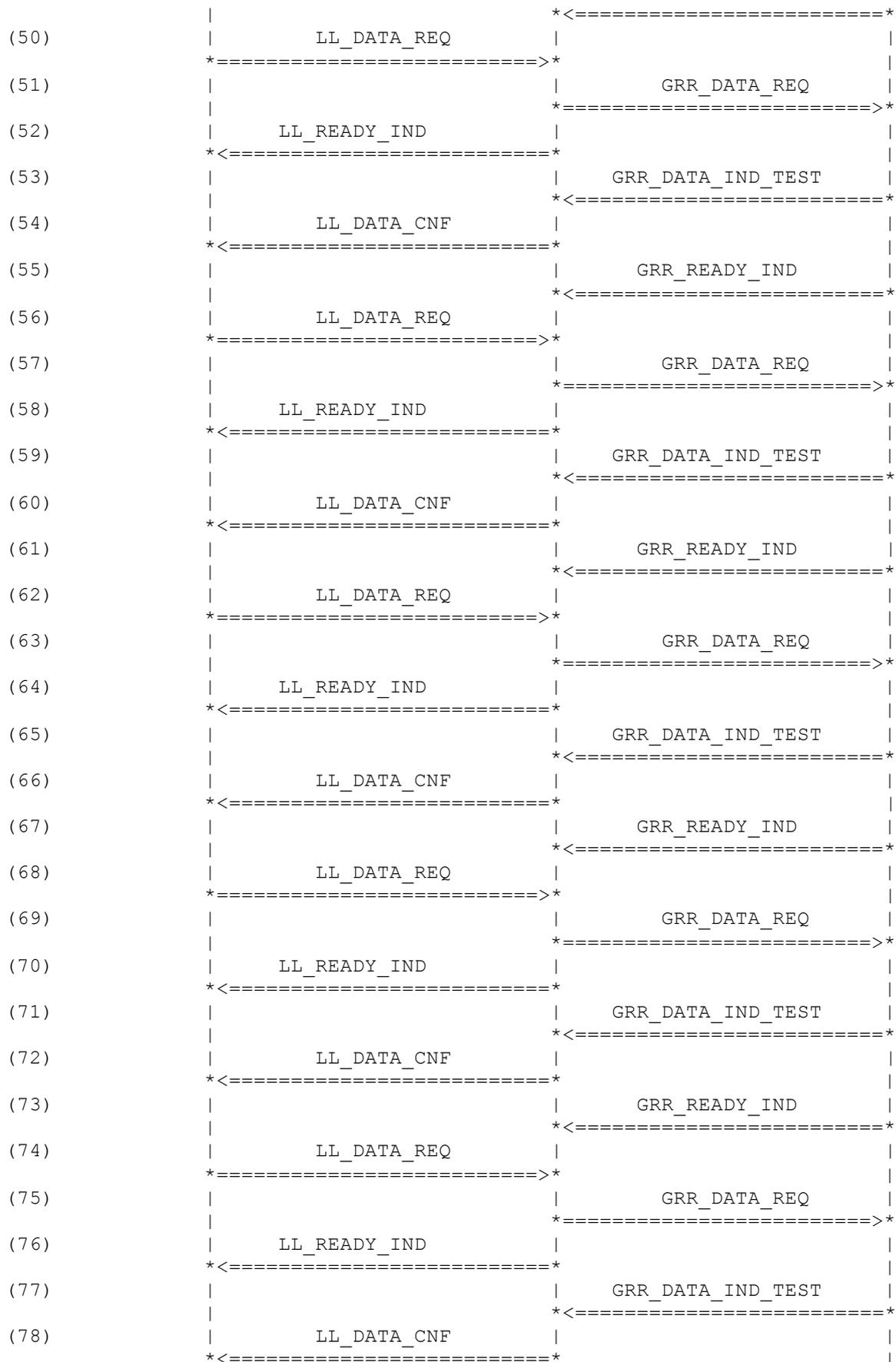
<A>....<P>

Preamble:

- <A>LLC400A
- LLC740A
- <C>LLC740B
- <D>LLC740C
- <E>LLC740D
- <F>LLC740E
- <G>LLC740F
- <H>LLC740G
- <I>LLC740H
- <J>LLC740I
- <K>LLC740J
- <L>LLC740K
- <M>LLC740L
- <N>LLC740M
- <O>LLC740N
- <P>LLC740O

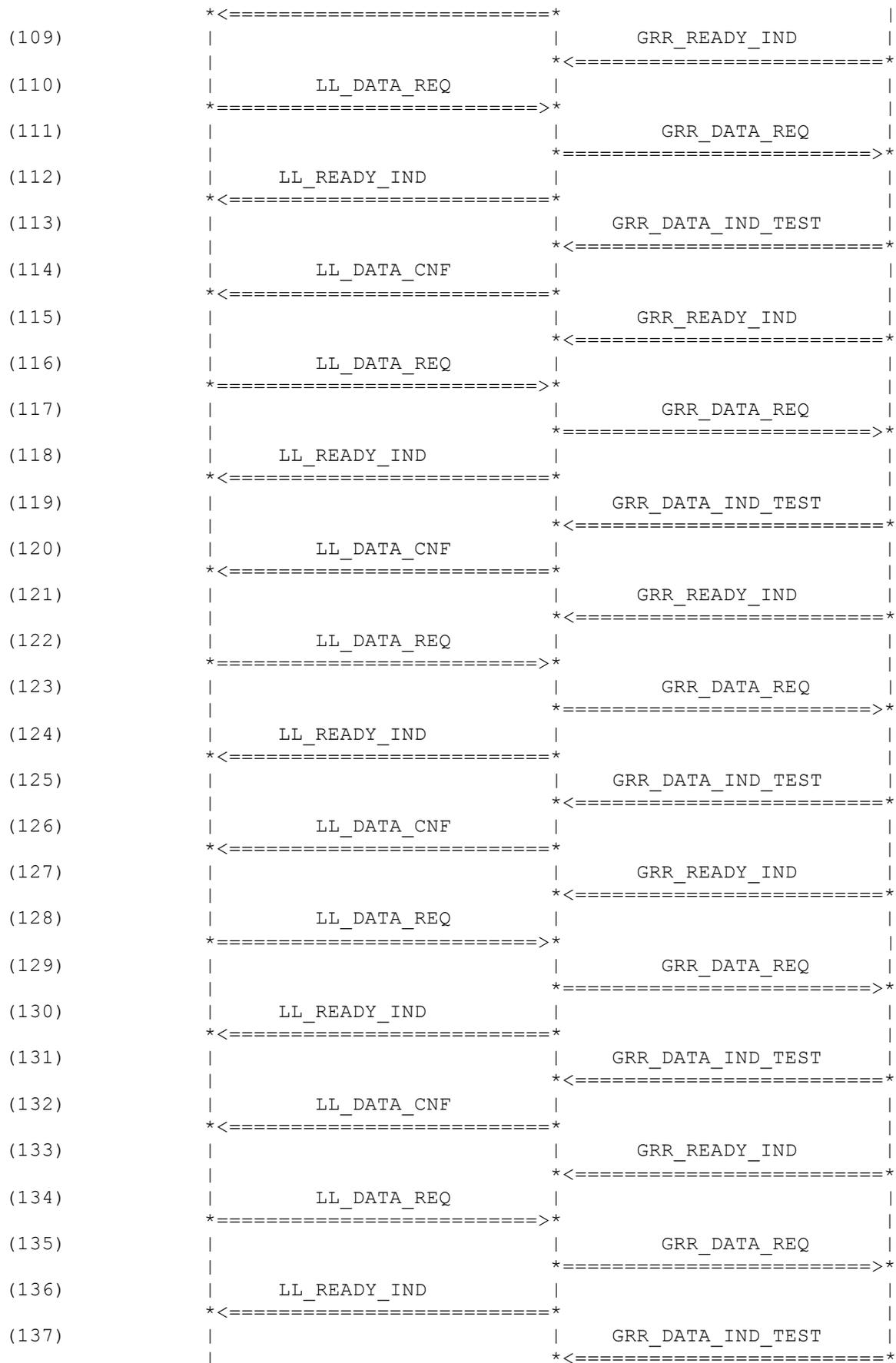


(20)		LL_DATA_REQ		
		=====	>	
(21)				GRR_DATA_REQ
				*=====
(22)		LL_READY_IND		
		*<=====		
(23)				GRR_DATA_IND_TEST
				*=====
(24)		LL_DATA_CNF		
		*<=====		
(25)				GRR_READY_IND
				*=====
(26)		LL_DATA_REQ		
		=====	>	
(27)				GRR_DATA_REQ
				*=====
(28)		LL_READY_IND		
		*<=====		
(29)				GRR_DATA_IND_TEST
				*=====
(30)		LL_DATA_CNF		
		*<=====		
(31)				GRR_READY_IND
				*=====
(32)		LL_DATA_REQ		
		=====	>	
(33)				GRR_DATA_REQ
				*=====
(34)		LL_READY_IND		
		*<=====		
(35)				GRR_DATA_IND_TEST
				*=====
(36)		LL_DATA_CNF		
		*<=====		
(37)				GRR_READY_IND
				*=====
(38)		LL_DATA_REQ		
		=====	>	
(39)				GRR_DATA_REQ
				*=====
(40)		LL_READY_IND		
		*<=====		
(41)				GRR_DATA_IND_TEST
				*=====
(42)		LL_DATA_CNF		
		*<=====		
(43)				GRR_READY_IND
				*=====
(44)		LL_DATA_REQ		
		=====	>	
(45)				GRR_DATA_REQ
				*=====
(46)		LL_READY_IND		
		*<=====		
(47)				GRR_DATA_IND_TEST
				*=====
(48)		LL_DATA_CNF		
		*<=====		
(49)				GRR_READY_IND



```

(79)      |                                     | GRR_READY_IND |
          |                                     | *<=====|
(80)      | LL_DATA_REQ                          |               |
          | *=====>*                          |               |
(81)      |                                     | GRR_DATA_REQ  |
          |                                     | *=====>*  |
(82)      | LL_READY_IND                         |               |
          | *<=====|
(83)      |                                     | GRR_DATA_IND_TEST |
          |                                     | *<=====|
(84)      | LL_DATA_CNF                          |               |
          | *<=====|
(85)      |                                     | GRR_READY_IND  |
          |                                     | *<=====|
(86)      | LL_DATA_REQ                          |               |
          | *=====>*                          |               |
(87)      |                                     | GRR_DATA_REQ  |
          |                                     | *=====>*  |
(88)      | LL_READY_IND                         |               |
          | *<=====|
(89)      |                                     | GRR_DATA_IND_TEST |
          |                                     | *<=====|
(90)      | LL_DATA_CNF                          |               |
          | *<=====|
(91)      |                                     | GRR_READY_IND  |
          |                                     | *<=====|
(92)      | LL_DATA_REQ                          |               |
          | *=====>*                          |               |
(93)      |                                     | GRR_DATA_REQ  |
          |                                     | *=====>*  |
(94)      | LL_READY_IND                         |               |
          | *<=====|
(95)      |                                     | GRR_DATA_IND_TEST |
          |                                     | *<=====|
(96)      | LL_DATA_CNF                          |               |
          | *<=====|
(97)      |                                     | GRR_READY_IND  |
          |                                     | *<=====|
(98)      | LL_DATA_REQ                          |               |
          | *=====>*                          |               |
(99)      |                                     | GRR_DATA_REQ  |
          |                                     | *=====>*  |
(100)     | LL_READY_IND                         |               |
          | *<=====|
(101)     |                                     | GRR_DATA_IND_TEST |
          |                                     | *<=====|
(102)     | LL_DATA_CNF                          |               |
          | *<=====|
(103)     |                                     | GRR_READY_IND  |
          |                                     | *<=====|
(104)     | LL_DATA_REQ                          |               |
          | *=====>*                          |               |
(105)     |                                     | GRR_DATA_REQ  |
          |                                     | *=====>*  |
(106)     | LL_READY_IND                         |               |
          | *<=====|
(107)     |                                     | GRR_DATA_IND_TEST |
          |                                     | *<=====|
(108)     | LL_DATA_CNF                          |               |
  
```



(138)		LL_DATA_CNF		
		*<=====		
(139)				GRR_READY_IND
				*<=====
(140)		LL_DATA_REQ		
		*=====		
(141)				GRR_DATA_REQ
				*=====
(142)		LL_READY_IND		
		*<=====		
(143)				GRR_DATA_IND_TEST
				*<=====
(144)		LL_DATA_CNF		
		*<=====		
(145)				GRR_READY_IND
				*<=====
(146)		LL_DATA_REQ		
		*=====		
(147)				GRR_DATA_REQ
				*=====
(148)		LL_READY_IND		
		*<=====		
(149)				GRR_DATA_IND_TEST
				*<=====
(150)		LL_DATA_CNF		
		*<=====		
(151)				GRR_READY_IND
				*<=====
(152)		LL_DATA_REQ		
		*=====		
(153)				GRR_DATA_REQ
				*=====
(154)		LL_READY_IND		
		*<=====		
(155)				GRR_DATA_IND_TEST
				*<=====
(156)		LL_DATA_CNF		
		*<=====		
(157)				GRR_READY_IND
				*<=====
(158)		LL_DATA_REQ		
		*=====		
(159)				GRR_DATA_REQ
				*=====
(160)		LL_READY_IND		
		*<=====		
(161)				GRR_DATA_IND_TEST
				*<=====
(162)		LL_DATA_CNF		
		*<=====		
(163)				GRR_READY_IND
				*<=====
(164)		LL_DATA_REQ		
		*=====		
(165)				GRR_DATA_REQ
				*=====
(166)		LL_READY_IND		
		*<=====		
(167)				GRR_DATA_IND_TEST

(3) GRR_DATA_REQ

	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS0_NR0_A1_S3
	sdu	RR_D3_NS31_NR0_A1_S3
<C>	sdu	RR_D3_NS62_NR0_A1_S3
<D>	sdu	RR_D3_NS93_NR0_A1_S3
<E>	sdu	RR_D3_NS124_NR0_A1_S3
<F>	sdu	RR_D3_NS155_NR0_A1_S3
<G>	sdu	RR_D3_NS186_NR0_A1_S3
<H>	sdu	RR_D3_NS217_NR0_A1_S3
<I>	sdu	RR_D3_NS248_NR0_A1_S3
<J>	sdu	RR_D3_NS279_NR0_A1_S3
<K>	sdu	RR_D3_NS310_NR0_A1_S3
<L>	sdu	RR_D3_NS341_NR0_A1_S3
<M>	sdu	RR_D3_NS372_NR0_A1_S3
<N>	sdu	RR_D3_NS403_NR0_A1_S3
<O>	sdu	RR_D3_NS434_NR0_A1_S3
<P>	sdu	RR_D3_NS465_NR0_A1_S3

(4) LL_READY_IND

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1

(5) GRR_DATA_IND_TEST

	tli	TLLI_LOCAL_1
<A>	sdu	RR_NR1_A0_S3
	sdu	RR_NR32_A0_S3
<C>	sdu	RR_NR63_A0_S3
<D>	sdu	RR_NR94_A0_S3
<E>	sdu	RR_NR125_A0_S3
<F>	sdu	RR_NR156_A0_S3
<G>	sdu	RR_NR187_A0_S3
<H>	sdu	RR_NR218_A0_S3
<I>	sdu	RR_NR249_A0_S3
<J>	sdu	RR_NR280_A0_S3
<K>	sdu	RR_NR311_A0_S3
<L>	sdu	RR_NR342_A0_S3
<M>	sdu	RR_NR373_A0_S3
<N>	sdu	RR_NR404_A0_S3
<O>	sdu	RR_NR435_A0_S3
<P>	sdu	RR_NR466_A0_S3

(6) LL_DATA_CNF

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
reference1	A_REF_0

(7) GRR_READY_IND

(8) LL_DATA_REQ

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL5
radio_prio	LL_RADIO_PRIO_3
reserved_data_req1	NOT_USED

	reference1	REF_1
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(9) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS1_NR0_A1_S3
	sdu	RR_D3_NS32_NR0_A1_S3
<C>	sdu	RR_D3_NS63_NR0_A1_S3
<D>	sdu	RR_D3_NS94_NR0_A1_S3
<E>	sdu	RR_D3_NS125_NR0_A1_S3
<F>	sdu	RR_D3_NS156_NR0_A1_S3
<G>	sdu	RR_D3_NS187_NR0_A1_S3
<H>	sdu	RR_D3_NS218_NR0_A1_S3
<I>	sdu	RR_D3_NS249_NR0_A1_S3
<J>	sdu	RR_D3_NS280_NR0_A1_S3
<K>	sdu	RR_D3_NS311_NR0_A1_S3
<L>	sdu	RR_D3_NS342_NR0_A1_S3
<M>	sdu	RR_D3_NS373_NR0_A1_S3
<N>	sdu	RR_D3_NS404_NR0_A1_S3
<O>	sdu	RR_D3_NS435_NR0_A1_S3
<P>	sdu	RR_D3_NS466_NR0_A1_S3
(10)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(11)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR2_A0_S3
	sdu	RR_NR33_A0_S3
<C>	sdu	RR_NR64_A0_S3
<D>	sdu	RR_NR95_A0_S3
<E>	sdu	RR_NR126_A0_S3
<F>	sdu	RR_NR157_A0_S3
<G>	sdu	RR_NR188_A0_S3
<H>	sdu	RR_NR219_A0_S3
<I>	sdu	RR_NR250_A0_S3
<J>	sdu	RR_NR281_A0_S3
<K>	sdu	RR_NR312_A0_S3
<L>	sdu	RR_NR343_A0_S3
<M>	sdu	RR_NR374_A0_S3
<N>	sdu	RR_NR405_A0_S3
<O>	sdu	RR_NR436_A0_S3
<P>	sdu	RR_NR467_A0_S3
(12)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_1
(13)	GRR_READY_IND	

(14)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_2
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(15)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS2_NR0_A1_S3
	sdu	RR_D3_NS33_NR0_A1_S3
<C>	sdu	RR_D3_NS64_NR0_A1_S3
<D>	sdu	RR_D3_NS95_NR0_A1_S3
<E>	sdu	RR_D3_NS126_NR0_A1_S3
<F>	sdu	RR_D3_NS157_NR0_A1_S3
<G>	sdu	RR_D3_NS188_NR0_A1_S3
<H>	sdu	RR_D3_NS219_NR0_A1_S3
<I>	sdu	RR_D3_NS250_NR0_A1_S3
<J>	sdu	RR_D3_NS281_NR0_A1_S3
<K>	sdu	RR_D3_NS312_NR0_A1_S3
<L>	sdu	RR_D3_NS343_NR0_A1_S3
<M>	sdu	RR_D3_NS374_NR0_A1_S3
<N>	sdu	RR_D3_NS405_NR0_A1_S3
<O>	sdu	RR_D3_NS436_NR0_A1_S3
<P>	sdu	RR_D3_NS467_NR0_A1_S3
(16)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(17)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR3_A0_S3
	sdu	RR_NR34_A0_S3
<C>	sdu	RR_NR65_A0_S3
<D>	sdu	RR_NR96_A0_S3
<E>	sdu	RR_NR127_A0_S3
<F>	sdu	RR_NR158_A0_S3
<G>	sdu	RR_NR189_A0_S3
<H>	sdu	RR_NR220_A0_S3
<I>	sdu	RR_NR251_A0_S3
<J>	sdu	RR_NR282_A0_S3
<K>	sdu	RR_NR313_A0_S3
<L>	sdu	RR_NR344_A0_S3
<M>	sdu	RR_NR375_A0_S3
<N>	sdu	RR_NR406_A0_S3
<O>	sdu	RR_NR437_A0_S3
<P>	sdu	RR_NR468_A0_S3

(18)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_2
(19)	GRR_READY_IND	
(20)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_3 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(21)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req <A> <C> <D> <E> <F> <G> <H> <I> <J> <K> <L> <M> <N> <O> <P>	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS3_NR0_A1_S3 RR_D3_NS34_NR0_A1_S3 RR_D3_NS65_NR0_A1_S3 RR_D3_NS96_NR0_A1_S3 RR_D3_NS127_NR0_A1_S3 RR_D3_NS158_NR0_A1_S3 RR_D3_NS189_NR0_A1_S3 RR_D3_NS220_NR0_A1_S3 RR_D3_NS251_NR0_A1_S3 RR_D3_NS282_NR0_A1_S3 RR_D3_NS313_NR0_A1_S3 RR_D3_NS344_NR0_A1_S3 RR_D3_NS375_NR0_A1_S3 RR_D3_NS406_NR0_A1_S3 RR_D3_NS437_NR0_A1_S3 RR_D3_NS468_NR0_A1_S3
(22)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(23)	GRR_DATA_IND_TEST tlli <A> <C> <D> <E> <F> <G> <H> <I> <J>	TLLI_LOCAL_1 RR_NR4_A0_S3 RR_NR35_A0_S3 RR_NR66_A0_S3 RR_NR97_A0_S3 RR_NR128_A0_S3 RR_NR159_A0_S3 RR_NR190_A0_S3 RR_NR221_A0_S3 RR_NR252_A0_S3 RR_NR283_A0_S3

<K>	sdu	RR_NR314_A0_S3
<L>	sdu	RR_NR345_A0_S3
<M>	sdu	RR_NR376_A0_S3
<N>	sdu	RR_NR407_A0_S3
<O>	sdu	RR_NR438_A0_S3
<P>	sdu	RR_NR469_A0_S3
(24)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_3
(25)	GRR_READY_IND	
(26)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_4
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(27)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS4_NR0_A1_S3
	sdu	RR_D3_NS35_NR0_A1_S3
<C>	sdu	RR_D3_NS66_NR0_A1_S3
<D>	sdu	RR_D3_NS97_NR0_A1_S3
<E>	sdu	RR_D3_NS128_NR0_A1_S3
<F>	sdu	RR_D3_NS159_NR0_A1_S3
<G>	sdu	RR_D3_NS190_NR0_A1_S3
<H>	sdu	RR_D3_NS221_NR0_A1_S3
<I>	sdu	RR_D3_NS252_NR0_A1_S3
<J>	sdu	RR_D3_NS283_NR0_A1_S3
<K>	sdu	RR_D3_NS314_NR0_A1_S3
<L>	sdu	RR_D3_NS345_NR0_A1_S3
<M>	sdu	RR_D3_NS376_NR0_A1_S3
<N>	sdu	RR_D3_NS407_NR0_A1_S3
<O>	sdu	RR_D3_NS438_NR0_A1_S3
<P>	sdu	RR_D3_NS469_NR0_A1_S3
(28)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(29)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR5_A0_S3
	sdu	RR_NR36_A0_S3
<C>	sdu	RR_NR67_A0_S3
<D>	sdu	RR_NR98_A0_S3

<E>	sdu	RR_NR129_A0_S3
<F>	sdu	RR_NR160_A0_S3
<G>	sdu	RR_NR191_A0_S3
<H>	sdu	RR_NR222_A0_S3
<I>	sdu	RR_NR253_A0_S3
<J>	sdu	RR_NR284_A0_S3
<K>	sdu	RR_NR315_A0_S3
<L>	sdu	RR_NR346_A0_S3
<M>	sdu	RR_NR377_A0_S3
<N>	sdu	RR_NR408_A0_S3
<O>	sdu	RR_NR439_A0_S3
<P>	sdu	RR_NR470_A0_S3
(30)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_4
(31)	GRR_READY_IND	
(32)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_5
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(33)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS5_NR0_A1_S3
	sdu	RR_D3_NS36_NR0_A1_S3
<C>	sdu	RR_D3_NS67_NR0_A1_S3
<D>	sdu	RR_D3_NS98_NR0_A1_S3
<E>	sdu	RR_D3_NS129_NR0_A1_S3
<F>	sdu	RR_D3_NS160_NR0_A1_S3
<G>	sdu	RR_D3_NS191_NR0_A1_S3
<H>	sdu	RR_D3_NS222_NR0_A1_S3
<I>	sdu	RR_D3_NS253_NR0_A1_S3
<J>	sdu	RR_D3_NS284_NR0_A1_S3
<K>	sdu	RR_D3_NS315_NR0_A1_S3
<L>	sdu	RR_D3_NS346_NR0_A1_S3
<M>	sdu	RR_D3_NS377_NR0_A1_S3
<N>	sdu	RR_D3_NS408_NR0_A1_S3
<O>	sdu	RR_D3_NS439_NR0_A1_S3
<P>	sdu	RR_D3_NS470_NR0_A1_S3
(34)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

(35)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR6_A0_S3
	sdu	RR_NR37_A0_S3
<C>	sdu	RR_NR68_A0_S3
<D>	sdu	RR_NR99_A0_S3
<E>	sdu	RR_NR130_A0_S3
<F>	sdu	RR_NR161_A0_S3
<G>	sdu	RR_NR192_A0_S3
<H>	sdu	RR_NR223_A0_S3
<I>	sdu	RR_NR254_A0_S3
<J>	sdu	RR_NR285_A0_S3
<K>	sdu	RR_NR316_A0_S3
<L>	sdu	RR_NR347_A0_S3
<M>	sdu	RR_NR378_A0_S3
<N>	sdu	RR_NR409_A0_S3
<O>	sdu	RR_NR440_A0_S3
<P>	sdu	RR_NR471_A0_S3
(36)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_5
(37)	GRR_READY_IND	
(38)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_6
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(39)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS6_NR0_A1_S3
	sdu	RR_D3_NS37_NR0_A1_S3
<C>	sdu	RR_D3_NS68_NR0_A1_S3
<D>	sdu	RR_D3_NS99_NR0_A1_S3
<E>	sdu	RR_D3_NS130_NR0_A1_S3
<F>	sdu	RR_D3_NS161_NR0_A1_S3
<G>	sdu	RR_D3_NS192_NR0_A1_S3
<H>	sdu	RR_D3_NS223_NR0_A1_S3
<I>	sdu	RR_D3_NS254_NR0_A1_S3
<J>	sdu	RR_D3_NS285_NR0_A1_S3
<K>	sdu	RR_D3_NS316_NR0_A1_S3
<L>	sdu	RR_D3_NS347_NR0_A1_S3
<M>	sdu	RR_D3_NS378_NR0_A1_S3
<N>	sdu	RR_D3_NS409_NR0_A1_S3

	<O>	sdu	RR_D3_NS440_NR0_A1_S3
	<P>	sdu	RR_D3_NS471_NR0_A1_S3
(40)		LL_READY_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(41)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_NR7_A0_S3
		sdu	RR_NR38_A0_S3
	<C>	sdu	RR_NR69_A0_S3
	<D>	sdu	RR_NR100_A0_S3
	<E>	sdu	RR_NR131_A0_S3
	<F>	sdu	RR_NR162_A0_S3
	<G>	sdu	RR_NR193_A0_S3
	<H>	sdu	RR_NR224_A0_S3
	<I>	sdu	RR_NR255_A0_S3
	<J>	sdu	RR_NR286_A0_S3
	<K>	sdu	RR_NR317_A0_S3
	<L>	sdu	RR_NR348_A0_S3
	<M>	sdu	RR_NR379_A0_S3
	<N>	sdu	RR_NR410_A0_S3
	<O>	sdu	RR_NR441_A0_S3
	<P>	sdu	RR_NR472_A0_S3
(42)		LL_DATA_CNF	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reference1	A_REF_6
(43)		GRR_READY_IND	
(44)		LL_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		ll_qos	LLREQ_QOS_DEL4_REL5
		radio_prio	LL_RADIO_PRIO_3
		reserved_data_req1	NOT_USED
		reference1	REF_7
		seg_pos	NOT_USED
		attached_counter	LLC_NO_ATTACHE
		reserved_data_req4	NOT_USED
		reserved_data_req5	NOT_USED
		sdu	LLREQ_SDU3
(45)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKSUB
		radio_prio	GRR_RADIO_PRIO_3
		cause	GRR_DTACS_DEF
		reserved_data_req	NOT_USED
	<A>	sdu	RR_D3_NS7_NR0_A1_S3
		sdu	RR_D3_NS38_NR0_A1_S3
	<C>	sdu	RR_D3_NS69_NR0_A1_S3
	<D>	sdu	RR_D3_NS100_NR0_A1_S3
	<E>	sdu	RR_D3_NS131_NR0_A1_S3
	<F>	sdu	RR_D3_NS162_NR0_A1_S3
	<G>	sdu	RR_D3_NS193_NR0_A1_S3
	<H>	sdu	RR_D3_NS224_NR0_A1_S3

	<I>	sdu	RR_D3_NS255_NR0_A1_S3
	<J>	sdu	RR_D3_NS286_NR0_A1_S3
	<K>	sdu	RR_D3_NS317_NR0_A1_S3
	<L>	sdu	RR_D3_NS348_NR0_A1_S3
	<M>	sdu	RR_D3_NS379_NR0_A1_S3
	<N>	sdu	RR_D3_NS410_NR0_A1_S3
	<O>	sdu	RR_D3_NS441_NR0_A1_S3
	<P>	sdu	RR_D3_NS472_NR0_A1_S3
(46)		LL_READY_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(47)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_NR8_A0_S3
		sdu	RR_NR39_A0_S3
	<C>	sdu	RR_NR70_A0_S3
	<D>	sdu	RR_NR101_A0_S3
	<E>	sdu	RR_NR132_A0_S3
	<F>	sdu	RR_NR163_A0_S3
	<G>	sdu	RR_NR194_A0_S3
	<H>	sdu	RR_NR225_A0_S3
	<I>	sdu	RR_NR256_A0_S3
	<J>	sdu	RR_NR287_A0_S3
	<K>	sdu	RR_NR318_A0_S3
	<L>	sdu	RR_NR349_A0_S3
	<M>	sdu	RR_NR380_A0_S3
	<N>	sdu	RR_NR411_A0_S3
	<O>	sdu	RR_NR442_A0_S3
	<P>	sdu	RR_NR473_A0_S3
(48)		LL_DATA_CNF	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reference1	A_REF_7
(49)		GRR_READY_IND	
(50)		LL_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		ll_qos	LLREQ_QOS_DEL4_REL5
		radio_prio	LL_RADIO_PRIO_3
		reserved_data_req1	NOT_USED
		reference1	REF_8
		seg_pos	NOT_USED
		attached_counter	LLC_NO_ATTACHE
		reserved_data_req4	NOT_USED
		reserved_data_req5	NOT_USED
		sdu	LLREQ_SDU3
(51)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKEUB
		radio_prio	GRR_RADIO_PRIO_3
		cause	GRR_DTACS_DEF
		reserved_data_req	NOT_USED
	<A>	sdu	RR_D3_NS8_NR0_A1_S3
		sdu	RR_D3_NS39_NR0_A1_S3

<C>	sdu	RR_D3_NS70_NR0_A1_S3
<D>	sdu	RR_D3_NS101_NR0_A1_S3
<E>	sdu	RR_D3_NS132_NR0_A1_S3
<F>	sdu	RR_D3_NS163_NR0_A1_S3
<G>	sdu	RR_D3_NS194_NR0_A1_S3
<H>	sdu	RR_D3_NS225_NR0_A1_S3
<I>	sdu	RR_D3_NS256_NR0_A1_S3
<J>	sdu	RR_D3_NS287_NR0_A1_S3
<K>	sdu	RR_D3_NS318_NR0_A1_S3
<L>	sdu	RR_D3_NS349_NR0_A1_S3
<M>	sdu	RR_D3_NS380_NR0_A1_S3
<N>	sdu	RR_D3_NS411_NR0_A1_S3
<O>	sdu	RR_D3_NS442_NR0_A1_S3
<P>	sdu	RR_D3_NS473_NR0_A1_S3
(52)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(53)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR9_A0_S3
	sdu	RR_NR40_A0_S3
<C>	sdu	RR_NR71_A0_S3
<D>	sdu	RR_NR102_A0_S3
<E>	sdu	RR_NR133_A0_S3
<F>	sdu	RR_NR164_A0_S3
<G>	sdu	RR_NR195_A0_S3
<H>	sdu	RR_NR226_A0_S3
<I>	sdu	RR_NR257_A0_S3
<J>	sdu	RR_NR288_A0_S3
<K>	sdu	RR_NR319_A0_S3
<L>	sdu	RR_NR350_A0_S3
<M>	sdu	RR_NR381_A0_S3
<N>	sdu	RR_NR412_A0_S3
<O>	sdu	RR_NR443_A0_S3
<P>	sdu	RR_NR474_A0_S3
(54)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_8
(55)	GRR_READY_IND	
(56)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_9
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(57)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS9_NR0_A1_S3
	sdu	RR_D3_NS40_NR0_A1_S3
<C>	sdu	RR_D3_NS71_NR0_A1_S3
<D>	sdu	RR_D3_NS102_NR0_A1_S3
<E>	sdu	RR_D3_NS133_NR0_A1_S3
<F>	sdu	RR_D3_NS164_NR0_A1_S3
<G>	sdu	RR_D3_NS195_NR0_A1_S3
<H>	sdu	RR_D3_NS226_NR0_A1_S3
<I>	sdu	RR_D3_NS257_NR0_A1_S3
<J>	sdu	RR_D3_NS288_NR0_A1_S3
<K>	sdu	RR_D3_NS319_NR0_A1_S3
<L>	sdu	RR_D3_NS350_NR0_A1_S3
<M>	sdu	RR_D3_NS381_NR0_A1_S3
<N>	sdu	RR_D3_NS412_NR0_A1_S3
<O>	sdu	RR_D3_NS443_NR0_A1_S3
<P>	sdu	RR_D3_NS474_NR0_A1_S3
(58)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(59)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR10_A0_S3
	sdu	RR_NR41_A0_S3
<C>	sdu	RR_NR72_A0_S3
<D>	sdu	RR_NR103_A0_S3
<E>	sdu	RR_NR134_A0_S3
<F>	sdu	RR_NR165_A0_S3
<G>	sdu	RR_NR196_A0_S3
<H>	sdu	RR_NR227_A0_S3
<I>	sdu	RR_NR258_A0_S3
<J>	sdu	RR_NR289_A0_S3
<K>	sdu	RR_NR320_A0_S3
<L>	sdu	RR_NR351_A0_S3
<M>	sdu	RR_NR382_A0_S3
<N>	sdu	RR_NR413_A0_S3
<O>	sdu	RR_NR444_A0_S3
<P>	sdu	RR_NR475_A0_S3
(60)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_9
(61)	GRR_READY_IND	
(62)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_10
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE

	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(63)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIQ_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS10_NR0_A1_S3
	sdu	RR_D3_NS41_NR0_A1_S3
<C>	sdu	RR_D3_NS72_NR0_A1_S3
<D>	sdu	RR_D3_NS103_NR0_A1_S3
<E>	sdu	RR_D3_NS134_NR0_A1_S3
<F>	sdu	RR_D3_NS165_NR0_A1_S3
<G>	sdu	RR_D3_NS196_NR0_A1_S3
<H>	sdu	RR_D3_NS227_NR0_A1_S3
<I>	sdu	RR_D3_NS258_NR0_A1_S3
<J>	sdu	RR_D3_NS289_NR0_A1_S3
<K>	sdu	RR_D3_NS320_NR0_A1_S3
<L>	sdu	RR_D3_NS351_NR0_A1_S3
<M>	sdu	RR_D3_NS382_NR0_A1_S3
<N>	sdu	RR_D3_NS413_NR0_A1_S3
<O>	sdu	RR_D3_NS444_NR0_A1_S3
<P>	sdu	RR_D3_NS475_NR0_A1_S3
(64)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(65)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR11_A0_S3
	sdu	RR_NR42_A0_S3
<C>	sdu	RR_NR73_A0_S3
<D>	sdu	RR_NR104_A0_S3
<E>	sdu	RR_NR135_A0_S3
<F>	sdu	RR_NR166_A0_S3
<G>	sdu	RR_NR197_A0_S3
<H>	sdu	RR_NR228_A0_S3
<I>	sdu	RR_NR259_A0_S3
<J>	sdu	RR_NR290_A0_S3
<K>	sdu	RR_NR321_A0_S3
<L>	sdu	RR_NR352_A0_S3
<M>	sdu	RR_NR383_A0_S3
<N>	sdu	RR_NR414_A0_S3
<O>	sdu	RR_NR445_A0_S3
<P>	sdu	RR_NR476_A0_S3
(66)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_10
(67)	GRR_READY_IND	
(68)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_11
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(69)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS11_NR0_A1_S3
	sdu	RR_D3_NS42_NR0_A1_S3
<C>	sdu	RR_D3_NS73_NR0_A1_S3
<D>	sdu	RR_D3_NS104_NR0_A1_S3
<E>	sdu	RR_D3_NS135_NR0_A1_S3
<F>	sdu	RR_D3_NS166_NR0_A1_S3
<G>	sdu	RR_D3_NS197_NR0_A1_S3
<H>	sdu	RR_D3_NS228_NR0_A1_S3
<I>	sdu	RR_D3_NS259_NR0_A1_S3
<J>	sdu	RR_D3_NS290_NR0_A1_S3
<K>	sdu	RR_D3_NS321_NR0_A1_S3
<L>	sdu	RR_D3_NS352_NR0_A1_S3
<M>	sdu	RR_D3_NS383_NR0_A1_S3
<N>	sdu	RR_D3_NS414_NR0_A1_S3
<O>	sdu	RR_D3_NS445_NR0_A1_S3
<P>	sdu	RR_D3_NS476_NR0_A1_S3
(70)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(71)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR12_A0_S3
	sdu	RR_NR43_A0_S3
<C>	sdu	RR_NR74_A0_S3
<D>	sdu	RR_NR105_A0_S3
<E>	sdu	RR_NR136_A0_S3
<F>	sdu	RR_NR167_A0_S3
<G>	sdu	RR_NR198_A0_S3
<H>	sdu	RR_NR229_A0_S3
<I>	sdu	RR_NR260_A0_S3
<J>	sdu	RR_NR291_A0_S3
<K>	sdu	RR_NR322_A0_S3
<L>	sdu	RR_NR353_A0_S3
<M>	sdu	RR_NR384_A0_S3
<N>	sdu	RR_NR415_A0_S3
<O>	sdu	RR_NR446_A0_S3
<P>	sdu	RR_NR477_A0_S3
(72)	LL_DATA_CNF	
	sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	reference1	A_REF_11
(73)	GRR_READY_IND	
(74)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_12
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(75)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS12_NR0_A1_S3
	sdu	RR_D3_NS43_NR0_A1_S3
<C>	sdu	RR_D3_NS74_NR0_A1_S3
<D>	sdu	RR_D3_NS105_NR0_A1_S3
<E>	sdu	RR_D3_NS136_NR0_A1_S3
<F>	sdu	RR_D3_NS167_NR0_A1_S3
<G>	sdu	RR_D3_NS198_NR0_A1_S3
<H>	sdu	RR_D3_NS229_NR0_A1_S3
<I>	sdu	RR_D3_NS260_NR0_A1_S3
<J>	sdu	RR_D3_NS291_NR0_A1_S3
<K>	sdu	RR_D3_NS322_NR0_A1_S3
<L>	sdu	RR_D3_NS353_NR0_A1_S3
<M>	sdu	RR_D3_NS384_NR0_A1_S3
<N>	sdu	RR_D3_NS415_NR0_A1_S3
<O>	sdu	RR_D3_NS446_NR0_A1_S3
<P>	sdu	RR_D3_NS477_NR0_A1_S3
(76)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(77)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR13_A0_S3
	sdu	RR_NR44_A0_S3
<C>	sdu	RR_NR75_A0_S3
<D>	sdu	RR_NR106_A0_S3
<E>	sdu	RR_NR137_A0_S3
<F>	sdu	RR_NR168_A0_S3
<G>	sdu	RR_NR199_A0_S3
<H>	sdu	RR_NR230_A0_S3
<I>	sdu	RR_NR261_A0_S3
<J>	sdu	RR_NR292_A0_S3
<K>	sdu	RR_NR323_A0_S3
<L>	sdu	RR_NR354_A0_S3

<M>	sdu	RR_NR385_A0_S3
<N>	sdu	RR_NR416_A0_S3
<O>	sdu	RR_NR447_A0_S3
<P>	sdu	RR_NR478_A0_S3
(78)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_12
(79)	GRR_READY_IND	
(80)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_13
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(81)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS13_NR0_A1_S3
	sdu	RR_D3_NS44_NR0_A1_S3
<C>	sdu	RR_D3_NS75_NR0_A1_S3
<D>	sdu	RR_D3_NS106_NR0_A1_S3
<E>	sdu	RR_D3_NS137_NR0_A1_S3
<F>	sdu	RR_D3_NS168_NR0_A1_S3
<G>	sdu	RR_D3_NS199_NR0_A1_S3
<H>	sdu	RR_D3_NS230_NR0_A1_S3
<I>	sdu	RR_D3_NS261_NR0_A1_S3
<J>	sdu	RR_D3_NS292_NR0_A1_S3
<K>	sdu	RR_D3_NS323_NR0_A1_S3
<L>	sdu	RR_D3_NS354_NR0_A1_S3
<M>	sdu	RR_D3_NS385_NR0_A1_S3
<N>	sdu	RR_D3_NS416_NR0_A1_S3
<O>	sdu	RR_D3_NS447_NR0_A1_S3
<P>	sdu	RR_D3_NS478_NR0_A1_S3
(82)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(83)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR14_A0_S3
	sdu	RR_NR45_A0_S3
<C>	sdu	RR_NR76_A0_S3
<D>	sdu	RR_NR107_A0_S3
<E>	sdu	RR_NR138_A0_S3
<F>	sdu	RR_NR169_A0_S3

	<G>	sdu	RR_NR200_A0_S3
	<H>	sdu	RR_NR231_A0_S3
	<I>	sdu	RR_NR262_A0_S3
	<J>	sdu	RR_NR293_A0_S3
	<K>	sdu	RR_NR324_A0_S3
	<L>	sdu	RR_NR355_A0_S3
	<M>	sdu	RR_NR386_A0_S3
	<N>	sdu	RR_NR417_A0_S3
	<O>	sdu	RR_NR448_A0_S3
	<P>	sdu	RR_NR479_A0_S3
(84)		LL_DATA_CNF	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reference1	A_REF_13
(85)		GRR_READY_IND	
(86)		LL_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		ll_qos	LLREQ_QOS_DEL4_REL5
		radio_prio	LL_RADIO_PRIO_3
		reserved_data_req1	NOT_USED
		reference1	REF_14
		seg_pos	NOT_USED
		attached_counter	LLC_NO_ATTACHE
		reserved_data_req4	NOT_USED
		reserved_data_req5	NOT_USED
		sdu	LLREQ_SDU3
(87)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKEUB
		radio_prio	GRR_RADIO_PRIO_3
		cause	GRR_DTACS_DEF
		reserved_data_req	NOT_USED
	<A>	sdu	RR_D3_NS14_NR0_A1_S3
		sdu	RR_D3_NS45_NR0_A1_S3
	<C>	sdu	RR_D3_NS76_NR0_A1_S3
	<D>	sdu	RR_D3_NS107_NR0_A1_S3
	<E>	sdu	RR_D3_NS138_NR0_A1_S3
	<F>	sdu	RR_D3_NS169_NR0_A1_S3
	<G>	sdu	RR_D3_NS200_NR0_A1_S3
	<H>	sdu	RR_D3_NS231_NR0_A1_S3
	<I>	sdu	RR_D3_NS262_NR0_A1_S3
	<J>	sdu	RR_D3_NS293_NR0_A1_S3
	<K>	sdu	RR_D3_NS324_NR0_A1_S3
	<L>	sdu	RR_D3_NS355_NR0_A1_S3
	<M>	sdu	RR_D3_NS386_NR0_A1_S3
	<N>	sdu	RR_D3_NS417_NR0_A1_S3
	<O>	sdu	RR_D3_NS448_NR0_A1_S3
	<P>	sdu	RR_D3_NS479_NR0_A1_S3
(88)		LL_READY_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(89)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1

<A>	sdu	RR_NR15_A0_S3
	sdu	RR_NR46_A0_S3
<C>	sdu	RR_NR77_A0_S3
<D>	sdu	RR_NR108_A0_S3
<E>	sdu	RR_NR139_A0_S3
<F>	sdu	RR_NR170_A0_S3
<G>	sdu	RR_NR201_A0_S3
<H>	sdu	RR_NR232_A0_S3
<I>	sdu	RR_NR263_A0_S3
<J>	sdu	RR_NR294_A0_S3
<K>	sdu	RR_NR325_A0_S3
<L>	sdu	RR_NR356_A0_S3
<M>	sdu	RR_NR387_A0_S3
<N>	sdu	RR_NR418_A0_S3
<O>	sdu	RR_NR449_A0_S3
<P>	sdu	RR_NR480_A0_S3
(90)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_14
(91)	GRR_READY_IND	
(92)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_15
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(93)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS15_NR0_A1_S3
	sdu	RR_D3_NS46_NR0_A1_S3
<C>	sdu	RR_D3_NS77_NR0_A1_S3
<D>	sdu	RR_D3_NS108_NR0_A1_S3
<E>	sdu	RR_D3_NS139_NR0_A1_S3
<F>	sdu	RR_D3_NS170_NR0_A1_S3
<G>	sdu	RR_D3_NS201_NR0_A1_S3
<H>	sdu	RR_D3_NS232_NR0_A1_S3
<I>	sdu	RR_D3_NS263_NR0_A1_S3
<J>	sdu	RR_D3_NS294_NR0_A1_S3
<K>	sdu	RR_D3_NS325_NR0_A1_S3
<L>	sdu	RR_D3_NS356_NR0_A1_S3
<M>	sdu	RR_D3_NS387_NR0_A1_S3
<N>	sdu	RR_D3_NS418_NR0_A1_S3
<O>	sdu	RR_D3_NS449_NR0_A1_S3
<P>	sdu	RR_D3_NS480_NR0_A1_S3

(94)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(95)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR16_A0_S3
	sdu	RR_NR47_A0_S3
<C>	sdu	RR_NR78_A0_S3
<D>	sdu	RR_NR109_A0_S3
<E>	sdu	RR_NR140_A0_S3
<F>	sdu	RR_NR171_A0_S3
<G>	sdu	RR_NR202_A0_S3
<H>	sdu	RR_NR233_A0_S3
<I>	sdu	RR_NR264_A0_S3
<J>	sdu	RR_NR295_A0_S3
<K>	sdu	RR_NR326_A0_S3
<L>	sdu	RR_NR357_A0_S3
<M>	sdu	RR_NR388_A0_S3
<N>	sdu	RR_NR419_A0_S3
<O>	sdu	RR_NR450_A0_S3
<P>	sdu	RR_NR481_A0_S3
(96)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_15
(97)	GRR_READY_IND	
(98)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_16
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(99)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS16_NR0_A1_S3
	sdu	RR_D3_NS47_NR0_A1_S3
<C>	sdu	RR_D3_NS78_NR0_A1_S3
<D>	sdu	RR_D3_NS109_NR0_A1_S3
<E>	sdu	RR_D3_NS140_NR0_A1_S3
<F>	sdu	RR_D3_NS171_NR0_A1_S3
<G>	sdu	RR_D3_NS202_NR0_A1_S3
<H>	sdu	RR_D3_NS233_NR0_A1_S3
<I>	sdu	RR_D3_NS264_NR0_A1_S3
<J>	sdu	RR_D3_NS295_NR0_A1_S3

<K>	sdu	RR_D3_NS326_NR0_A1_S3
<L>	sdu	RR_D3_NS357_NR0_A1_S3
<M>	sdu	RR_D3_NS388_NR0_A1_S3
<N>	sdu	RR_D3_NS419_NR0_A1_S3
<O>	sdu	RR_D3_NS450_NR0_A1_S3
<P>	sdu	RR_D3_NS481_NR0_A1_S3
(100)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(101)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR17_A0_S3
	sdu	RR_NR48_A0_S3
<C>	sdu	RR_NR79_A0_S3
<D>	sdu	RR_NR110_A0_S3
<E>	sdu	RR_NR141_A0_S3
<F>	sdu	RR_NR172_A0_S3
<G>	sdu	RR_NR203_A0_S3
<H>	sdu	RR_NR234_A0_S3
<I>	sdu	RR_NR265_A0_S3
<J>	sdu	RR_NR296_A0_S3
<K>	sdu	RR_NR327_A0_S3
<L>	sdu	RR_NR358_A0_S3
<M>	sdu	RR_NR389_A0_S3
<N>	sdu	RR_NR420_A0_S3
<O>	sdu	RR_NR451_A0_S3
<P>	sdu	RR_NR482_A0_S3
(102)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_16
(103)	GRR_READY_IND	
(104)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_17
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(105)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS17_NR0_A1_S3
	sdu	RR_D3_NS48_NR0_A1_S3
<C>	sdu	RR_D3_NS79_NR0_A1_S3
<D>	sdu	RR_D3_NS110_NR0_A1_S3

<E>	sdu	RR_D3_NS141_NR0_A1_S3
<F>	sdu	RR_D3_NS172_NR0_A1_S3
<G>	sdu	RR_D3_NS203_NR0_A1_S3
<H>	sdu	RR_D3_NS234_NR0_A1_S3
<I>	sdu	RR_D3_NS265_NR0_A1_S3
<J>	sdu	RR_D3_NS296_NR0_A1_S3
<K>	sdu	RR_D3_NS327_NR0_A1_S3
<L>	sdu	RR_D3_NS358_NR0_A1_S3
<M>	sdu	RR_D3_NS389_NR0_A1_S3
<N>	sdu	RR_D3_NS420_NR0_A1_S3
<O>	sdu	RR_D3_NS451_NR0_A1_S3
<P>	sdu	RR_D3_NS482_NR0_A1_S3
(106)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(107)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR18_A0_S3
	sdu	RR_NR49_A0_S3
<C>	sdu	RR_NR80_A0_S3
<D>	sdu	RR_NR111_A0_S3
<E>	sdu	RR_NR142_A0_S3
<F>	sdu	RR_NR173_A0_S3
<G>	sdu	RR_NR204_A0_S3
<H>	sdu	RR_NR235_A0_S3
<I>	sdu	RR_NR266_A0_S3
<J>	sdu	RR_NR297_A0_S3
<K>	sdu	RR_NR328_A0_S3
<L>	sdu	RR_NR359_A0_S3
<M>	sdu	RR_NR390_A0_S3
<N>	sdu	RR_NR421_A0_S3
<O>	sdu	RR_NR452_A0_S3
<P>	sdu	RR_NR483_A0_S3
(108)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_17
(109)	GRR_READY_IND	
(110)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_18
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(111)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3

	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS18_NR0_A1_S3
	sdu	RR_D3_NS49_NR0_A1_S3
<C>	sdu	RR_D3_NS80_NR0_A1_S3
<D>	sdu	RR_D3_NS111_NR0_A1_S3
<E>	sdu	RR_D3_NS142_NR0_A1_S3
<F>	sdu	RR_D3_NS173_NR0_A1_S3
<G>	sdu	RR_D3_NS204_NR0_A1_S3
<H>	sdu	RR_D3_NS235_NR0_A1_S3
<I>	sdu	RR_D3_NS266_NR0_A1_S3
<J>	sdu	RR_D3_NS297_NR0_A1_S3
<K>	sdu	RR_D3_NS328_NR0_A1_S3
<L>	sdu	RR_D3_NS359_NR0_A1_S3
<M>	sdu	RR_D3_NS390_NR0_A1_S3
<N>	sdu	RR_D3_NS421_NR0_A1_S3
<O>	sdu	RR_D3_NS452_NR0_A1_S3
<P>	sdu	RR_D3_NS483_NR0_A1_S3
(112)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(113)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR19_A0_S3
	sdu	RR_NR50_A0_S3
<C>	sdu	RR_NR81_A0_S3
<D>	sdu	RR_NR112_A0_S3
<E>	sdu	RR_NR143_A0_S3
<F>	sdu	RR_NR174_A0_S3
<G>	sdu	RR_NR205_A0_S3
<H>	sdu	RR_NR236_A0_S3
<I>	sdu	RR_NR267_A0_S3
<J>	sdu	RR_NR298_A0_S3
<K>	sdu	RR_NR329_A0_S3
<L>	sdu	RR_NR360_A0_S3
<M>	sdu	RR_NR391_A0_S3
<N>	sdu	RR_NR422_A0_S3
<O>	sdu	RR_NR453_A0_S3
<P>	sdu	RR_NR484_A0_S3
(114)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_18
(115)	GRR_READY_IND	
(116)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_19
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED

	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(117)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIQ_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS19_NR0_A1_S3
	sdu	RR_D3_NS50_NR0_A1_S3
<C>	sdu	RR_D3_NS81_NR0_A1_S3
<D>	sdu	RR_D3_NS112_NR0_A1_S3
<E>	sdu	RR_D3_NS143_NR0_A1_S3
<F>	sdu	RR_D3_NS174_NR0_A1_S3
<G>	sdu	RR_D3_NS205_NR0_A1_S3
<H>	sdu	RR_D3_NS236_NR0_A1_S3
<I>	sdu	RR_D3_NS267_NR0_A1_S3
<J>	sdu	RR_D3_NS298_NR0_A1_S3
<K>	sdu	RR_D3_NS329_NR0_A1_S3
<L>	sdu	RR_D3_NS360_NR0_A1_S3
<M>	sdu	RR_D3_NS391_NR0_A1_S3
<N>	sdu	RR_D3_NS422_NR0_A1_S3
<O>	sdu	RR_D3_NS453_NR0_A1_S3
<P>	sdu	RR_D3_NS484_NR0_A1_S3
(118)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(119)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR20_A0_S3
	sdu	RR_NR51_A0_S3
<C>	sdu	RR_NR82_A0_S3
<D>	sdu	RR_NR113_A0_S3
<E>	sdu	RR_NR144_A0_S3
<F>	sdu	RR_NR175_A0_S3
<G>	sdu	RR_NR206_A0_S3
<H>	sdu	RR_NR237_A0_S3
<I>	sdu	RR_NR268_A0_S3
<J>	sdu	RR_NR299_A0_S3
<K>	sdu	RR_NR330_A0_S3
<L>	sdu	RR_NR361_A0_S3
<M>	sdu	RR_NR392_A0_S3
<N>	sdu	RR_NR423_A0_S3
<O>	sdu	RR_NR454_A0_S3
<P>	sdu	RR_NR485_A0_S3
(120)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_19
(121)	GRR_READY_IND	
(122)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DELE4_REL5

	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_20
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(123)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS20_NR0_A1_S3
	sdu	RR_D3_NS51_NR0_A1_S3
<C>	sdu	RR_D3_NS82_NR0_A1_S3
<D>	sdu	RR_D3_NS113_NR0_A1_S3
<E>	sdu	RR_D3_NS144_NR0_A1_S3
<F>	sdu	RR_D3_NS175_NR0_A1_S3
<G>	sdu	RR_D3_NS206_NR0_A1_S3
<H>	sdu	RR_D3_NS237_NR0_A1_S3
<I>	sdu	RR_D3_NS268_NR0_A1_S3
<J>	sdu	RR_D3_NS299_NR0_A1_S3
<K>	sdu	RR_D3_NS330_NR0_A1_S3
<L>	sdu	RR_D3_NS361_NR0_A1_S3
<M>	sdu	RR_D3_NS392_NR0_A1_S3
<N>	sdu	RR_D3_NS423_NR0_A1_S3
<O>	sdu	RR_D3_NS454_NR0_A1_S3
<P>	sdu	RR_D3_NS485_NR0_A1_S3
(124)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(125)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR21_A0_S3
	sdu	RR_NR52_A0_S3
<C>	sdu	RR_NR83_A0_S3
<D>	sdu	RR_NR114_A0_S3
<E>	sdu	RR_NR145_A0_S3
<F>	sdu	RR_NR176_A0_S3
<G>	sdu	RR_NR207_A0_S3
<H>	sdu	RR_NR238_A0_S3
<I>	sdu	RR_NR269_A0_S3
<J>	sdu	RR_NR300_A0_S3
<K>	sdu	RR_NR331_A0_S3
<L>	sdu	RR_NR362_A0_S3
<M>	sdu	RR_NR393_A0_S3
<N>	sdu	RR_NR424_A0_S3
<O>	sdu	RR_NR455_A0_S3
<P>	sdu	RR_NR486_A0_S3
(126)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_20

(127)	GRR_READY_IND	
(128)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_21
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(129)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKESSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS21_NR0_A1_S3
	sdu	RR_D3_NS52_NR0_A1_S3
<C>	sdu	RR_D3_NS83_NR0_A1_S3
<D>	sdu	RR_D3_NS114_NR0_A1_S3
<E>	sdu	RR_D3_NS145_NR0_A1_S3
<F>	sdu	RR_D3_NS176_NR0_A1_S3
<G>	sdu	RR_D3_NS207_NR0_A1_S3
<H>	sdu	RR_D3_NS238_NR0_A1_S3
<I>	sdu	RR_D3_NS269_NR0_A1_S3
<J>	sdu	RR_D3_NS300_NR0_A1_S3
<K>	sdu	RR_D3_NS331_NR0_A1_S3
<L>	sdu	RR_D3_NS362_NR0_A1_S3
<M>	sdu	RR_D3_NS393_NR0_A1_S3
<N>	sdu	RR_D3_NS424_NR0_A1_S3
<O>	sdu	RR_D3_NS455_NR0_A1_S3
<P>	sdu	RR_D3_NS486_NR0_A1_S3
(130)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(131)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR22_A0_S3
	sdu	RR_NR53_A0_S3
<C>	sdu	RR_NR84_A0_S3
<D>	sdu	RR_NR115_A0_S3
<E>	sdu	RR_NR146_A0_S3
<F>	sdu	RR_NR177_A0_S3
<G>	sdu	RR_NR208_A0_S3
<H>	sdu	RR_NR239_A0_S3
<I>	sdu	RR_NR270_A0_S3
<J>	sdu	RR_NR301_A0_S3
<K>	sdu	RR_NR332_A0_S3
<L>	sdu	RR_NR363_A0_S3
<M>	sdu	RR_NR394_A0_S3
<N>	sdu	RR_NR425_A0_S3

	<O>	sdu	RR_NR456_A0_S3
	<P>	sdu	RR_NR487_A0_S3
(132)		LL_DATA_CNF	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reference1	A_REF_21
(133)		GRR_READY_IND	
(134)		LL_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		ll_qos	LLREQ_QOS_DEL4_REL5
		radio_prio	LL_RADIO_PRIO_3
		reserved_data_req1	NOT_USED
		reference1	REF_22
		seg_pos	NOT_USED
		attached_counter	LLC_NO_ATTACHE
		reserved_data_req4	NOT_USED
		reserved_data_req5	NOT_USED
		sdu	LLREQ_SDU3
(135)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKEUB
		radio_prio	GRR_RADIO_PRIO_3
		cause	GRR_DTACS_DEF
		reserved_data_req	NOT_USED
	<A>	sdu	RR_D3_NS22_NR0_A1_S3
		sdu	RR_D3_NS53_NR0_A1_S3
	<C>	sdu	RR_D3_NS84_NR0_A1_S3
	<D>	sdu	RR_D3_NS115_NR0_A1_S3
	<E>	sdu	RR_D3_NS146_NR0_A1_S3
	<F>	sdu	RR_D3_NS177_NR0_A1_S3
	<G>	sdu	RR_D3_NS208_NR0_A1_S3
	<H>	sdu	RR_D3_NS239_NR0_A1_S3
	<I>	sdu	RR_D3_NS270_NR0_A1_S3
	<J>	sdu	RR_D3_NS301_NR0_A1_S3
	<K>	sdu	RR_D3_NS332_NR0_A1_S3
	<L>	sdu	RR_D3_NS363_NR0_A1_S3
	<M>	sdu	RR_D3_NS394_NR0_A1_S3
	<N>	sdu	RR_D3_NS425_NR0_A1_S3
	<O>	sdu	RR_D3_NS456_NR0_A1_S3
	<P>	sdu	RR_D3_NS487_NR0_A1_S3
(136)		LL_READY_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(137)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_NR23_A0_S3
		sdu	RR_NR54_A0_S3
	<C>	sdu	RR_NR85_A0_S3
	<D>	sdu	RR_NR116_A0_S3
	<E>	sdu	RR_NR147_A0_S3
	<F>	sdu	RR_NR178_A0_S3
	<G>	sdu	RR_NR209_A0_S3
	<H>	sdu	RR_NR240_A0_S3

	<I>	sdu	RR_NR271_A0_S3
	<J>	sdu	RR_NR302_A0_S3
	<K>	sdu	RR_NR333_A0_S3
	<L>	sdu	RR_NR364_A0_S3
	<M>	sdu	RR_NR395_A0_S3
	<N>	sdu	RR_NR426_A0_S3
	<O>	sdu	RR_NR457_A0_S3
	<P>	sdu	RR_NR488_A0_S3
(138)		LL_DATA_CNF	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reference1	A_REF_22
(139)		GRR_READY_IND	
(140)		LL_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		ll_qos	LLREQ_QOS_DEL4_REL5
		radio_prio	LL_RADIO_PRIO_3
		reserved_data_req1	NOT_USED
		reference1	REF_23
		seg_pos	NOT_USED
		attached_counter	LLC_NO_ATTACHE
		reserved_data_req4	NOT_USED
		reserved_data_req5	NOT_USED
		sdu	LLREQ_SDU3
(141)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKEUB
		radio_prio	GRR_RADIO_PRIO_3
		cause	GRR_DTACS_DEF
		reserved_data_req	NOT_USED
	<A>	sdu	RR_D3_NS23_NR0_A1_S3
		sdu	RR_D3_NS54_NR0_A1_S3
	<C>	sdu	RR_D3_NS85_NR0_A1_S3
	<D>	sdu	RR_D3_NS116_NR0_A1_S3
	<E>	sdu	RR_D3_NS147_NR0_A1_S3
	<F>	sdu	RR_D3_NS178_NR0_A1_S3
	<G>	sdu	RR_D3_NS209_NR0_A1_S3
	<H>	sdu	RR_D3_NS240_NR0_A1_S3
	<I>	sdu	RR_D3_NS271_NR0_A1_S3
	<J>	sdu	RR_D3_NS302_NR0_A1_S3
	<K>	sdu	RR_D3_NS333_NR0_A1_S3
	<L>	sdu	RR_D3_NS364_NR0_A1_S3
	<M>	sdu	RR_D3_NS395_NR0_A1_S3
	<N>	sdu	RR_D3_NS426_NR0_A1_S3
	<O>	sdu	RR_D3_NS457_NR0_A1_S3
	<P>	sdu	RR_D3_NS488_NR0_A1_S3
(142)		LL_READY_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(143)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_NR24_A0_S3
		sdu	RR_NR55_A0_S3

<C>	sdu	RR_NR86_A0_S3
<D>	sdu	RR_NR117_A0_S3
<E>	sdu	RR_NR148_A0_S3
<F>	sdu	RR_NR179_A0_S3
<G>	sdu	RR_NR210_A0_S3
<H>	sdu	RR_NR241_A0_S3
<I>	sdu	RR_NR272_A0_S3
<J>	sdu	RR_NR303_A0_S3
<K>	sdu	RR_NR334_A0_S3
<L>	sdu	RR_NR365_A0_S3
<M>	sdu	RR_NR396_A0_S3
<N>	sdu	RR_NR427_A0_S3
<O>	sdu	RR_NR458_A0_S3
<P>	sdu	RR_NR489_A0_S3
(144)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_23
(145)	GRR_READY_IND	
(146)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_24
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(147)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS24_NR0_A1_S3
	sdu	RR_D3_NS55_NR0_A1_S3
<C>	sdu	RR_D3_NS86_NR0_A1_S3
<D>	sdu	RR_D3_NS117_NR0_A1_S3
<E>	sdu	RR_D3_NS148_NR0_A1_S3
<F>	sdu	RR_D3_NS179_NR0_A1_S3
<G>	sdu	RR_D3_NS210_NR0_A1_S3
<H>	sdu	RR_D3_NS241_NR0_A1_S3
<I>	sdu	RR_D3_NS272_NR0_A1_S3
<J>	sdu	RR_D3_NS303_NR0_A1_S3
<K>	sdu	RR_D3_NS334_NR0_A1_S3
<L>	sdu	RR_D3_NS365_NR0_A1_S3
<M>	sdu	RR_D3_NS396_NR0_A1_S3
<N>	sdu	RR_D3_NS427_NR0_A1_S3
<O>	sdu	RR_D3_NS458_NR0_A1_S3
<P>	sdu	RR_D3_NS489_NR0_A1_S3

(148)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(149)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR25_A0_S3
	sdu	RR_NR56_A0_S3
<C>	sdu	RR_NR87_A0_S3
<D>	sdu	RR_NR118_A0_S3
<E>	sdu	RR_NR149_A0_S3
<F>	sdu	RR_NR180_A0_S3
<G>	sdu	RR_NR211_A0_S3
<H>	sdu	RR_NR242_A0_S3
<I>	sdu	RR_NR273_A0_S3
<J>	sdu	RR_NR304_A0_S3
<K>	sdu	RR_NR335_A0_S3
<L>	sdu	RR_NR366_A0_S3
<M>	sdu	RR_NR397_A0_S3
<N>	sdu	RR_NR428_A0_S3
<O>	sdu	RR_NR459_A0_S3
<P>	sdu	RR_NR490_A0_S3
(150)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_24
(151)	GRR_READY_IND	
(152)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_25
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(153)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS25_NR0_A1_S3
	sdu	RR_D3_NS56_NR0_A1_S3
<C>	sdu	RR_D3_NS87_NR0_A1_S3
<D>	sdu	RR_D3_NS118_NR0_A1_S3
<E>	sdu	RR_D3_NS149_NR0_A1_S3
<F>	sdu	RR_D3_NS180_NR0_A1_S3
<G>	sdu	RR_D3_NS211_NR0_A1_S3
<H>	sdu	RR_D3_NS242_NR0_A1_S3
<I>	sdu	RR_D3_NS273_NR0_A1_S3
<J>	sdu	RR_D3_NS304_NR0_A1_S3

	<K>	sdu	RR_D3_NS335_NR0_A1_S3
	<L>	sdu	RR_D3_NS366_NR0_A1_S3
	<M>	sdu	RR_D3_NS397_NR0_A1_S3
	<N>	sdu	RR_D3_NS428_NR0_A1_S3
	<O>	sdu	RR_D3_NS459_NR0_A1_S3
	<P>	sdu	RR_D3_NS490_NR0_A1_S3
(154)		LL_READY_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(155)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_NR26_A0_S3
		sdu	RR_NR57_A0_S3
	<C>	sdu	RR_NR88_A0_S3
	<D>	sdu	RR_NR119_A0_S3
	<E>	sdu	RR_NR150_A0_S3
	<F>	sdu	RR_NR181_A0_S3
	<G>	sdu	RR_NR212_A0_S3
	<H>	sdu	RR_NR243_A0_S3
	<I>	sdu	RR_NR274_A0_S3
	<J>	sdu	RR_NR305_A0_S3
	<K>	sdu	RR_NR336_A0_S3
	<L>	sdu	RR_NR367_A0_S3
	<M>	sdu	RR_NR398_A0_S3
	<N>	sdu	RR_NR429_A0_S3
	<O>	sdu	RR_NR460_A0_S3
	<P>	sdu	RR_NR491_A0_S3
(156)		LL_DATA_CNF	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reference1	A_REF_25
(157)		GRR_READY_IND	
(158)		LL_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		ll_qos	LLREQ_QOS_DEL4_REL5
		radio_prio	LL_RADIO_PRIO_3
		reserved_data_req1	NOT_USED
		reference1	REF_26
		seg_pos	NOT_USED
		attached_counter	LLC_NO_ATTACHE
		reserved_data_req4	NOT_USED
		reserved_data_req5	NOT_USED
		sdu	LLREQ_SDU3
(159)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKEUB
		radio_prio	GRR_RADIO_PRIO_3
		cause	GRR_DTACS_DEF
		reserved_data_req	NOT_USED
	<A>	sdu	RR_D3_NS26_NR0_A1_S3
		sdu	RR_D3_NS57_NR0_A1_S3
	<C>	sdu	RR_D3_NS88_NR0_A1_S3
	<D>	sdu	RR_D3_NS119_NR0_A1_S3

<E>	sdu	RR_D3_NS150_NR0_A1_S3
<F>	sdu	RR_D3_NS181_NR0_A1_S3
<G>	sdu	RR_D3_NS212_NR0_A1_S3
<H>	sdu	RR_D3_NS243_NR0_A1_S3
<I>	sdu	RR_D3_NS274_NR0_A1_S3
<J>	sdu	RR_D3_NS305_NR0_A1_S3
<K>	sdu	RR_D3_NS336_NR0_A1_S3
<L>	sdu	RR_D3_NS367_NR0_A1_S3
<M>	sdu	RR_D3_NS398_NR0_A1_S3
<N>	sdu	RR_D3_NS429_NR0_A1_S3
<O>	sdu	RR_D3_NS460_NR0_A1_S3
<P>	sdu	RR_D3_NS491_NR0_A1_S3
(160)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(161)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR27_A0_S3
	sdu	RR_NR58_A0_S3
<C>	sdu	RR_NR89_A0_S3
<D>	sdu	RR_NR120_A0_S3
<E>	sdu	RR_NR151_A0_S3
<F>	sdu	RR_NR182_A0_S3
<G>	sdu	RR_NR213_A0_S3
<H>	sdu	RR_NR244_A0_S3
<I>	sdu	RR_NR275_A0_S3
<J>	sdu	RR_NR306_A0_S3
<K>	sdu	RR_NR337_A0_S3
<L>	sdu	RR_NR368_A0_S3
<M>	sdu	RR_NR399_A0_S3
<N>	sdu	RR_NR430_A0_S3
<O>	sdu	RR_NR461_A0_S3
<P>	sdu	RR_NR492_A0_S3
(162)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_26
(163)	GRR_READY_IND	
(164)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_27
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(165)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3

	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS27_NR0_A1_S3
	sdu	RR_D3_NS58_NR0_A1_S3
<C>	sdu	RR_D3_NS89_NR0_A1_S3
<D>	sdu	RR_D3_NS120_NR0_A1_S3
<E>	sdu	RR_D3_NS151_NR0_A1_S3
<F>	sdu	RR_D3_NS182_NR0_A1_S3
<G>	sdu	RR_D3_NS213_NR0_A1_S3
<H>	sdu	RR_D3_NS244_NR0_A1_S3
<I>	sdu	RR_D3_NS275_NR0_A1_S3
<J>	sdu	RR_D3_NS306_NR0_A1_S3
<K>	sdu	RR_D3_NS337_NR0_A1_S3
<L>	sdu	RR_D3_NS368_NR0_A1_S3
<M>	sdu	RR_D3_NS399_NR0_A1_S3
<N>	sdu	RR_D3_NS430_NR0_A1_S3
<O>	sdu	RR_D3_NS461_NR0_A1_S3
<P>	sdu	RR_D3_NS492_NR0_A1_S3
(166)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(167)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR28_A0_S3
	sdu	RR_NR59_A0_S3
<C>	sdu	RR_NR90_A0_S3
<D>	sdu	RR_NR121_A0_S3
<E>	sdu	RR_NR152_A0_S3
<F>	sdu	RR_NR183_A0_S3
<G>	sdu	RR_NR214_A0_S3
<H>	sdu	RR_NR245_A0_S3
<I>	sdu	RR_NR276_A0_S3
<J>	sdu	RR_NR307_A0_S3
<K>	sdu	RR_NR338_A0_S3
<L>	sdu	RR_NR369_A0_S3
<M>	sdu	RR_NR400_A0_S3
<N>	sdu	RR_NR431_A0_S3
<O>	sdu	RR_NR462_A0_S3
<P>	sdu	RR_NR493_A0_S3
(168)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_27
(169)	GRR_READY_IND	
(170)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_28
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED

	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(171)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS28_NR0_A1_S3
	sdu	RR_D3_NS59_NR0_A1_S3
<C>	sdu	RR_D3_NS90_NR0_A1_S3
<D>	sdu	RR_D3_NS121_NR0_A1_S3
<E>	sdu	RR_D3_NS152_NR0_A1_S3
<F>	sdu	RR_D3_NS183_NR0_A1_S3
<G>	sdu	RR_D3_NS214_NR0_A1_S3
<H>	sdu	RR_D3_NS245_NR0_A1_S3
<I>	sdu	RR_D3_NS276_NR0_A1_S3
<J>	sdu	RR_D3_NS307_NR0_A1_S3
<K>	sdu	RR_D3_NS338_NR0_A1_S3
<L>	sdu	RR_D3_NS369_NR0_A1_S3
<M>	sdu	RR_D3_NS400_NR0_A1_S3
<N>	sdu	RR_D3_NS431_NR0_A1_S3
<O>	sdu	RR_D3_NS462_NR0_A1_S3
<P>	sdu	RR_D3_NS493_NR0_A1_S3
(172)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(173)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR29_A0_S3
	sdu	RR_NR60_A0_S3
<C>	sdu	RR_NR91_A0_S3
<D>	sdu	RR_NR122_A0_S3
<E>	sdu	RR_NR153_A0_S3
<F>	sdu	RR_NR184_A0_S3
<G>	sdu	RR_NR215_A0_S3
<H>	sdu	RR_NR246_A0_S3
<I>	sdu	RR_NR277_A0_S3
<J>	sdu	RR_NR308_A0_S3
<K>	sdu	RR_NR339_A0_S3
<L>	sdu	RR_NR370_A0_S3
<M>	sdu	RR_NR401_A0_S3
<N>	sdu	RR_NR432_A0_S3
<O>	sdu	RR_NR463_A0_S3
<P>	sdu	RR_NR494_A0_S3
(174)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_28
(175)	GRR_READY_IND	
(176)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DELE4_REL5

	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_29
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(177)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS29_NR0_A1_S3
	sdu	RR_D3_NS60_NR0_A1_S3
<C>	sdu	RR_D3_NS91_NR0_A1_S3
<D>	sdu	RR_D3_NS122_NR0_A1_S3
<E>	sdu	RR_D3_NS153_NR0_A1_S3
<F>	sdu	RR_D3_NS184_NR0_A1_S3
<G>	sdu	RR_D3_NS215_NR0_A1_S3
<H>	sdu	RR_D3_NS246_NR0_A1_S3
<I>	sdu	RR_D3_NS277_NR0_A1_S3
<J>	sdu	RR_D3_NS308_NR0_A1_S3
<K>	sdu	RR_D3_NS339_NR0_A1_S3
<L>	sdu	RR_D3_NS370_NR0_A1_S3
<M>	sdu	RR_D3_NS401_NR0_A1_S3
<N>	sdu	RR_D3_NS432_NR0_A1_S3
<O>	sdu	RR_D3_NS463_NR0_A1_S3
<P>	sdu	RR_D3_NS494_NR0_A1_S3
(178)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(179)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR30_A0_S3
	sdu	RR_NR61_A0_S3
<C>	sdu	RR_NR92_A0_S3
<D>	sdu	RR_NR123_A0_S3
<E>	sdu	RR_NR154_A0_S3
<F>	sdu	RR_NR185_A0_S3
<G>	sdu	RR_NR216_A0_S3
<H>	sdu	RR_NR247_A0_S3
<I>	sdu	RR_NR278_A0_S3
<J>	sdu	RR_NR309_A0_S3
<K>	sdu	RR_NR340_A0_S3
<L>	sdu	RR_NR371_A0_S3
<M>	sdu	RR_NR402_A0_S3
<N>	sdu	RR_NR433_A0_S3
<O>	sdu	RR_NR464_A0_S3
<P>	sdu	RR_NR495_A0_S3
(180)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_29

(181)	GRR_READY_IND	
(182)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_30
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(183)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_D3_NS30_NR0_A1_S3
	sdu	RR_D3_NS61_NR0_A1_S3
<C>	sdu	RR_D3_NS92_NR0_A1_S3
<D>	sdu	RR_D3_NS123_NR0_A1_S3
<E>	sdu	RR_D3_NS154_NR0_A1_S3
<F>	sdu	RR_D3_NS185_NR0_A1_S3
<G>	sdu	RR_D3_NS216_NR0_A1_S3
<H>	sdu	RR_D3_NS247_NR0_A1_S3
<I>	sdu	RR_D3_NS278_NR0_A1_S3
<J>	sdu	RR_D3_NS309_NR0_A1_S3
<K>	sdu	RR_D3_NS340_NR0_A1_S3
<L>	sdu	RR_D3_NS371_NR0_A1_S3
<M>	sdu	RR_D3_NS402_NR0_A1_S3
<N>	sdu	RR_D3_NS433_NR0_A1_S3
<O>	sdu	RR_D3_NS464_NR0_A1_S3
<P>	sdu	RR_D3_NS495_NR0_A1_S3
(184)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(185)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_NR31_A0_S3
	sdu	RR_NR62_A0_S3
<C>	sdu	RR_NR93_A0_S3
<D>	sdu	RR_NR124_A0_S3
<E>	sdu	RR_NR155_A0_S3
<F>	sdu	RR_NR186_A0_S3
<G>	sdu	RR_NR217_A0_S3
<H>	sdu	RR_NR248_A0_S3
<I>	sdu	RR_NR279_A0_S3
<J>	sdu	RR_NR310_A0_S3
<K>	sdu	RR_NR341_A0_S3
<L>	sdu	RR_NR372_A0_S3
<M>	sdu	RR_NR403_A0_S3
<N>	sdu	RR_NR434_A0_S3

<O>	sdu	RR_NR465_A0_S3
<P>	sdu	RR_NR496_A0_S3
(186)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	reference1	A_REF_30

History:

19-Mar-2001	GS	Initial
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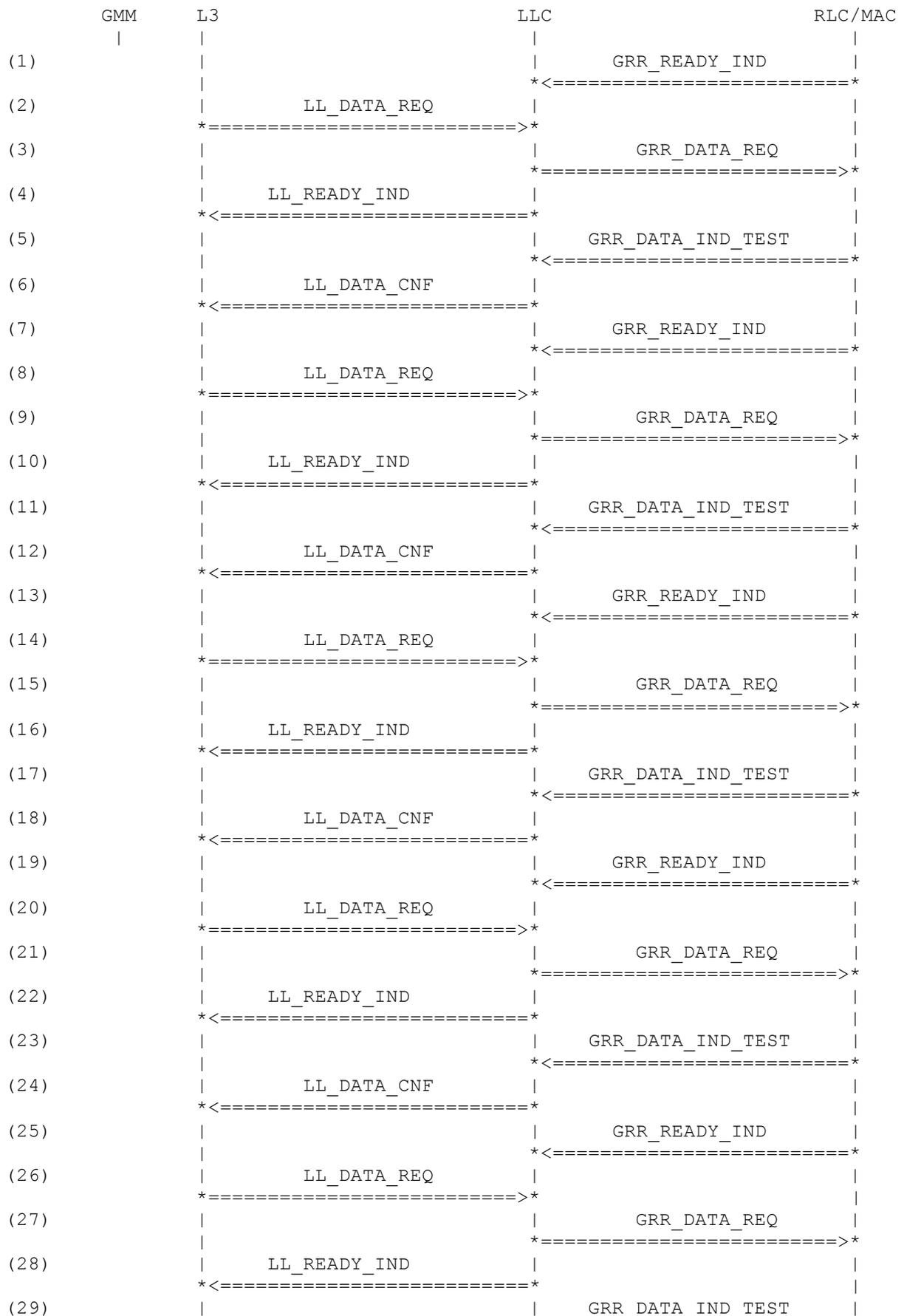
3.15.24 LLC741: ABM - Receive 18 PDUs from L3 and forward them to GRR; N(S) wraps

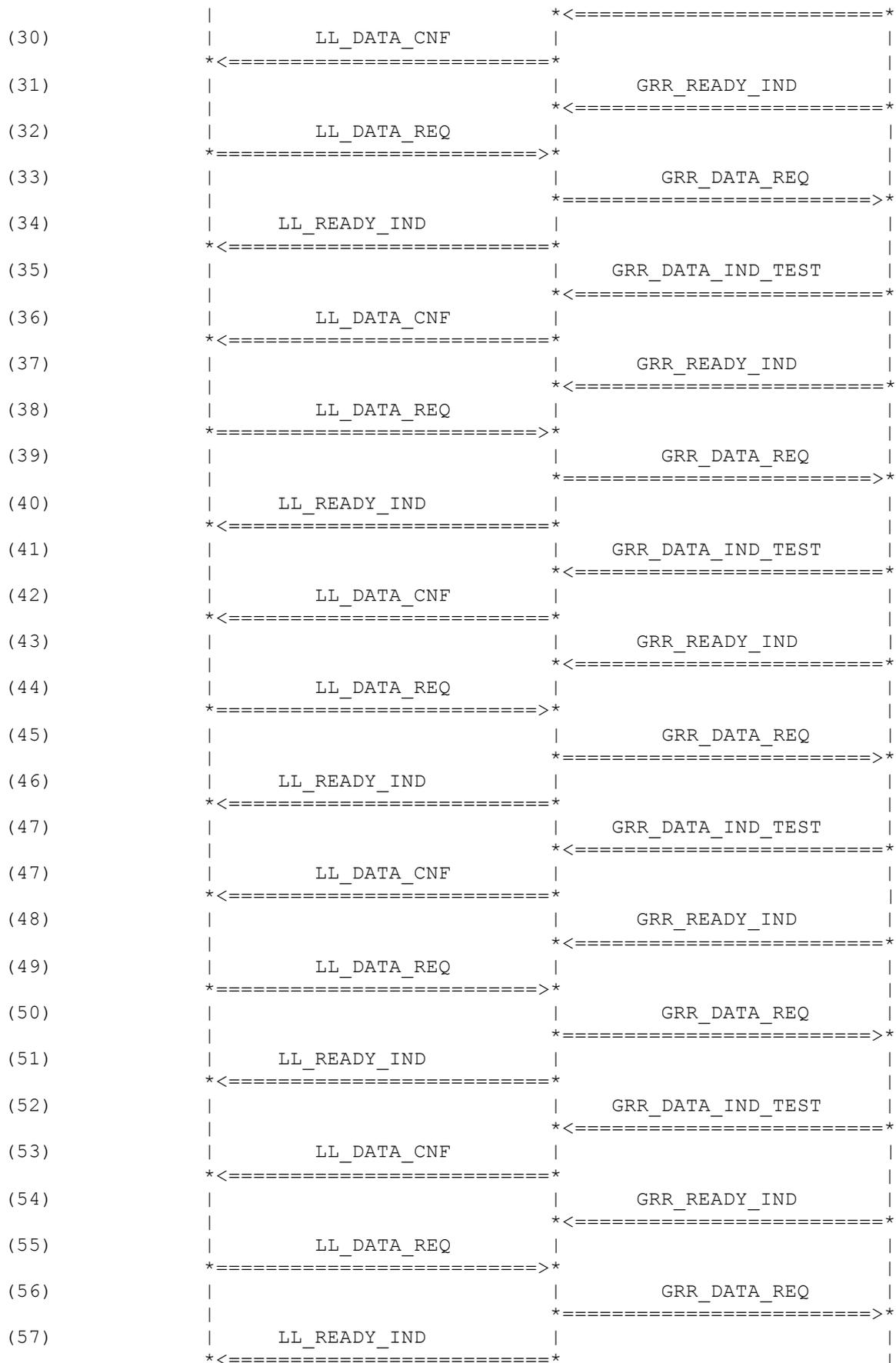
Description:

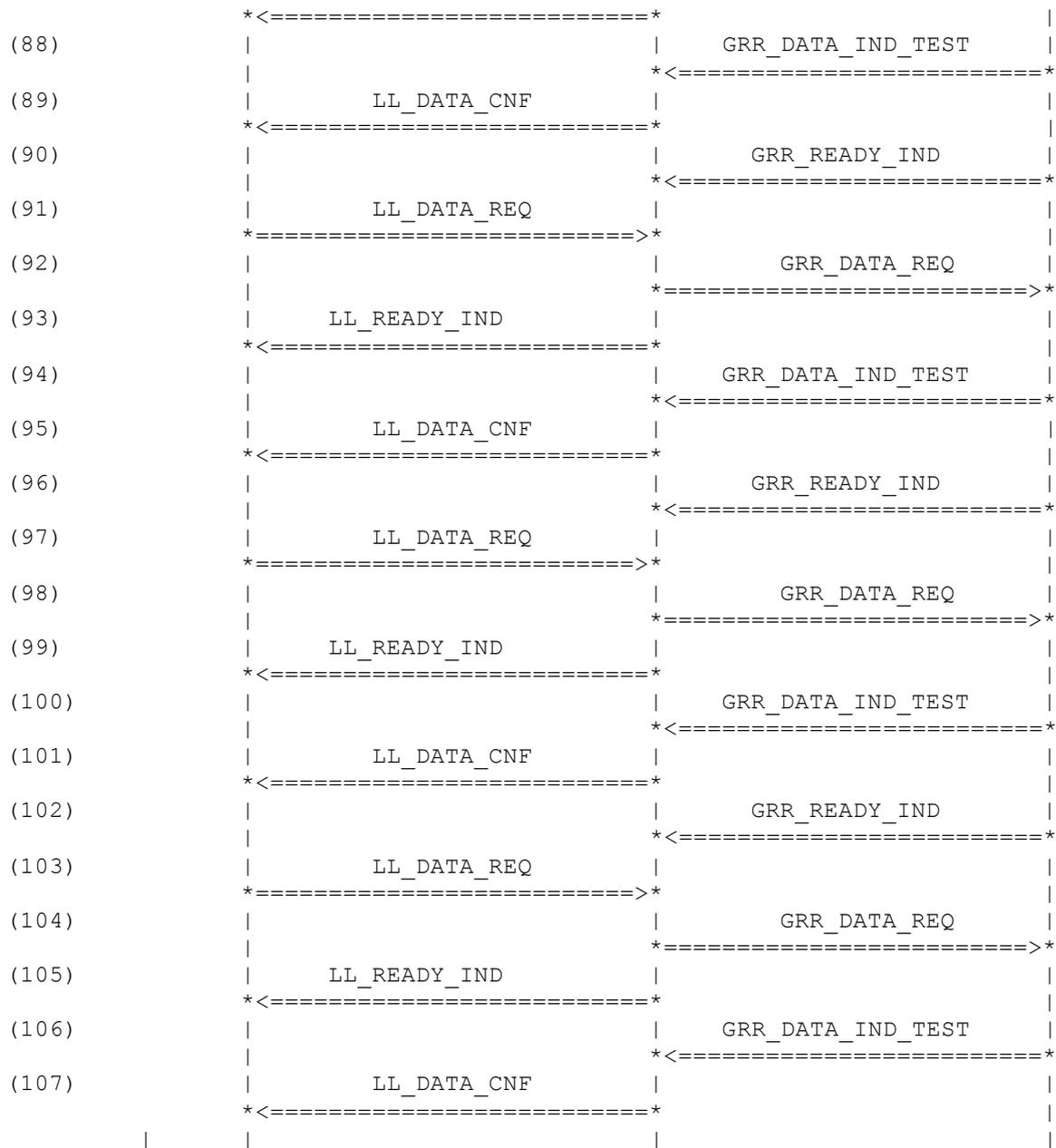
LLC receives 18 PDU's from L3. The frames are forwarded to the RLC/MAC layer. After 16 I-frames N(S) wraps from 511 to 0.

Preamble:

LLC740P







Parametrization:

Primitive	Parameter	Value
(187)	GRR_READY_IND	
(188)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_31
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED

	reserved_data_req5 sdu	NOT_USED LLREQ_SDU3
(189)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS496_NR0_A1_S3
(190)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(191)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR497_A0_S3
(192)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_31
(193)	GRR_READY_IND	
(194)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_0 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(195)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS497_NR0_A1_S3
(196)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(197)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR498_A0_S3
(198)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_0
(199)	GRR_READY_IND	

(200)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_1 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(201)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKE GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS498_NR0_A1_S3
(202)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(203)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR499_A0_S3
(204)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_1
(205)	GRR_READY_IND	
(206)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_2 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(207)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKE GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS499_NR0_A1_S3
(208)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1

(209)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR500_A0_S3
(210)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_2
(211)	GRR_READY_IND	
(212)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_3 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(213)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS500_NR0_A1_S3
(214)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(215)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR501_A0_S3
(216)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_3
(217)	GRR_READY_IND	
(218)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_4 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(219)	GRR_DATA_REQ sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS501_NR0_A1_S3
(220)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(221)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR502_A0_S3
(222)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_4
(223)	GRR_READY_IND	
(224)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_5
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(225)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS502_NR0_A1_S3
(226)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(227)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR503_A0_S3
(228)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_5
(229)	GRR_READY_IND	
(230)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5

	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_6
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(231)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS503_NR0_A1_S3
(232)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(233)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR504_A0_S3
(234)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_6
(235)	GRR_READY_IND	
(236)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_7
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(237)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS504_NR0_A1_S3
(238)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(239)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR505_A0_S3

(240)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_7
(241)	GRR_READY_IND	
(242)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_8 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(243)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS505_NR0_A1_S3
(244)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(245)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR506_A0_S3
(246)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_8
(247)	GRR_READY_IND	
(248)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_9 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(249)	GRR_DATA_REQ sapi tlli grr_qos radio_prio	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3

	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS506_NR0_A1_S3
(250)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(251)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR507_A0_S3
(252)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_9
(253)	GRR_READY_IND	
(254)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_10
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(255)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS507_NR0_A1_S3
(256)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(257)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR508_A0_S3
(258)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_10
(259)	GRR_READY_IND	
(260)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_11

	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(261)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS508_NR0_A1_S3
(262)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(263)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR509_A0_S3
(264)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_11
(265)	GRR_READY_IND	
(266)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_12
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(267)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS509_NR0_A1_S3
(268)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(269)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR510_A0_S3
(270)	LL_DATA_CNF	
	sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	reference1	A_REF_12
(271)	GRR_READY_IND	
(272)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_13
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(273)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS510_NR0_A1_S3
(274)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(275)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR511_A0_S3
(276)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_13
(277)	GRR_READY_IND	
(278)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_14
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(279)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF

	reserved_data_req sdu	NOT_USED RR_D3_NS511_NR0_A1_S3
(280)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(281)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR0_A0_S3
(282)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_14
(283)	GRR_READY_IND	
(284)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_15 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(285)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKE GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS0_NR0_A1_S3
(286)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(287)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR1_A0_S3
(288)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_15
(289)	GRR_READY_IND	
(290)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_16 NOT_USED

	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(291)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRI0_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS1_NR0_A1_S3
(292)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(293)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR2_A0_S3
(294)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_16

History:

21-Mar-2001	GS	Initial
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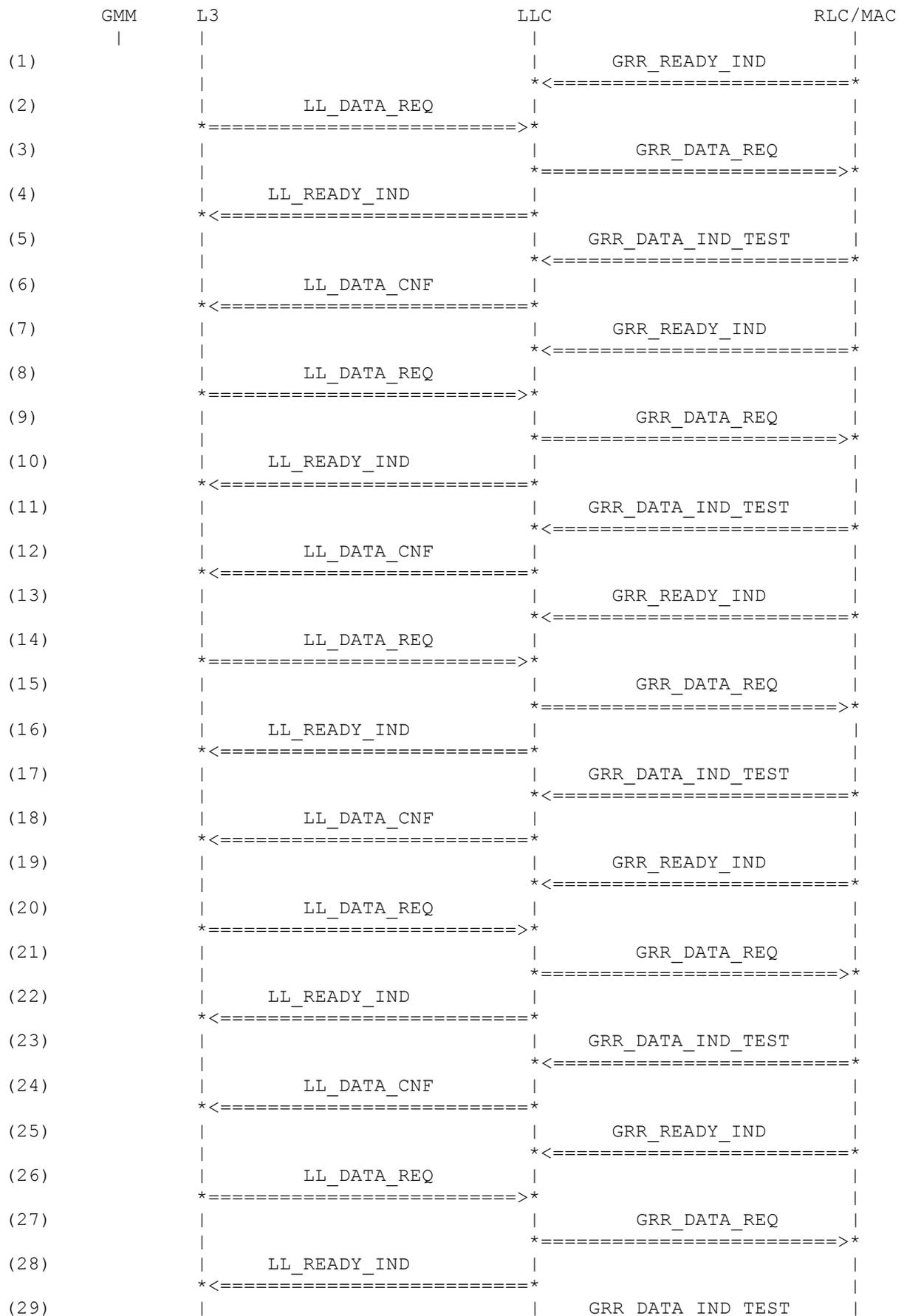
3.15.25 LLC742: ABM - Receive 8 PDUs from L3 and forward them to GRR

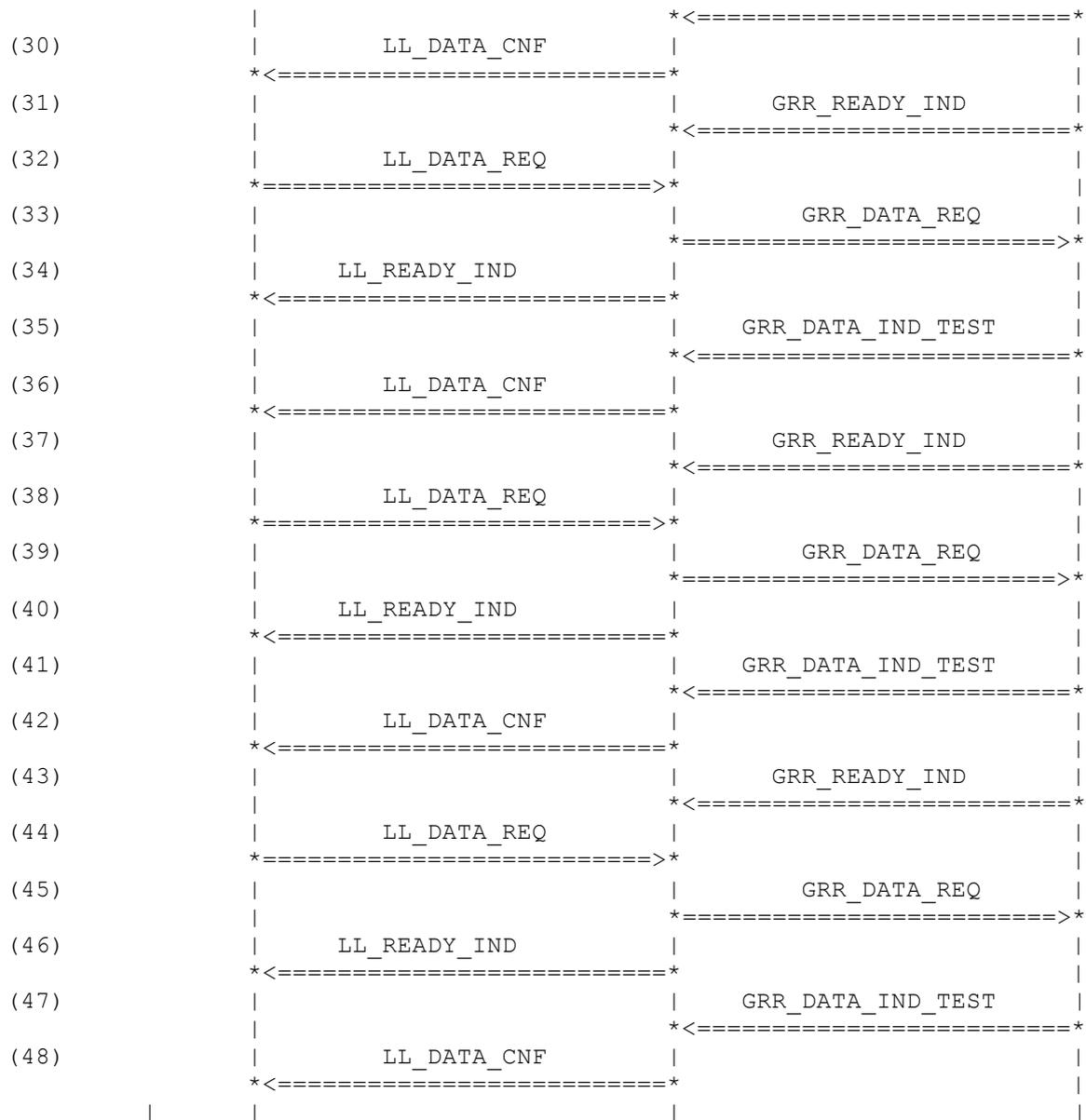
Description:

LLC receives 8 PDU's from L3. The frames are forwarded to the RLC/MAC layer.

Preamble:

LLC740P





Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) LL_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_31
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3

(3) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEBUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS496_NR0_A1_S3
(4) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(5) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 RR_NR497_A0_S3
(6) LL_DATA_CNF	sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_31
(7) GRR_READY_IND		
(8) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DELE4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_0 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(9) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEBUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS497_NR0_A1_S3
(10)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(11)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR498_A0_S3
(12)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_0
(13)	GRR_READY_IND	
(14)	LL_DATA_REQ sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_1
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(15)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS498_NR0_A1_S3
(16)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(17)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR499_A0_S3
(18)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_1
(19)	GRR_READY_IND	
(20)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_2
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(21)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS499_NR0_A1_S3
(22)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

(23)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR500_A0_S3
(24)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_2
(25)	GRR_READY_IND	
(26)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_3 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(27)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS500_NR0_A1_S3
(28)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(29)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR501_A0_S3
(30)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_3
(31)	GRR_READY_IND	
(32)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_4 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(33)	GRR_DATA_REQ sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS501_NR0_A1_S3
(34)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(35)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR502_A0_S3
(36)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_4
(37)	GRR_READY_IND	
(38)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_5
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(39)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS502_NR0_A1_S3
(40)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(41)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR503_A0_S3
(42)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_5
(43)	GRR_READY_IND	
(44)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5

	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_6
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(45)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS503_NR0_A1_S3
(46)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(47)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR504_A0_S3
(48)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_6

History:

21-Mar-2001	GS	Initial
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3.15.26 LLC743: ABM - Receive 8 PDUs from L3; then they are forward to GRR; N(S) wraps

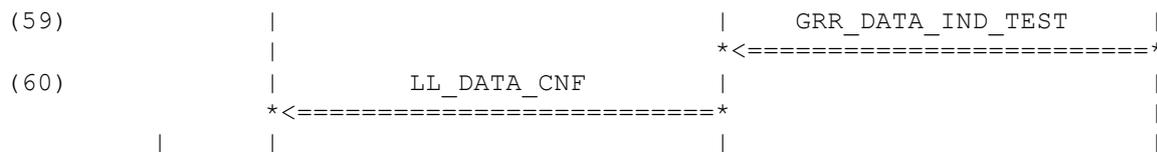
Description:

LLC receives 8 PDU's from L3. After complete receiving the frames are forwarded to the RLC/MAC layer. N(S) wraps from 511 to 0.

Preamble:

LLC742

	GMM	L3	LLC	RLC/MAC
(1)		LL_DATA_REQ		
		=====>		
(2)		LL_READY_IND		
		<=====		
(3)		LL_DATA_REQ		
		=====>		
(4)		LL_READY_IND		
		<=====		
(5)		LL_DATA_REQ		
		=====>		
(6)		LL_READY_IND		
		<=====		
(7)		LL_DATA_REQ		
		=====>		
(8)		LL_READY_IND		
		<=====		
(9)		LL_DATA_REQ		
		=====>		
(10)		LL_READY_IND		
		<=====		
(11)		LL_DATA_REQ		
		=====>		
(12)		LL_READY_IND		
		<=====		
(13)		LL_DATA_REQ		
		=====>		
(14)		LL_READY_IND		
		<=====		
(15)		LL_DATA_REQ		
		=====>		
(16)		LL_READY_IND		
		<=====		
(17)		LL_DATA_REQ		
		=====>		
(18)		LL_READY_IND		
		<=====		
(19)		LL_DATA_REQ		
		=====>		
(20)		LL_READY_IND		
		<=====		
(21)			GRR_READY_IND	
			<=====	
(22)			GRR_DATA_REQ	
			=====>	
(23)			GRR_DATA_IND_TEST	
			<=====	
(24)		LL_DATA_CNF		
		<=====		
(25)			GRR_READY_IND	
			<=====	
(26)			GRR_DATA_REQ	
			=====>	
(27)			GRR_DATA_IND_TEST	
			<=====	
(28)		LL_DATA_CNF		
		<=====		
(29)			GRR_READY_IND	

**Parametrization:**

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_0
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(2) LL_READY_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(3) LL_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_1
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(4) LL_READY_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(5) LL_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_2
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(6) LL_READY_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

(7) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_3 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(8) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(9) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_4 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(10)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(11)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_5 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(12)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(13)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_6 NOT_USED LLC_NO_ATTACHE NOT_USED

	reserved_data_req5 sdu	NOT_USED LLREQ_SDU3
(14)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(15)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_7 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(16)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(17)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_8 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(18)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(19)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_9 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(20)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(21)	GRR_READY_IND	
(22)	GRR_DATA_REQ sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS504_NR0_A1_S3
(23)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR505_A0_S3
(24)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_0
(25)	GRR_READY_IND	
(26)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS505_NR0_A0_S3
(27)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR506_A0_S3
(28)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_1
(29)	GRR_READY_IND	
(30)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS506_NR0_A0_S3
(31)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR507_A0_S3
(32)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_2
(33)	GRR_READY_IND	
(34)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF

	reserved_data_req	NOT_USED
	sdu	RR_D3_NS507_NR0_A0_S3
(35)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR508_A0_S3
(36)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_3
(37)	GRR_READY_IND	
(38)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS508_NR0_A0_S3
(39)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR509_A0_S3
(40)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_4
(41)	GRR_READY_IND	
(42)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS509_NR0_A0_S3
(43)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR510_A0_S3
(44)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_5
(45)	GRR_READY_IND	
(46)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS510_NR0_A0_S3

(47)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR511_A0_S3
(48)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_6
(49)	GRR_READY_IND	
(50)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS511_NR0_A0_S3
(51)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR0_A0_S3
(52)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_7
(53)	GRR_READY_IND	
(54)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS0_NR0_A0_S3
(55)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR1_A0_S3
(56)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_8
(57)	GRR_READY_IND	
(58)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS1_NR0_A1_S3
(59)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR2_A0_S3

(60)

LL_DATA_CNF
sapi
tli
reference1

LL_SAPI_3
TLLI_LOCAL_1
A_REF_9

History:

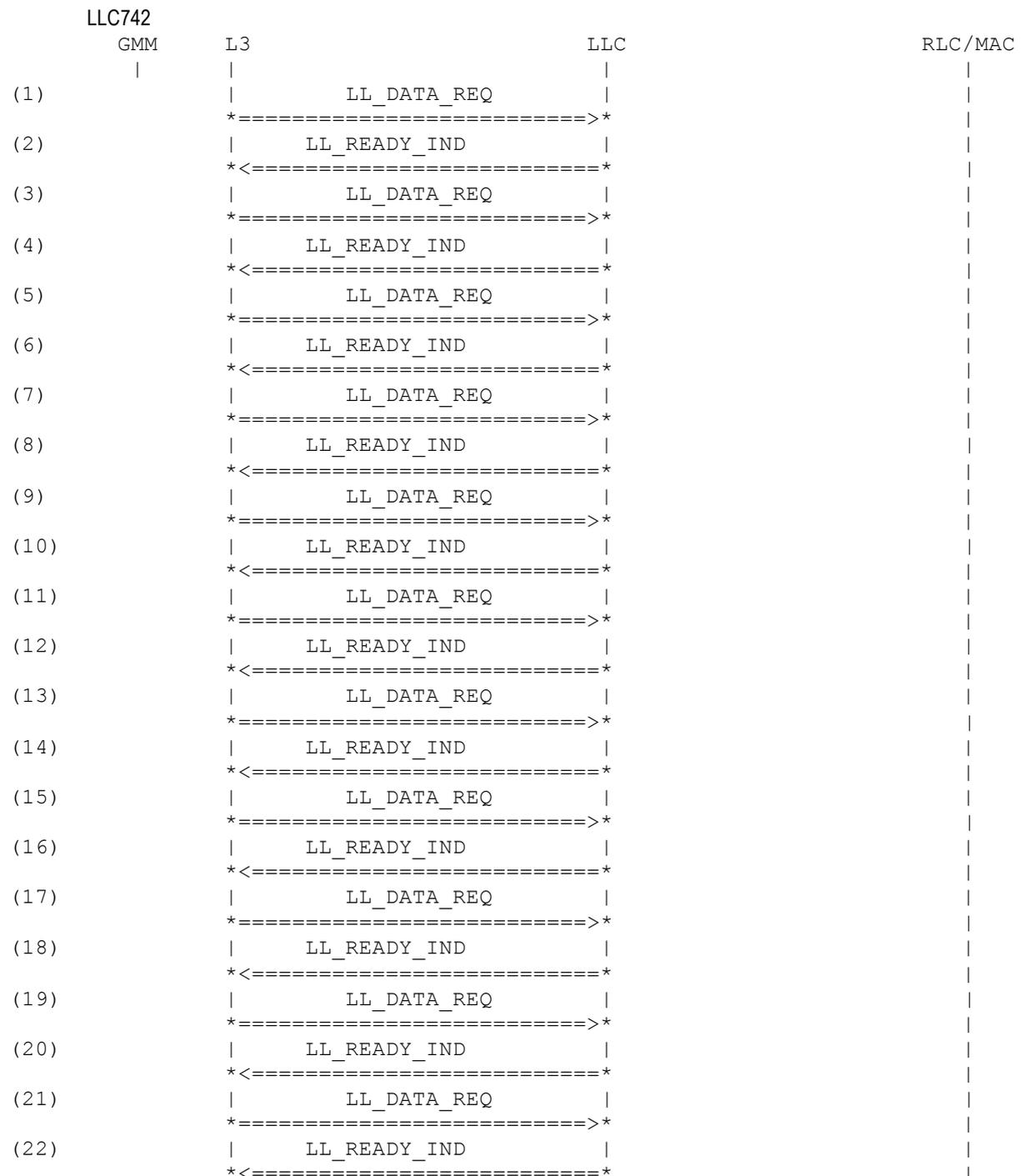
21-Mar-2001 GS Initial

3.15.27 LLC744: ABM - Receive 16 PDUs from L3 and forward them to GRR; last N(S) = 511

Description:

LLC receives 16 PDU's from L3. The frames are forwarded to the RLC/MAC layer. After sending N(S) = 511. This test is designed to be a preamble. The next frame number is 0, which is the same as if we were just established.

Preamble:



(23)		LL_DATA_REQ		
		=====	>	
(24)		LL_READY_IND		
		*<=====	*	
(25)		LL_DATA_REQ		
		=====	>	
(26)		LL_READY_IND		
		*<=====	*	
(27)		LL_DATA_REQ		
		=====	>	
(28)		LL_READY_IND		
		*<=====	*	
(29)		LL_DATA_REQ		
		=====	>	
(30)		LL_READY_IND		
		*<=====	*	
(31)		LL_DATA_REQ		
		=====	>	
(32)		LL_READY_IND		
		*<=====	*	
(33)		LL_DATA_REQ		
		=====	>	
(34)		LL_READY_IND		
		*<=====	*	
(35)		LL_DATA_REQ		
		=====	>	
(36)		LL_READY_IND		
		*<=====	*	
(37)		LL_DATA_REQ		
		=====	>	
(38)		LL_READY_IND		
		*<=====	*	
(39)		LL_DATA_REQ		
		=====	>	
(40)		LL_READY_IND		
		*<=====	*	
(41)				GRR_READY_IND
				*<=====
(42)				GRR_DATA_REQ
				*=====
(43)				GRR_READY_IND
				*<=====
(44)				GRR_DATA_REQ
				*=====
(45)				GRR_READY_IND
				*<=====
(46)				GRR_DATA_REQ
				*=====
(47)				GRR_READY_IND
				*<=====
(48)				GRR_DATA_REQ
				*=====
(49)				GRR_READY_IND
				*<=====
(50)				GRR_DATA_REQ
				*=====
(51)				GRR_READY_IND
				*<=====
(52)				GRR_DATA_REQ

(53)			*=====>*
			GRR_READY_IND
			<=====
(54)			GRR_DATA_REQ
			=====>
(55)			GRR_READY_IND
			<=====
(56)			GRR_DATA_REQ
			=====>
(57)			GRR_READY_IND
			<=====
(58)			GRR_DATA_REQ
			=====>
(59)			GRR_READY_IND
			<=====
(60)			GRR_DATA_REQ
			=====>
(61)			GRR_READY_IND
			<=====
(62)			GRR_DATA_REQ
			=====>
(63)			GRR_READY_IND
			<=====
(64)			GRR_DATA_REQ
			=====>
(65)			GRR_READY_IND
			<=====
(66)			GRR_DATA_REQ
			=====>
(67)			GRR_READY_IND
			<=====
(68)			GRR_DATA_REQ
			=====>
(69)			GRR_READY_IND
			<=====
(70)			GRR_DATA_REQ
			=====>
(71)			GRR_READY_IND
			<=====
(72)			GRR_DATA_REQ
			=====>
(73)			GRR_READY_IND
			<=====

Parametrization:

<u>Primitive</u>	<u>Parameter</u>	<u>Value</u>
(1) LL_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_0
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED

	reserved_data_req5 sdu	NOT_USED LLREQ_SDU3
(2) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(3) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_1 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(4) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(5) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_2 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(6) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(7) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_3 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(8) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(9) LL_DATA_REQ	sapi tlli ll_qos	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5

	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_4
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(10)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(11)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_5
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(12)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(13)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_6
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(14)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(15)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_7
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3

(16)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(17)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_8 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(18)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(19)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_9 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(20)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(21)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_10 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(22)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(23)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED

	reference1	REF_11
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(24)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(25)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_12
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(26)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(27)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_13
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(28)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(29)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_14
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(30)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

(31)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_15 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(32)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(33)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_16 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(34)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(35)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_17 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(36)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(37)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_18 NOT_USED LLC_NO_ATTACHE NOT_USED

	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(38)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(39)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_19
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(40)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(41)	GRR_READY_IND	
(42)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS504_NR0_A1_S3
(43)	GRR_READY_IND	
(44)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS505_NR0_A0_S3
(45)	GRR_READY_IND	
(46)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS506_NR0_A0_S3
(47)	GRR_READY_IND	
(48)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB

	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS507_NR0_A0_S3
(49)	GRR_READY_IND	
(50)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS508_NR0_A0_S3
(51)	GRR_READY_IND	
(52)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS509_NR0_A0_S3
(53)	GRR_READY_IND	
(54)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS510_NR0_A0_S3
(55)	GRR_READY_IND	
(56)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS511_NR0_A0_S3
(57)	GRR_READY_IND	
(58)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS0_NR0_A0_S3
(59)	GRR_READY_IND	
(60)	GRR_DATA_REQ	
	sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS1_NR0_A0_S3
(61)	GRR_READY_IND	
(62)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS2_NR0_A0_S3
(63)	GRR_READY_IND	
(64)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS3_NR0_A0_S3
(65)	GRR_READY_IND	
(66)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS4_NR0_A0_S3
(67)	GRR_READY_IND	
(68)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS5_NR0_A0_S3
(69)	GRR_READY_IND	
(70)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS6_NR0_A0_S3
(71)	GRR_READY_IND	

(72)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKESSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS7_NR0_A1_S3
(73)	GRR_READY_IND	

History:

22-Mar-2001	GS	Initial
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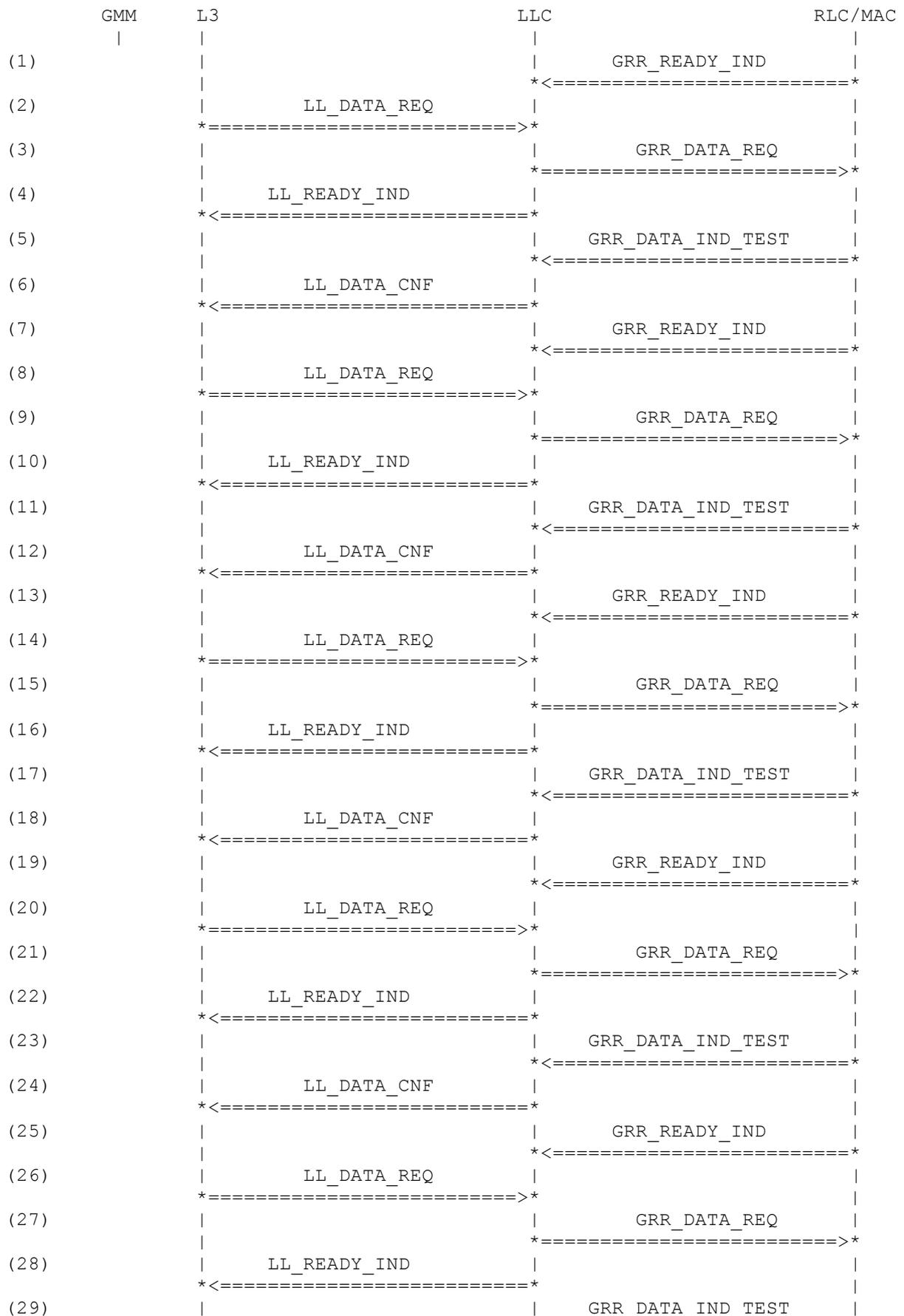
3.15.28 LLC745: ABM - Receive 16 PDUs from L3 and forward them to GRR; last N(S) = 511

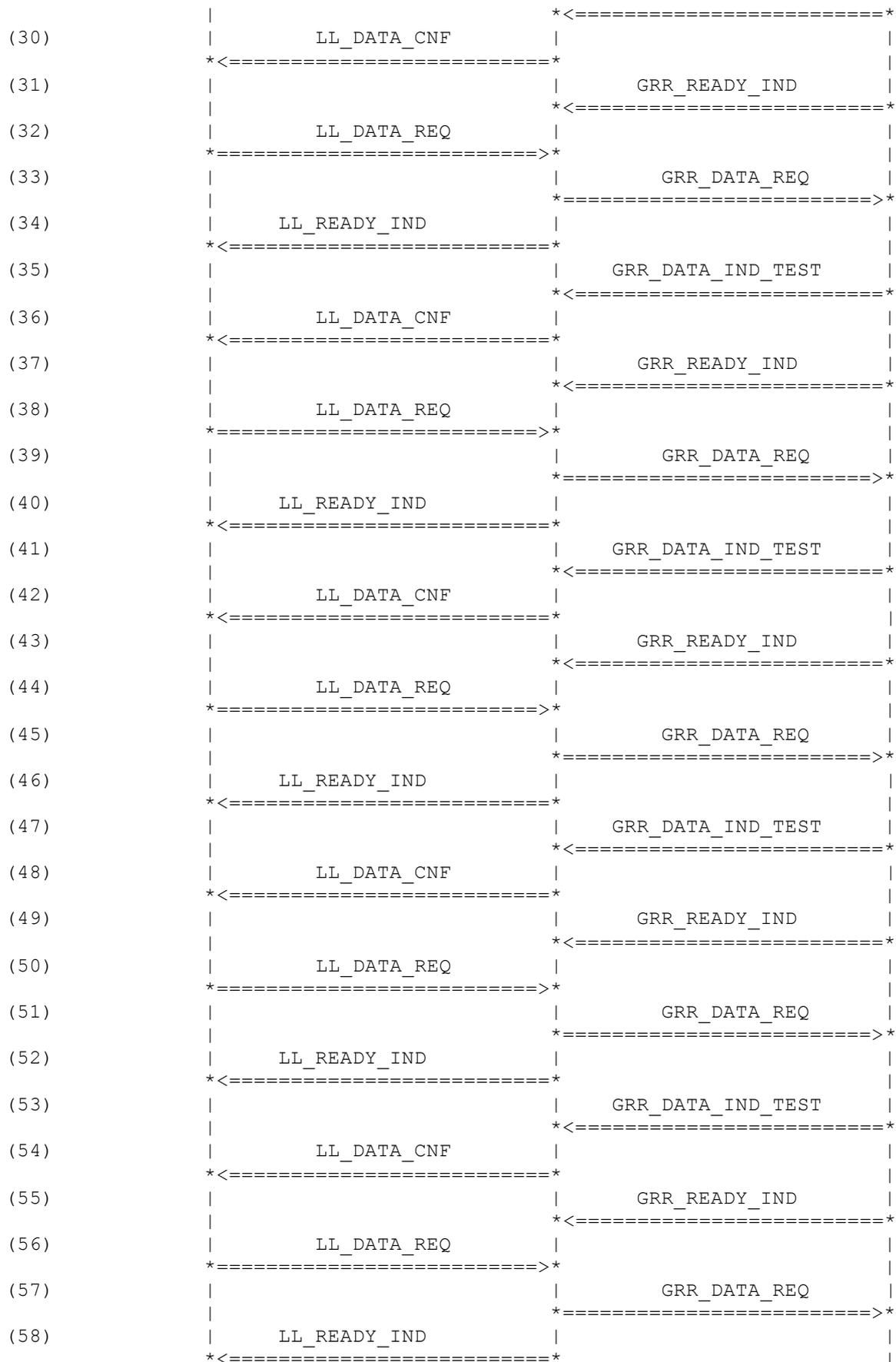
Description:

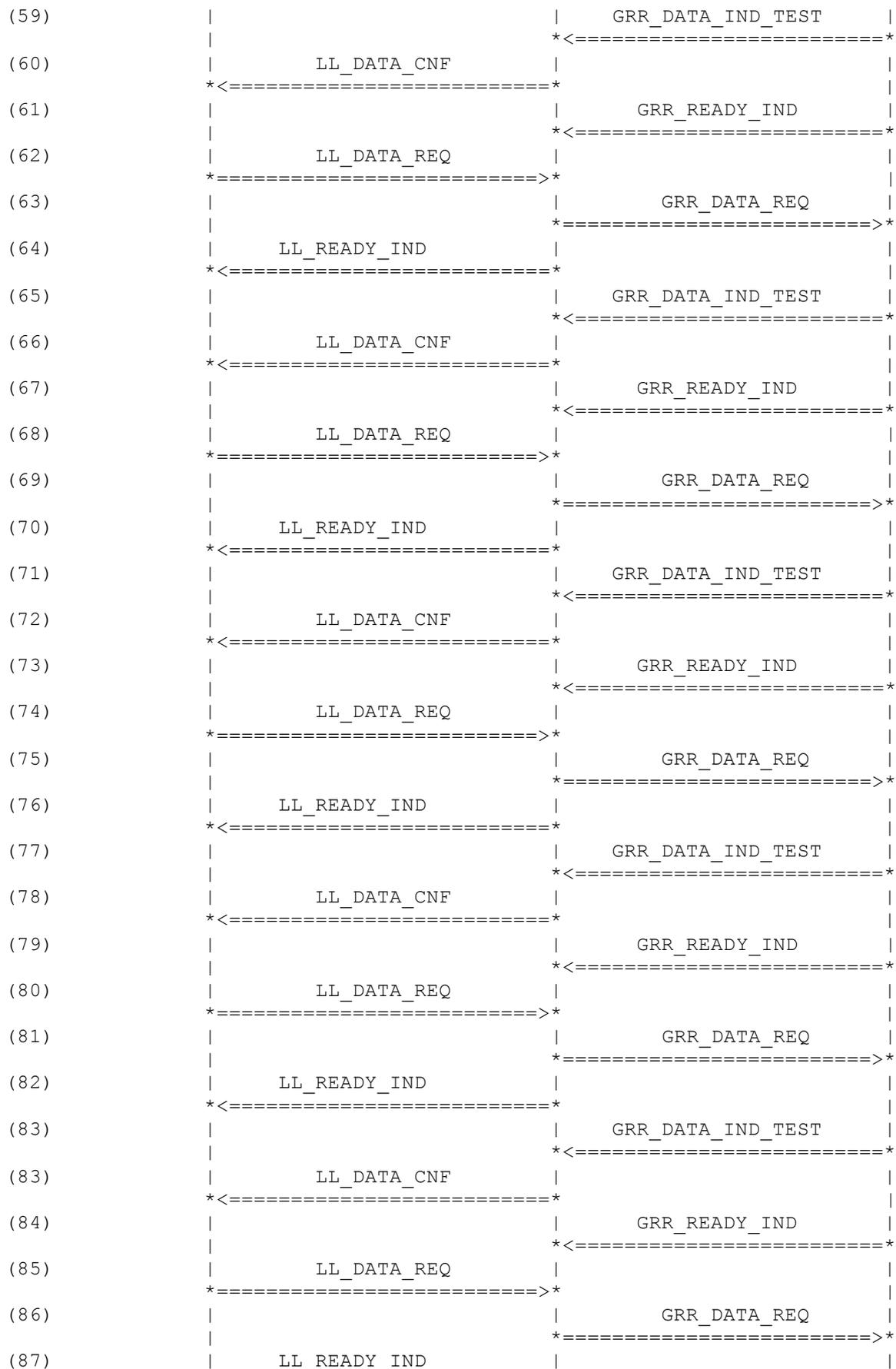
LLC receives 16 PDU's from L3. The frames are forwarded to the RLC/MAC layer. After sending N(S) = 511. This test is designed to be a preamble. The next frame number is 0, which is the same as if we were just established.

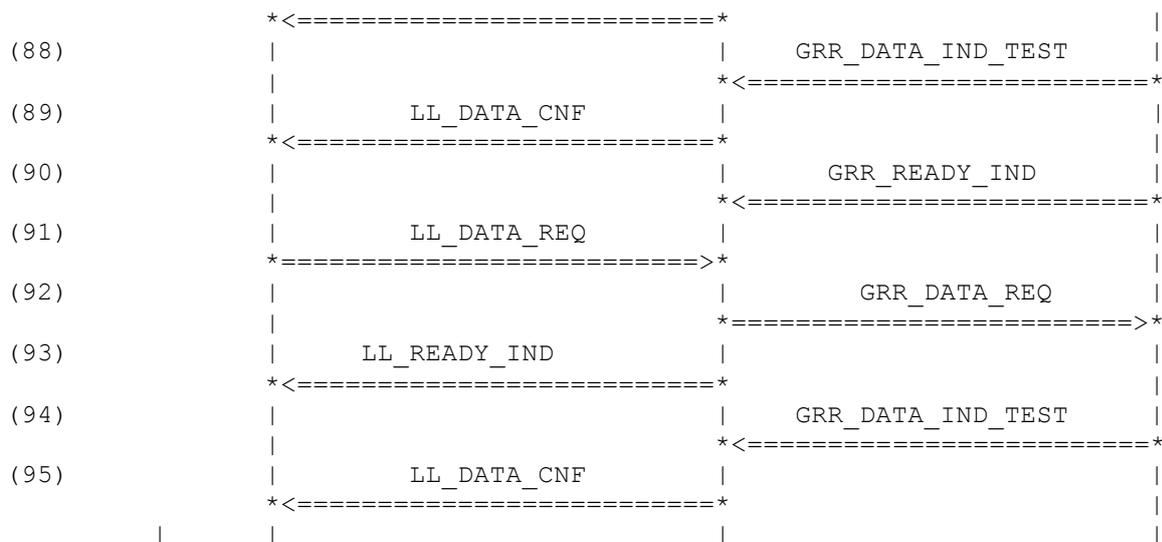
Preamble:

LLC740P







**Parametrization:**

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) LL_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_31
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(3) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS496_NR0_A1_S3
(4) LL_READY_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(5) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	RR_NR497_A0_S3
(6) LL_DATA_CNF	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_31
(7) GRR_READY_IND		

(8)	LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_0 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(9)	GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS497_NR0_A1_S3
(10)	LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(11)	GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 RR_NR498_A0_S3
(12)	LL_DATA_CNF	sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_0
(13)	GRR_READY_IND		
(14)	LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_1 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(15)	GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS498_NR0_A1_S3
(16)	LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1

(17)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR499_A0_S3
(18)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_1
(19)	GRR_READY_IND	
(20)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_2 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(21)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS499_NR0_A1_S3
(22)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(23)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR500_A0_S3
(24)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_2
(25)	GRR_READY_IND	
(26)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_3 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(27)	GRR_DATA_REQ sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS500_NR0_A1_S3
(28)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(29)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR501_A0_S3
(30)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_3
(31)	GRR_READY_IND	
(32)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_4
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(33)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS501_NR0_A1_S3
(34)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(35)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR502_A0_S3
(36)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_4
(37)	GRR_READY_IND	
(38)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5

	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_5
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(39)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS502_NR0_A1_S3
(40)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(41)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR503_A0_S3
(42)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_5
(43)	GRR_READY_IND	
(44)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_6
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(45)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS503_NR0_A1_S3
(46)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(47)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR504_A0_S3

(48)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_6
(49)	GRR_READY_IND	
(50)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_7 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(51)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED RR_D3_NS504_NR0_A1_S3
(52)	LL_READY_IND sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(53)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_NR505_A0_S3
(54)	LL_DATA_CNF sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 A_REF_7
(55)	GRR_READY_IND	
(56)	LL_DATA_REQ sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED REF_8 NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU3
(57)	GRR_DATA_REQ sapi tlli grr_qos radio_prio	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_3

	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS505_NR0_A1_S3
(58)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(59)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR506_A0_S3
(60)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_8
(61)	GRR_READY_IND	
(62)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_9
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(63)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS506_NR0_A1_S3
(64)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(65)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR507_A0_S3
(66)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_9
(67)	GRR_READY_IND	
(68)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_10

	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(69)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS507_NR0_A1_S3
(70)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(71)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR508_A0_S3
(72)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_10
(73)	GRR_READY_IND	
(74)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_11
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(75)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS508_NR0_A1_S3
(76)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(77)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR509_A0_S3
(78)	LL_DATA_CNF	
	sapi	LL_SAPI_3

	‡li	TLLI_LOCAL_1
	reference1	A_REF_11
(79)	GRR_READY_IND	
(80)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	‡li	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_12
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(81)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	‡li	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS509_NR0_A1_S3
(82)	LL_READY_IND	
	sapi	LL_SAPI_3
	‡li	TLLI_LOCAL_1
(83)	GRR_DATA_IND_TEST	
	‡li	TLLI_LOCAL_1
	sdu	RR_NR510_A0_S3
(84)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	‡li	TLLI_LOCAL_1
	reference1	A_REF_12
(85)	GRR_READY_IND	
(86)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	‡li	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_13
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(87)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	‡li	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF

	reserved_data_req	NOT_USED
	sdu	RR_D3_NS510_NR0_A1_S3
(88)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(89)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR511_A0_S3
(90)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_13
(91)	GRR_READY_IND	
(92)	LL_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	REF_14
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU3
(93)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_D3_NS511_NR0_A1_S3
(94)	LL_READY_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(95)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_NR0_A0_S3
(96)	LL_DATA_CNF	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	A_REF_14

History:

22-Mar-2001

GS

Initial

3.16 Acknowledged receipt of frames (LLC800-LC899)

3.16.1 LLC801: ABM - Receive of a full window of 16 frames; RR send; LL is not ready

Description:

LLC receives acknowledged data frames. After the complete window (16 frames for SAPI 3) has been received with the A bit set to 1 in last frame, an RR frame must be sent by LLC.

Preamble:

LLC400A					
	GMM	L3	LLC		RLC/MAC
(1)				GRR_READY_IND	
				<=====	
(2)				GRR_DATA_IND_TEST	
				<=====	
(3)				GRR_DATA_IND_TEST	
				<=====	
(4)				GRR_DATA_IND_TEST	
				<=====	
(5)				GRR_DATA_IND_TEST	
				<=====	
(6)				GRR_DATA_IND_TEST	
				<=====	
(7)				GRR_DATA_IND_TEST	
				<=====	
(8)				GRR_DATA_IND_TEST	
				<=====	
TIMEOUT (1000)				GRR_DATA_IND_TEST	
(9)				*<=====*	
(10)				GRR_DATA_IND_TEST	
				<=====	
(11)				GRR_DATA_IND_TEST	
				<=====	
(12)				GRR_DATA_IND_TEST	
				<=====	
(13)				GRR_DATA_IND_TEST	
				<=====	
(14)				GRR_DATA_IND_TEST	
				<=====	
(15)				GRR_DATA_IND_TEST	
				<=====	
(16)				GRR_DATA_IND_TEST	
				<=====	
TIMEOUT (1000)				GRR_DATA_IND_TEST	
(17)				*<=====*	
(18)				GRR_DATA_REQ	
				<=====	

Parametrization:

Primitive	Parameter	Value
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(1) GRR_READY_IND		
(2) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS0_NR0_SDU100_SAPI3
(3) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS1_NR0_SDU100_SAPI3
(4) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS2_NR0_SDU100_SAPI3
(5) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS3_NR0_SDU100_SAPI3
(6) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS4_NR0_SDU100_SAPI3
(7) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS5_NR0_SDU100_SAPI3
(8) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS6_NR0_SDU100_SAPI3
(9) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS7_NR0_SDU100_SAPI3
(10) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS8_NR0_SDU100_SAPI3
(11) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS9_NR0_SDU100_SAPI3
(12) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS10_NR0_SDU100_SAPI3
(13) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS11_NR0_SDU100_SAPI3
(14) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS12_NR0_SDU100_SAPI3
(15) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS13_NR0_SDU100_SAPI3
(16) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS14_NR0_SDU100_SAPI3
(17) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS15_NR0_SDU100_SAPI3

(18) GRR_DATA_REQ

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAQSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRRIND_RR_NR16_SAPI3

History:

08-Sep-2000	SLM	Initial
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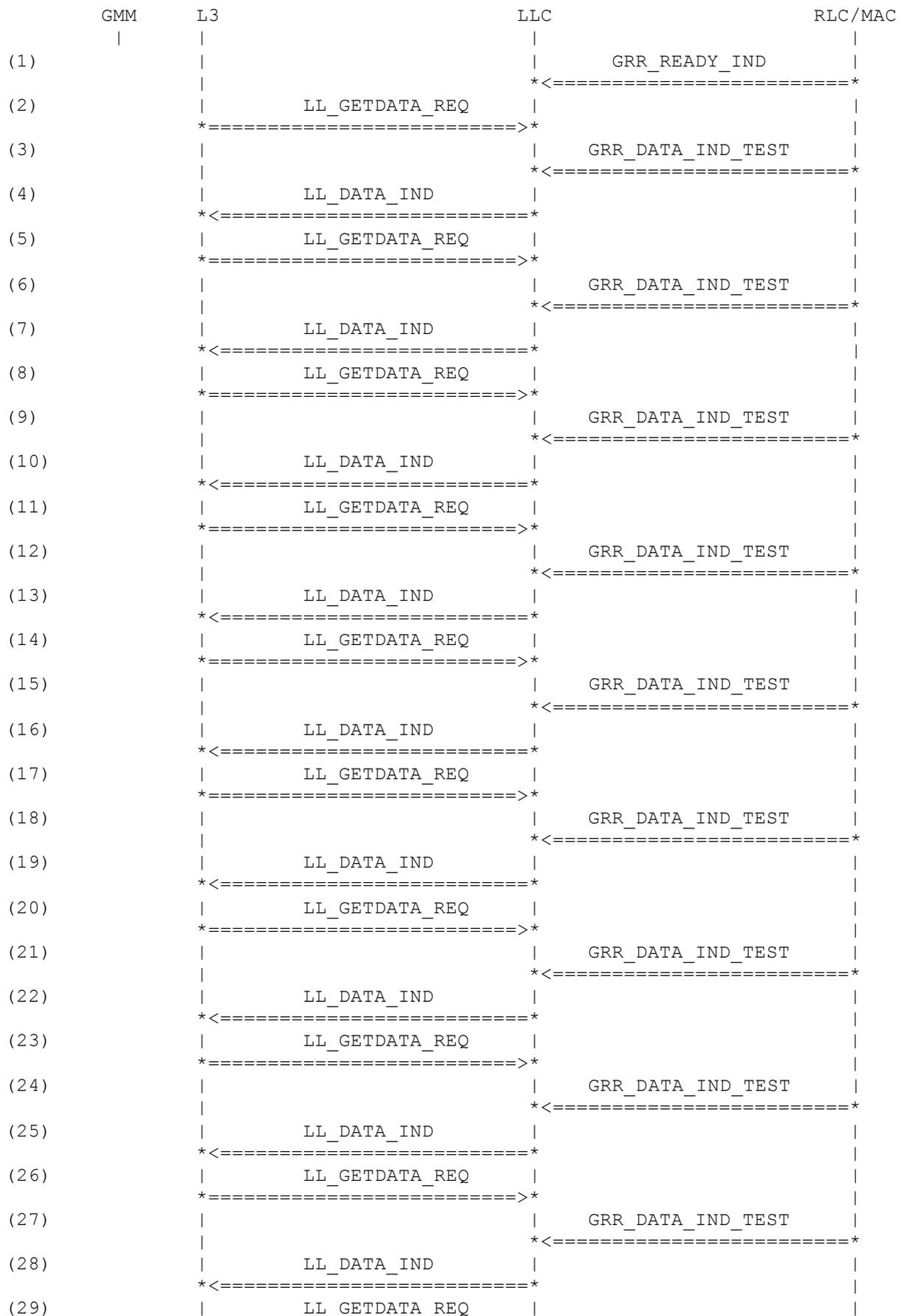
3.16.2 LLC802: ABM - Receive of a full window of 16 frames; RR send and data forwarded to LL

Description:

LLC receives acknowledged data frames. Each frame is forwarded after a get data request to LL. The last frame is received with A bit set to 1. Therefore an RR including an acknowledge for all frames.

Preamble:

LLC400A



(4) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N0_SAPI3
(5) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(6) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS1_NR0_SDU100_SAPI3
(7) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N1_SAPI3
(8) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(9) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS2_NR0_SDU100_SAPI3
(10) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N2_SAPI3
(11) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(12) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS3_NR0_SDU100_SAPI3
(13) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED

	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N3_SAPI3
(14) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(15) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS4_NR0_SDU100_SAPI3
(16) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N4_SAPI3
(17) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(18) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS5_NR0_SDU100_SAPI3
(19) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N5_SAPI3
(20) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(21) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS6_NR0_SDU100_SAPI3
(22) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N6_SAPI3

(23) LL_GETDATA_REQ	sapi tli	LL_SAPI_3 TLLI_LOCAL_1
(24) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS7_NR0_SDU100_SAPI3
(25) LL_DATA_IND	sapi tli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N7_SAPI3
(26) LL_GETDATA_REQ	sapi tli	LL_SAPI_3 TLLI_LOCAL_1
(27) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS8_NR0_SDU100_SAPI3
(28) LL_DATA_IND	sapi tli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N8_SAPI3
(29) LL_GETDATA_REQ	sapi tli	LL_SAPI_3 TLLI_LOCAL_1
(30) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS9_NR0_SDU100_SAPI3
(31) LL_DATA_IND	sapi tli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N9_SAPI3
(32) LL_GETDATA_REQ	sapi tli	LL_SAPI_3 TLLI_LOCAL_1

(33) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS10_NR0_SDU100_SAPI3
(34) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N10_SAPI3
(35) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(36) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS11_NR0_SDU100_SAPI3
(37) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N11_SAPI3
(38) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(39) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS12_NR0_SDU100_SAPI3
(40) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N12_SAPI3
(41) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(42) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS13_NR0_SDU100_SAPI3
(43) LL_DATA_IND	sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N13_SAPI3
(44) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(45) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS14_NR0_SDU100_SAPI3
(46) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N14_SAPI3
(47) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(48) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS15_NR0_SDU100_SAPI3
(49) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N15A1_SAPI3
(50) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(51) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRRIND_RR_NR16_SAPI3

History:

08-Nov-2000 GS Initial

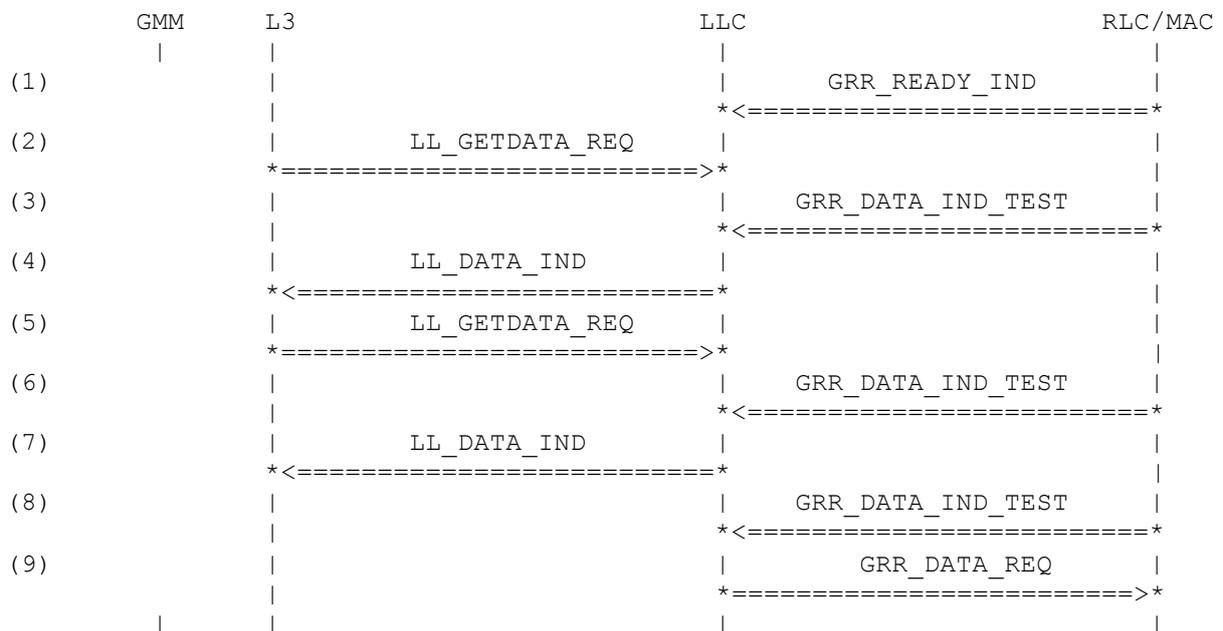
3.16.3 LLC810: ABM - Receive of frames, one missing; ACK send

Description:

LLC receives a acknowledged data frames. One frame number in sequence is missing. Therefore an ACK is send to the peer.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(3) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS0_NR0_SDU100_SAPI3
(4) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED

	reserved_data_ind6 sdu	NOT_USED LLDATAIND_SDU100_N0_SAPI3
(5) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(6) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS1_NR0_SDU100_SAPI3
(7) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N1_SAPI3
(8) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS3_NR0_SDU100_SAPI3
(9) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_ACK_NR2_SAPI3

History:

08-Nov-2000	GS	Initial
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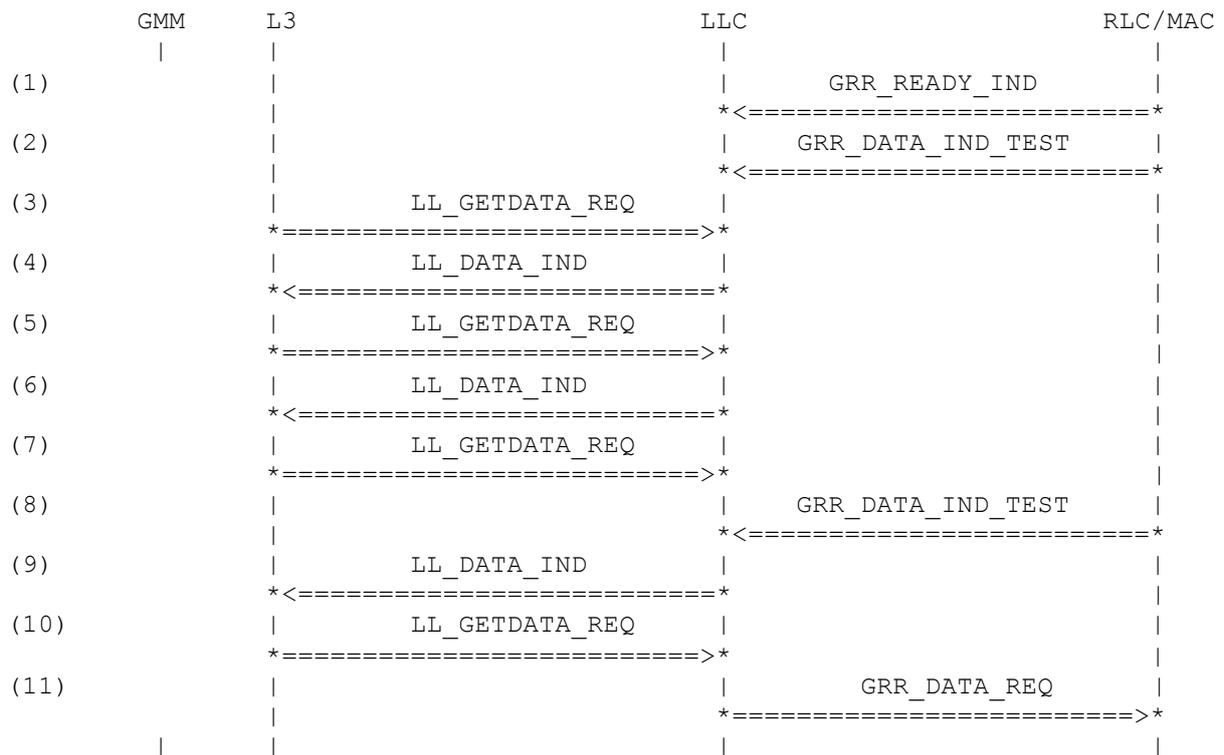
3.16.4 LLC811: ABM - Receive of missing frame; Frames forwarded to L3

Description:

LLC receives the retransmitted data frame and then the next new one with A bit set to 1. All in sequence acknowledge frames are forwarded to LL and a RR is send to the peer.

Preamble:

LLC810



Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS2_NR0_SDU100_SAPI3
(3) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(4) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N2_SAPI3
(5) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(6) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED

	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N3_SAPI3
(7) LL_GETDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(8) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS4_NR0_SDU100_A1_SAPI3
(9) LL_DATA_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N4A1_SAPI3
(10) LL_GETDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(11) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRRIND_RR_NR5_A0_SAPI3

History:

08-Nov-2000

GS

Initial

3.16.5 LLC812: ABM - Receive of missing frame; L3 not ready

Description:

LLC receives the retransmitted data frame and then the next new one with A bit set to 1. All in sequence acknowledge frames are forwarded to LL and a RR is send to the peer.

Preamble:

LLC810

	GMM	L3	LLC	RLC/MAC
(1)			GRR_READY_IND	
(2)			*<=====*	
(3)			GRR_DATA_IND_TEST	
(4)			*<=====*	
			GRR_DATA_REQ	
			=====>*	

Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS2_NR0_SDU100_SAPI3
(3) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS4_NR0_SDU100_A1_SAPI3
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR5_A0_SAPI3

History:

08-Nov-2000	GS	Initial
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3.16.6 LLC813: ABM - Forward of frame sequence to L3

Description:

LLC receives the retransmitted data frame and then the next new one with A bit set to 1. All in sequence acknowledge frames are forwarded to LL and a RR is send to the peer.

Preamble:

LLC812

	GMM	L3	LLC	RLC/MAC
(1)			LL_GETDATA_REQ	
			=====>	
(2)			LL_DATA_IND	
			<=====	
(3)			LL_GETDATA_REQ	
			=====>	
(4)			LL_DATA_IND	
			<=====	
(5)			LL_GETDATA_REQ	
			=====>	
(6)			LL_DATA_IND	
			<=====	
(7)			LL_GETDATA_REQ	
			=====>	
TIMEOUT (1000)				

Parametrization:

Primitive	Parameter	Value
(1) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(2) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
sdu	LLDATAIND_SDU100_N2_SAPI3	
(3) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(4) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
sdu	LLDATAIND_SDU100_N3_SAPI3	
(5) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(6) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED

	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N4A1_SAPI3
(7) LL_GETDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

History:

08-Nov-2000	GS	Initial
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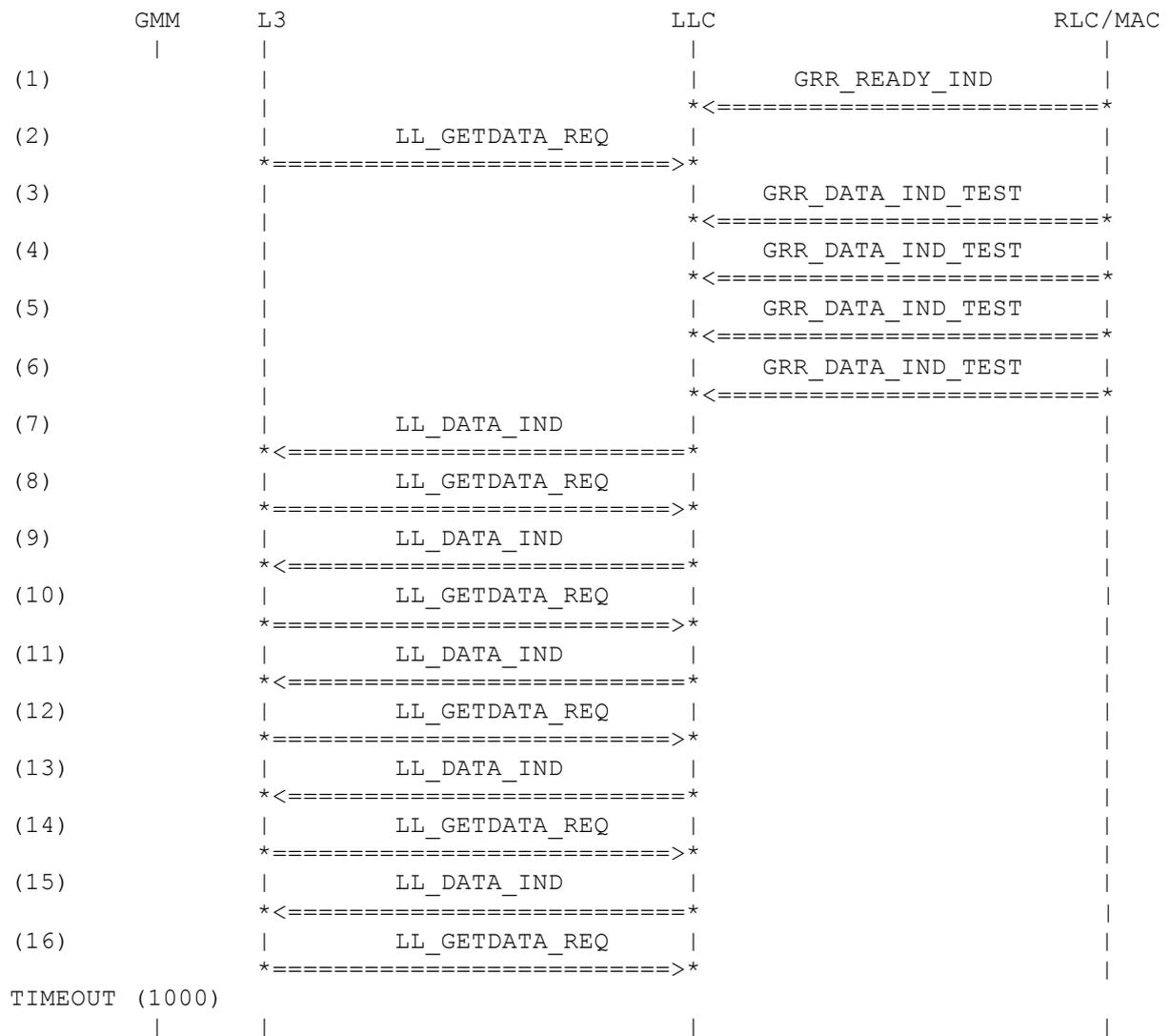
3.16.7 LLC814: ABM - Receive of new frames and the one missing frame; Frames forwarded to L3

Description:

LLC receives new frames and then the retransmitted data frame. All in sequence acknowledge frames are forwarded to LL each after a get data request of L3.

Preamble:

LLC810



Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(3) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS4_NR0_SDU100_SAPI3
(4) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS5_NR0_SDU100_SAPI3
(5) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS6_NR0_SDU100_SAPI3

(6) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS2_NR0_SDU100_SAPI3
(7) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N2_SAPI3
(8) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(9) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N3_SAPI3
(10) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(11) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N4_SAPI3
(12) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(13) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N5_SAPI3

(14) LL_GETDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1

(15) LL_DATA_IND

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
reserved_data_ind1	NOT_USED
reserved_data_ind2	NOT_USED
reserved_data_ind3	NOT_USED
reserved_data_ind4	NOT_USED
reserved_data_ind5	NOT_USED
reserved_data_ind6	NOT_USED
sdu	LLDATAIND_SDU100_N6_SAPI3

(16) LL_GETDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1

History:

09-Nov-2000	GS	Initial
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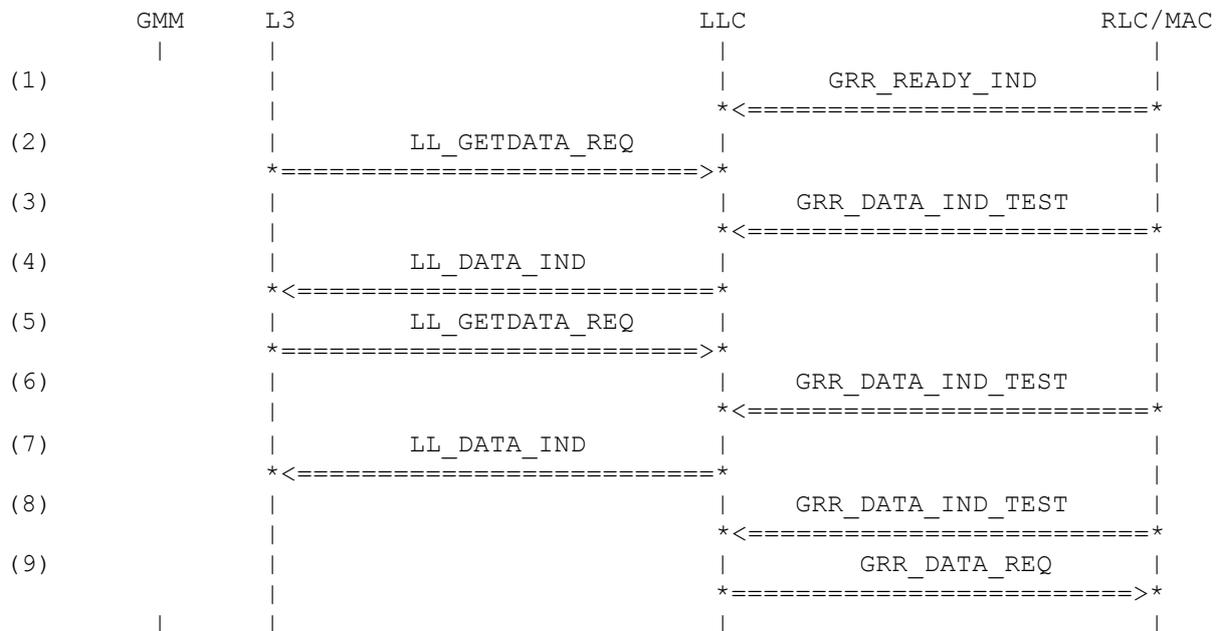
3.16.8 LLC820: ABM - Receive of frames, two missing; SACK send

Description:

LLC receives acknowledged data frames. Two frames in sequence are missing. LLC sends an SACK to the peer entity.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
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(1) GRR_READY_IND		
(2) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(3) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS0_NR0_SDU100_SAPI3
(4) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N0_SAPI3
(5) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(6) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS1_NR0_SDU100_SAPI3
(7) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N1_SAPI3
(8) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS4_NR0_SDU100_SAPI3
(9) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_SACK_NR2_R1_SAPI3

History:

08-Nov-2000 GS Initial

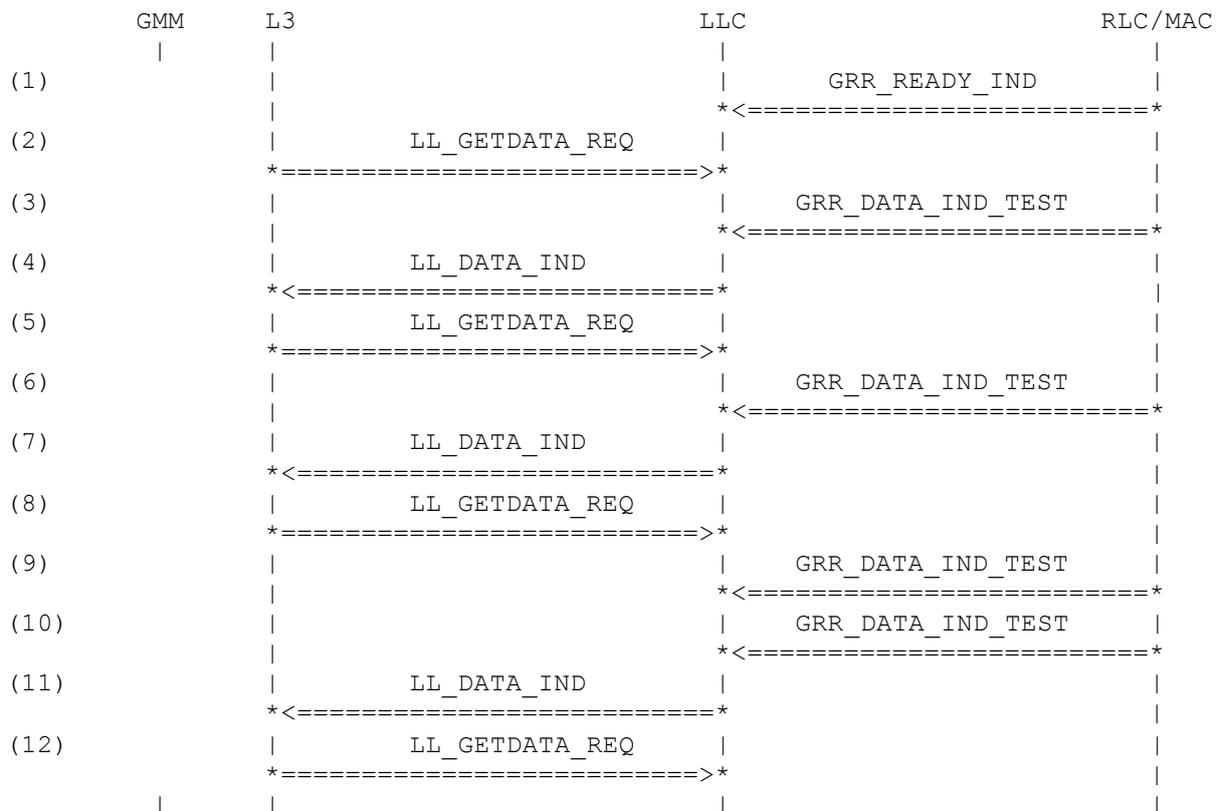
3.16.9 LLC830: ABM - Receive of a duplicated frame; Original frame is already forwarded to L3

Description:

LLC receives acknowledged data frames. One frames is duplicated (N(s) equal N(S) of last frame) and the original frame is already forwarded to L3. LLC ignores the frame.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) LL_GETDATA_REQ	sapi tli	LL_SAPI_3 TLLI_LOCAL_1
(3) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS0_NR0_SDU100_SAPI3
(4) LL_DATA_IND	sapi tli reserved_data_ind1 reserved_data_ind2	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED

	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N0_SAPI3
(5) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(6) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS1_NR0_SDU100_SAPI3
(7) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N1_SAPI3
(8) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(9) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS1_NR0_SDU100_SAPI3
(10) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS2_NR0_SDU100_SAPI3
(11) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N2_SAPI3
(12) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

History:

09-Nov-2000 GS Initial

3.16.10 LLC831: ABM - Receive of a duplicated frame; Original frame is queued to be forwarded to L3

Description:

LLC receives acknowledged data frames. One frames is duplicated (N(s) equal N(S) of last frame) and the original frame is ready to be forwarded to L3. LLC ignores the frame.

Preamble:

LLC400A

	GMM	L3	LLC	RLC/MAC
(1)			GRR_READY_IND	
			<=====	
(2)			GRR_DATA_IND_TEST	
			<=====	
(3)			GRR_DATA_IND_TEST	
			<=====	
(4)			GRR_DATA_IND_TEST	
			<=====	
(5)			GRR_DATA_IND_TEST	
			<=====	
(6)		LL_GETDATA_REQ		
		=====>		
(7)		LL_DATA_IND		
		<=====		
(8)		LL_GETDATA_REQ		
		=====>		
(9)		LL_DATA_IND		
		<=====		
(10)		LL_GETDATA_REQ		
		=====>		
(11)		LL_DATA_IND		
		<=====		
(12)		LL_GETDATA_REQ		
		=====>		

Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS0_NR0_SDU100_SAPI3
(3) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS1_NR0_SDU100_SAPI3
(4) GRR_DATA_IND_TEST	tli sdu	TLLI_LOCAL_1 GRRIND_NS1_NR0_SDU100_SAPI3

(5) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_NS2_NR0_SDU100_SAPI3
(6) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(7) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N0_SAPI3
(8) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(9) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N1_SAPI3
(10) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(11) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N2_SAPI3
(12) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1

History:

09-Nov-2000 GS Initial

3.16.11 LLC840: ABM - Receive of 31 I-Frames per variant; PDUs forwarded to L3

Description:

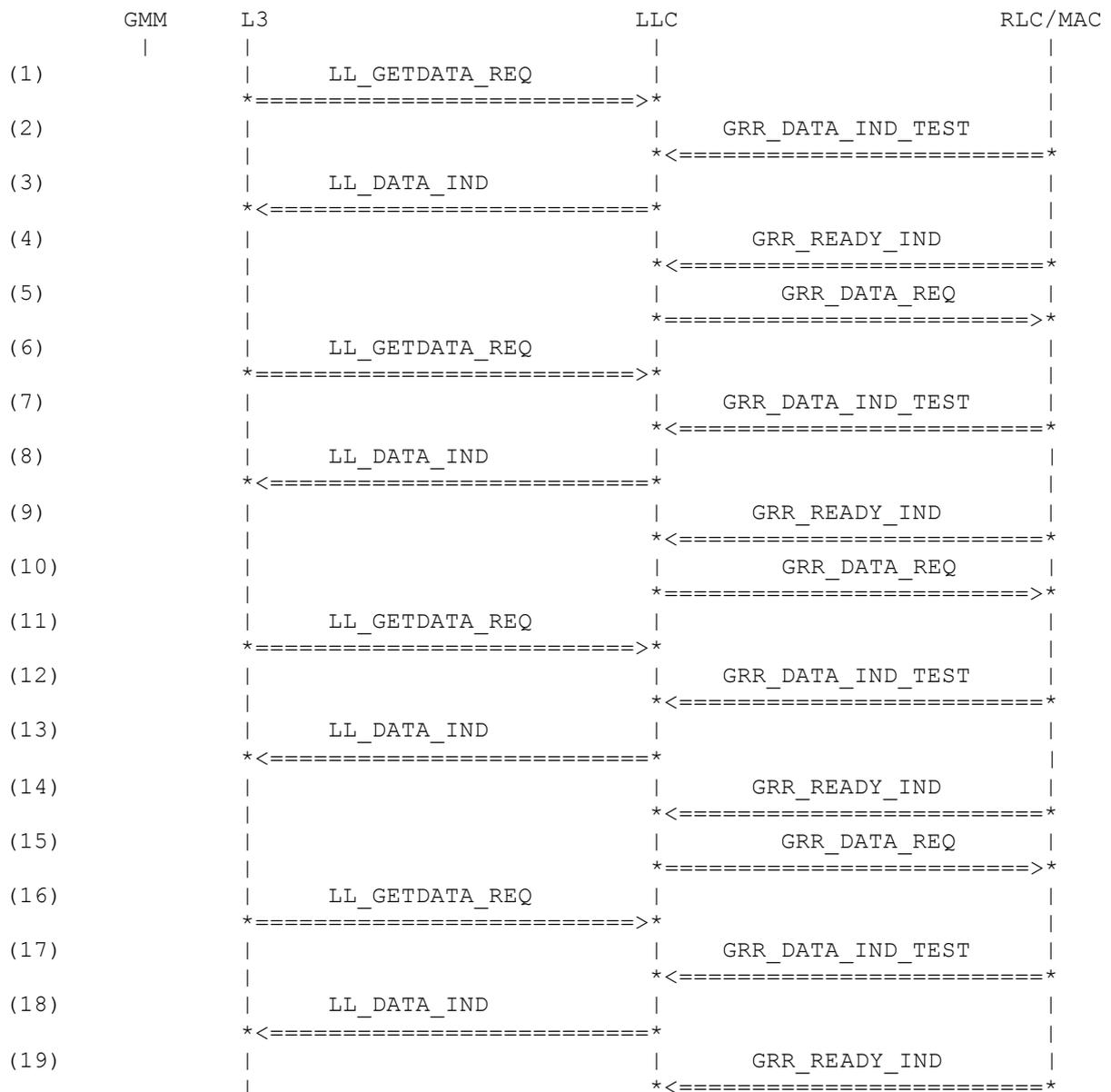
LLC receives 31 I-Frames from the RLC/MAC layer. LLC sends an RR to the peer entity and forwards the PDU to L3. If variant P is used, altogether 496 I-frames are transmitted.

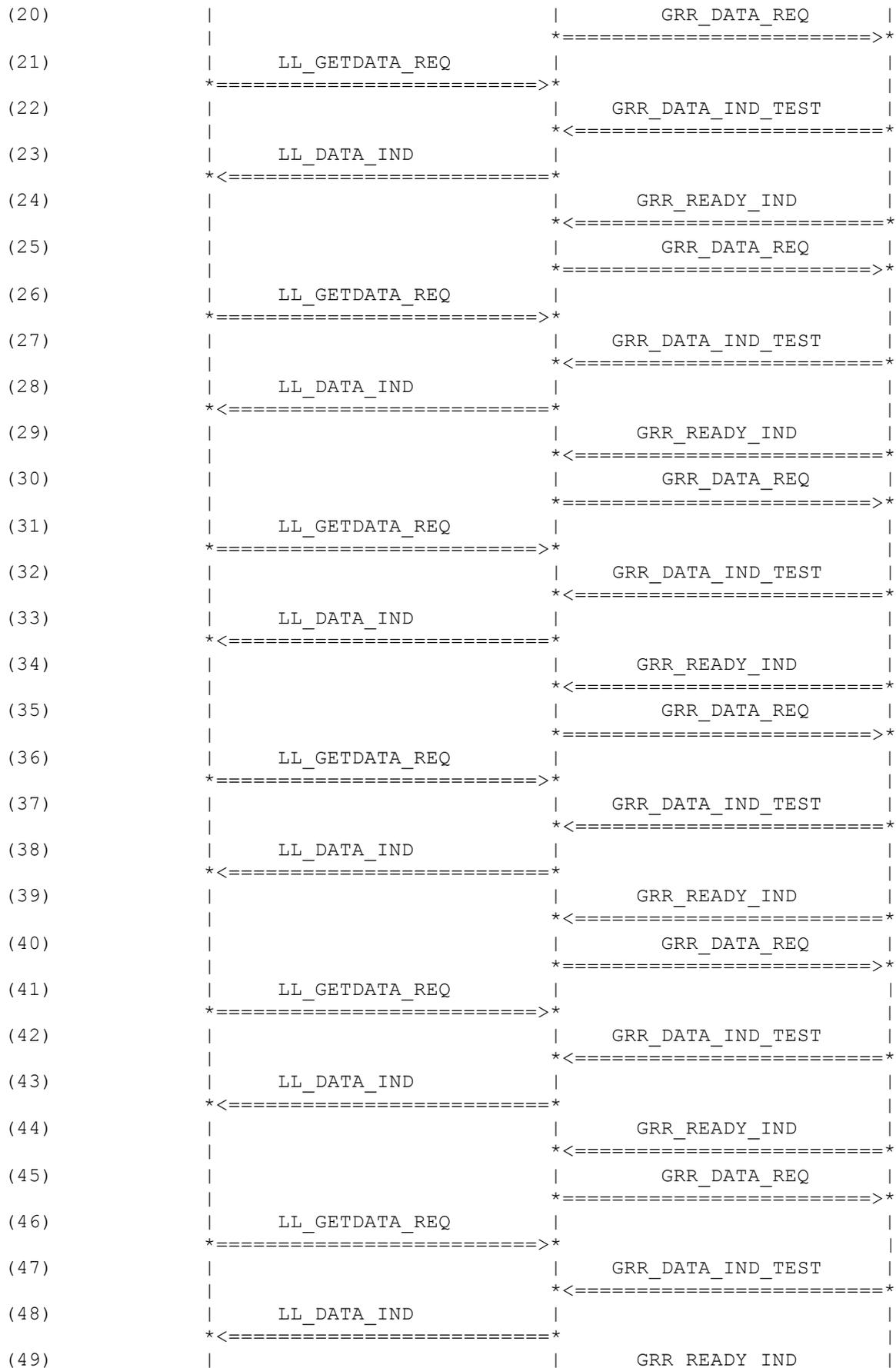
Variants:

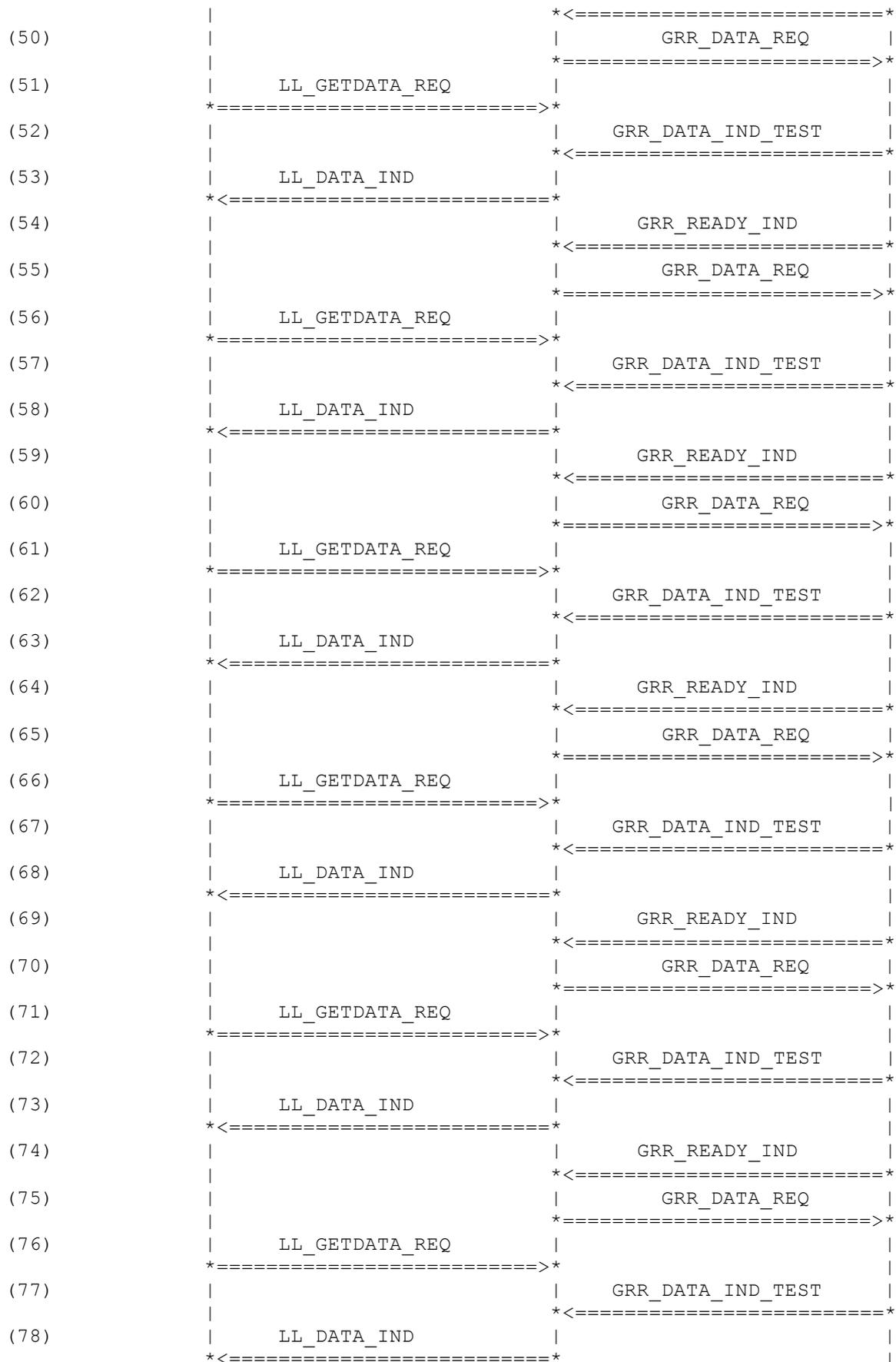
<A>....<P>

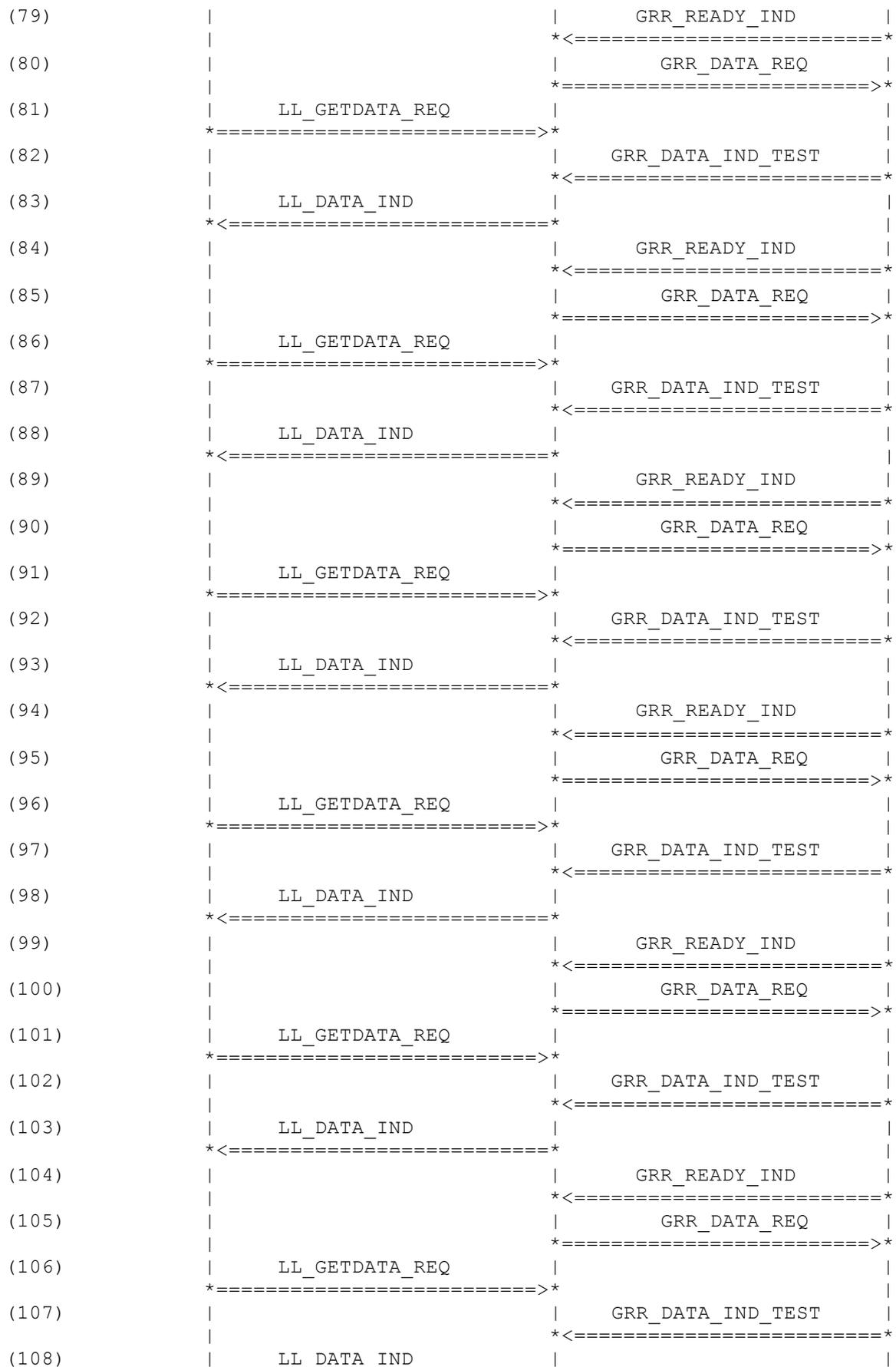
Preamble:

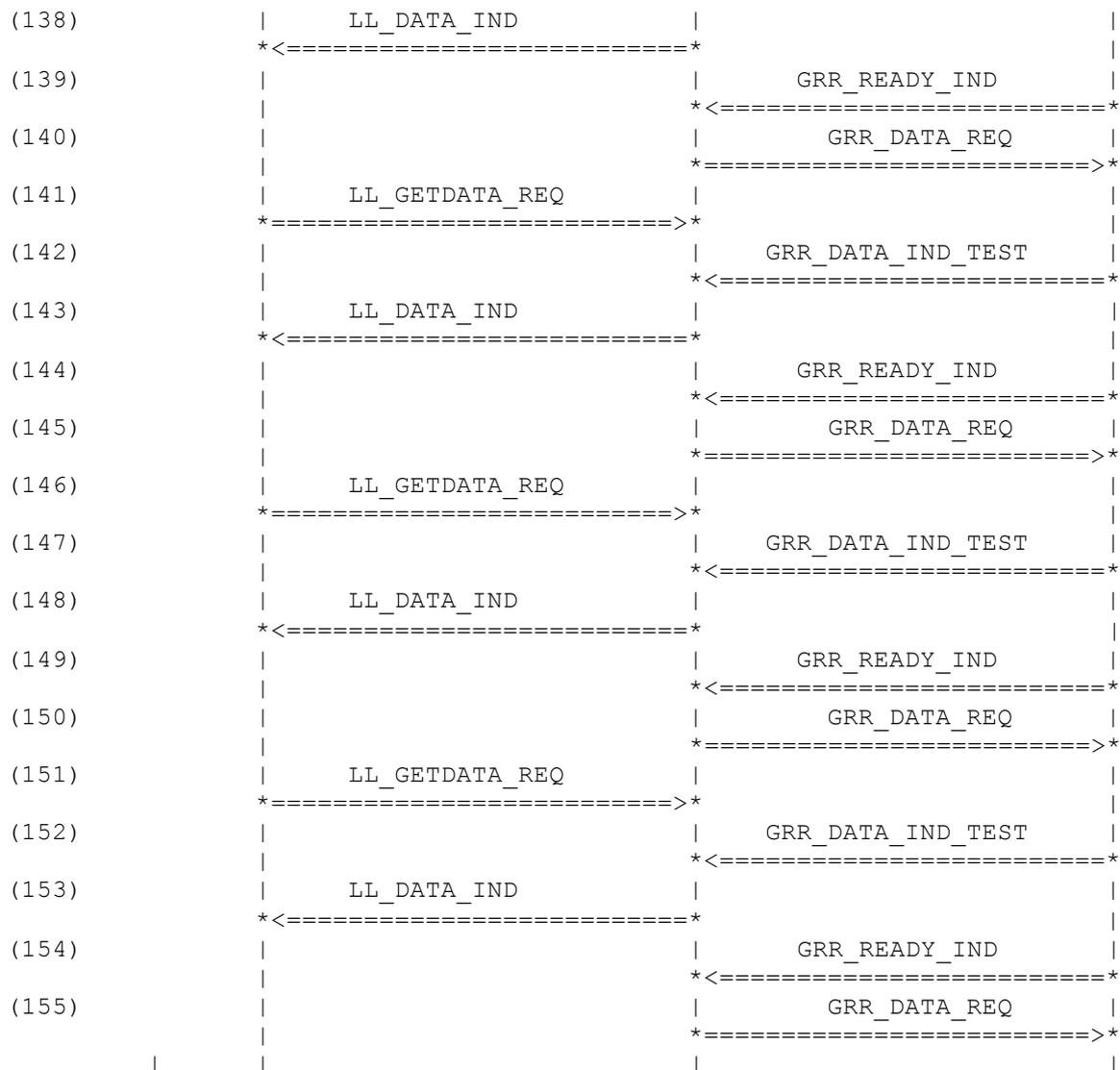
- <A>LLC400A
- LLC840A
- <C>LLC840B
- <D>LLC840C
- <E>LLC840D
- <F>LLC840E
- <G>LLC840F
- <H>LLC840G
- <I>LLC840H
- <J>LLC840I
- <K>LLC840J
- <L>LLC840K
- <M>LLC840L
- <N>LLC840M
- <O>LLC840N
- <P>LLC840O











Parametrization:

Primitive	Parameter	Value
(1) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
(2) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS0_NR0_A1_S3
	sdu	RR_D3_NS31_NR0_A1_S3
<C>	sdu	RR_D3_NS62_NR0_A1_S3
<D>	sdu	RR_D3_NS93_NR0_A1_S3
<E>	sdu	RR_D3_NS124_NR0_A1_S3
<F>	sdu	RR_D3_NS155_NR0_A1_S3
<G>	sdu	RR_D3_NS186_NR0_A1_S3
<H>	sdu	RR_D3_NS217_NR0_A1_S3
<I>	sdu	RR_D3_NS248_NR0_A1_S3
<J>	sdu	RR_D3_NS279_NR0_A1_S3
<K>	sdu	RR_D3_NS310_NR0_A1_S3

<L>	sdu	RR_D3_NS341_NR0_A1_S3
<M>	sdu	RR_D3_NS372_NR0_A1_S3
<N>	sdu	RR_D3_NS403_NR0_A1_S3
<O>	sdu	RR_D3_NS434_NR0_A1_S3
<P>	sdu	RR_D3_NS465_NR0_A1_S3
 (3) LL_DATA_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS0_NR0_A1_S3
	sdu	LLIND_D3_NS31_NR0_A1_S3
<C>	sdu	LLIND_D3_NS62_NR0_A1_S3
<D>	sdu	LLIND_D3_NS93_NR0_A1_S3
<E>	sdu	LLIND_D3_NS124_NR0_A1_S3
<F>	sdu	LLIND_D3_NS155_NR0_A1_S3
<G>	sdu	LLIND_D3_NS186_NR0_A1_S3
<H>	sdu	LLIND_D3_NS217_NR0_A1_S3
<I>	sdu	LLIND_D3_NS248_NR0_A1_S3
<J>	sdu	LLIND_D3_NS279_NR0_A1_S3
<K>	sdu	LLIND_D3_NS310_NR0_A1_S3
<L>	sdu	LLIND_D3_NS341_NR0_A1_S3
<M>	sdu	LLIND_D3_NS372_NR0_A1_S3
<N>	sdu	LLIND_D3_NS403_NR0_A1_S3
<O>	sdu	LLIND_D3_NS434_NR0_A1_S3
<P>	sdu	LLIND_D3_NS465_NR0_A1_S3
 (4) GRR_READY_IND		
 (5) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR1_A0_S3
	sdu	RR_NR32_A0_S3
<C>	sdu	RR_NR63_A0_S3
<D>	sdu	RR_NR94_A0_S3
<E>	sdu	RR_NR125_A0_S3
<F>	sdu	RR_NR156_A0_S3
<G>	sdu	RR_NR187_A0_S3
<H>	sdu	RR_NR218_A0_S3
<I>	sdu	RR_NR249_A0_S3
<J>	sdu	RR_NR280_A0_S3
<K>	sdu	RR_NR311_A0_S3
<L>	sdu	RR_NR342_A0_S3
<M>	sdu	RR_NR373_A0_S3
<N>	sdu	RR_NR404_A0_S3
<O>	sdu	RR_NR435_A0_S3
<P>	sdu	RR_NR466_A0_S3

(6) LL_GETDATA_REQ

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1

(7) GRR_DATA_IND_TEST

	tli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS1_NR0_A1_S3
	sdu	RR_D3_NS32_NR0_A1_S3
<C>	sdu	RR_D3_NS63_NR0_A1_S3
<D>	sdu	RR_D3_NS94_NR0_A1_S3
<E>	sdu	RR_D3_NS125_NR0_A1_S3
<F>	sdu	RR_D3_NS156_NR0_A1_S3
<G>	sdu	RR_D3_NS187_NR0_A1_S3
<H>	sdu	RR_D3_NS218_NR0_A1_S3
<I>	sdu	RR_D3_NS249_NR0_A1_S3
<J>	sdu	RR_D3_NS280_NR0_A1_S3
<K>	sdu	RR_D3_NS311_NR0_A1_S3
<L>	sdu	RR_D3_NS342_NR0_A1_S3
<M>	sdu	RR_D3_NS373_NR0_A1_S3
<N>	sdu	RR_D3_NS404_NR0_A1_S3
<O>	sdu	RR_D3_NS435_NR0_A1_S3
<P>	sdu	RR_D3_NS466_NR0_A1_S3

(8) LL_DATA_IND

	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS1_NR0_A1_S3
	sdu	LLIND_D3_NS32_NR0_A1_S3
<C>	sdu	LLIND_D3_NS63_NR0_A1_S3
<D>	sdu	LLIND_D3_NS94_NR0_A1_S3
<E>	sdu	LLIND_D3_NS125_NR0_A1_S3
<F>	sdu	LLIND_D3_NS156_NR0_A1_S3
<G>	sdu	LLIND_D3_NS187_NR0_A1_S3
<H>	sdu	LLIND_D3_NS218_NR0_A1_S3
<I>	sdu	LLIND_D3_NS249_NR0_A1_S3
<J>	sdu	LLIND_D3_NS280_NR0_A1_S3
<K>	sdu	LLIND_D3_NS311_NR0_A1_S3
<L>	sdu	LLIND_D3_NS342_NR0_A1_S3
<M>	sdu	LLIND_D3_NS373_NR0_A1_S3
<N>	sdu	LLIND_D3_NS404_NR0_A1_S3
<O>	sdu	LLIND_D3_NS435_NR0_A1_S3
<P>	sdu	LLIND_D3_NS466_NR0_A1_S3

(9) GRR_READY_IND

(10)

GRR_DATA_REQ	LL_SAPI_3
sapi	TLLI_LOCAL_1
tli	GRRREQ_QOS_PEAQSUB
grr_qos	GRR_RADIO_PRIO_1
radio_prio	GRR_DTACS_DEF
cause	NOT_USED
reserved_data_req	RR_NR2_A0_S3
<A>	RR_NR33_A0_S3
	

<C>	sdu	RR_NR64_A0_S3
<D>	sdu	RR_NR95_A0_S3
<E>	sdu	RR_NR126_A0_S3
<F>	sdu	RR_NR157_A0_S3
<G>	sdu	RR_NR188_A0_S3
<H>	sdu	RR_NR219_A0_S3
<I>	sdu	RR_NR250_A0_S3
<J>	sdu	RR_NR281_A0_S3
<K>	sdu	RR_NR312_A0_S3
<L>	sdu	RR_NR343_A0_S3
<M>	sdu	RR_NR374_A0_S3
<N>	sdu	RR_NR405_A0_S3
<O>	sdu	RR_NR436_A0_S3
<P>	sdu	RR_NR467_A0_S3
(11)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(12)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS2_NR0_A1_S3
	sdu	RR_D3_NS33_NR0_A1_S3
<C>	sdu	RR_D3_NS64_NR0_A1_S3
<D>	sdu	RR_D3_NS95_NR0_A1_S3
<E>	sdu	RR_D3_NS126_NR0_A1_S3
<F>	sdu	RR_D3_NS157_NR0_A1_S3
<G>	sdu	RR_D3_NS188_NR0_A1_S3
<H>	sdu	RR_D3_NS219_NR0_A1_S3
<I>	sdu	RR_D3_NS250_NR0_A1_S3
<J>	sdu	RR_D3_NS281_NR0_A1_S3
<K>	sdu	RR_D3_NS312_NR0_A1_S3
<L>	sdu	RR_D3_NS343_NR0_A1_S3
<M>	sdu	RR_D3_NS374_NR0_A1_S3
<N>	sdu	RR_D3_NS405_NR0_A1_S3
<O>	sdu	RR_D3_NS436_NR0_A1_S3
<P>	sdu	RR_D3_NS467_NR0_A1_S3
(13)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS2_NR0_A1_S3
	sdu	LLIND_D3_NS33_NR0_A1_S3
<C>	sdu	LLIND_D3_NS64_NR0_A1_S3
<D>	sdu	LLIND_D3_NS95_NR0_A1_S3
<E>	sdu	LLIND_D3_NS126_NR0_A1_S3
<F>	sdu	LLIND_D3_NS157_NR0_A1_S3
<G>	sdu	LLIND_D3_NS188_NR0_A1_S3
<H>	sdu	LLIND_D3_NS219_NR0_A1_S3
<I>	sdu	LLIND_D3_NS250_NR0_A1_S3
<J>	sdu	LLIND_D3_NS281_NR0_A1_S3
<K>	sdu	LLIND_D3_NS312_NR0_A1_S3
<L>	sdu	LLIND_D3_NS343_NR0_A1_S3

	<M>	sdu	LLIND_D3_NS374_NR0_A1_S3
	<N>	sdu	LLIND_D3_NS405_NR0_A1_S3
	<O>	sdu	LLIND_D3_NS436_NR0_A1_S3
	<P>	sdu	LLIND_D3_NS467_NR0_A1_S3
(14)		GRR_READY_IND	
(15)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKEQSUB
		radio_prio	GRR_RADIO_PRIO_1
		cause	GRR_DTACS_DEF
		reserved_data_req	NOT_USED
	<A>	sdu	RR_NR3_A0_S3
		sdu	RR_NR34_A0_S3
	<C>	sdu	RR_NR65_A0_S3
	<D>	sdu	RR_NR96_A0_S3
	<E>	sdu	RR_NR127_A0_S3
	<F>	sdu	RR_NR158_A0_S3
	<G>	sdu	RR_NR189_A0_S3
	<H>	sdu	RR_NR220_A0_S3
	<I>	sdu	RR_NR251_A0_S3
	<J>	sdu	RR_NR282_A0_S3
	<K>	sdu	RR_NR313_A0_S3
	<L>	sdu	RR_NR344_A0_S3
	<M>	sdu	RR_NR375_A0_S3
	<N>	sdu	RR_NR406_A0_S3
	<O>	sdu	RR_NR437_A0_S3
	<P>	sdu	RR_NR468_A0_S3
(16)		LL_GETDATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(17)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_D3_NS3_NR0_A1_S3
		sdu	RR_D3_NS34_NR0_A1_S3
	<C>	sdu	RR_D3_NS65_NR0_A1_S3
	<D>	sdu	RR_D3_NS96_NR0_A1_S3
	<E>	sdu	RR_D3_NS127_NR0_A1_S3
	<F>	sdu	RR_D3_NS158_NR0_A1_S3
	<G>	sdu	RR_D3_NS189_NR0_A1_S3
	<H>	sdu	RR_D3_NS220_NR0_A1_S3
	<I>	sdu	RR_D3_NS251_NR0_A1_S3
	<J>	sdu	RR_D3_NS282_NR0_A1_S3
	<K>	sdu	RR_D3_NS313_NR0_A1_S3
	<L>	sdu	RR_D3_NS344_NR0_A1_S3
	<M>	sdu	RR_D3_NS375_NR0_A1_S3
	<N>	sdu	RR_D3_NS406_NR0_A1_S3
	<O>	sdu	RR_D3_NS437_NR0_A1_S3
	<P>	sdu	RR_D3_NS468_NR0_A1_S3
(18)		LL_DATA_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reserved_data_ind1	NOT_USED
		reserved_data_ind2	NOT_USED
		reserved_data_ind3	NOT_USED

	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS3_NR0_A1_S3
	sdu	LLIND_D3_NS34_NR0_A1_S3
<C>	sdu	LLIND_D3_NS65_NR0_A1_S3
<D>	sdu	LLIND_D3_NS96_NR0_A1_S3
<E>	sdu	LLIND_D3_NS127_NR0_A1_S3
<F>	sdu	LLIND_D3_NS158_NR0_A1_S3
<G>	sdu	LLIND_D3_NS189_NR0_A1_S3
<H>	sdu	LLIND_D3_NS220_NR0_A1_S3
<I>	sdu	LLIND_D3_NS251_NR0_A1_S3
<J>	sdu	LLIND_D3_NS282_NR0_A1_S3
<K>	sdu	LLIND_D3_NS313_NR0_A1_S3
<L>	sdu	LLIND_D3_NS344_NR0_A1_S3
<M>	sdu	LLIND_D3_NS375_NR0_A1_S3
<N>	sdu	LLIND_D3_NS406_NR0_A1_S3
<O>	sdu	LLIND_D3_NS437_NR0_A1_S3
<P>	sdu	LLIND_D3_NS468_NR0_A1_S3
(19)	GRR_READY_IND	
(20)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR4_A0_S3
	sdu	RR_NR35_A0_S3
<C>	sdu	RR_NR66_A0_S3
<D>	sdu	RR_NR97_A0_S3
<E>	sdu	RR_NR128_A0_S3
<F>	sdu	RR_NR159_A0_S3
<G>	sdu	RR_NR190_A0_S3
<H>	sdu	RR_NR221_A0_S3
<I>	sdu	RR_NR252_A0_S3
<J>	sdu	RR_NR283_A0_S3
<K>	sdu	RR_NR314_A0_S3
<L>	sdu	RR_NR345_A0_S3
<M>	sdu	RR_NR376_A0_S3
<N>	sdu	RR_NR407_A0_S3
<O>	sdu	RR_NR438_A0_S3
<P>	sdu	RR_NR469_A0_S3
(21)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(22)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS4_NR0_A1_S3
	sdu	RR_D3_NS35_NR0_A1_S3
<C>	sdu	RR_D3_NS66_NR0_A1_S3
<D>	sdu	RR_D3_NS97_NR0_A1_S3
<E>	sdu	RR_D3_NS128_NR0_A1_S3
<F>	sdu	RR_D3_NS159_NR0_A1_S3
<G>	sdu	RR_D3_NS190_NR0_A1_S3
<H>	sdu	RR_D3_NS221_NR0_A1_S3

<I>	sdu	RR_D3_NS252_NR0_A1_S3
<J>	sdu	RR_D3_NS283_NR0_A1_S3
<K>	sdu	RR_D3_NS314_NR0_A1_S3
<L>	sdu	RR_D3_NS345_NR0_A1_S3
<M>	sdu	RR_D3_NS376_NR0_A1_S3
<N>	sdu	RR_D3_NS407_NR0_A1_S3
<O>	sdu	RR_D3_NS438_NR0_A1_S3
<P>	sdu	RR_D3_NS469_NR0_A1_S3
(23)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS4_NR0_A1_S3
	sdu	LLIND_D3_NS35_NR0_A1_S3
<C>	sdu	LLIND_D3_NS66_NR0_A1_S3
<D>	sdu	LLIND_D3_NS97_NR0_A1_S3
<E>	sdu	LLIND_D3_NS128_NR0_A1_S3
<F>	sdu	LLIND_D3_NS159_NR0_A1_S3
<G>	sdu	LLIND_D3_NS190_NR0_A1_S3
<H>	sdu	LLIND_D3_NS221_NR0_A1_S3
<I>	sdu	LLIND_D3_NS252_NR0_A1_S3
<J>	sdu	LLIND_D3_NS283_NR0_A1_S3
<K>	sdu	LLIND_D3_NS314_NR0_A1_S3
<L>	sdu	LLIND_D3_NS345_NR0_A1_S3
<M>	sdu	LLIND_D3_NS376_NR0_A1_S3
<N>	sdu	LLIND_D3_NS407_NR0_A1_S3
<O>	sdu	LLIND_D3_NS438_NR0_A1_S3
<P>	sdu	LLIND_D3_NS469_NR0_A1_S3
(24)	GRR_READY_IND	
(25)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR5_A0_S3
	sdu	RR_NR36_A0_S3
<C>	sdu	RR_NR67_A0_S3
<D>	sdu	RR_NR98_A0_S3
<E>	sdu	RR_NR129_A0_S3
<F>	sdu	RR_NR160_A0_S3
<G>	sdu	RR_NR191_A0_S3
<H>	sdu	RR_NR222_A0_S3
<I>	sdu	RR_NR253_A0_S3
<J>	sdu	RR_NR284_A0_S3
<K>	sdu	RR_NR315_A0_S3
<L>	sdu	RR_NR346_A0_S3
<M>	sdu	RR_NR377_A0_S3
<N>	sdu	RR_NR408_A0_S3

	<O>	sdu	RR_NR439_A0_S3
	<P>	sdu	RR_NR470_A0_S3
(26)		LL_GETDATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(27)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_D3_NS5_NR0_A1_S3
		sdu	RR_D3_NS36_NR0_A1_S3
	<C>	sdu	RR_D3_NS67_NR0_A1_S3
	<D>	sdu	RR_D3_NS98_NR0_A1_S3
	<E>	sdu	RR_D3_NS129_NR0_A1_S3
	<F>	sdu	RR_D3_NS160_NR0_A1_S3
	<G>	sdu	RR_D3_NS191_NR0_A1_S3
	<H>	sdu	RR_D3_NS222_NR0_A1_S3
	<I>	sdu	RR_D3_NS253_NR0_A1_S3
	<J>	sdu	RR_D3_NS284_NR0_A1_S3
	<K>	sdu	RR_D3_NS315_NR0_A1_S3
	<L>	sdu	RR_D3_NS346_NR0_A1_S3
	<M>	sdu	RR_D3_NS377_NR0_A1_S3
	<N>	sdu	RR_D3_NS408_NR0_A1_S3
	<O>	sdu	RR_D3_NS439_NR0_A1_S3
	<P>	sdu	RR_D3_NS470_NR0_A1_S3
(28)		LL_DATA_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reserved_data_ind1	NOT_USED
		reserved_data_ind2	NOT_USED
		reserved_data_ind3	NOT_USED
		reserved_data_ind4	NOT_USED
		reserved_data_ind5	NOT_USED
		reserved_data_ind6	NOT_USED
	<A>	sdu	LLIND_D3_NS5_NR0_A1_S3
		sdu	LLIND_D3_NS36_NR0_A1_S3
	<C>	sdu	LLIND_D3_NS67_NR0_A1_S3
	<D>	sdu	LLIND_D3_NS98_NR0_A1_S3
	<E>	sdu	LLIND_D3_NS129_NR0_A1_S3
	<F>	sdu	LLIND_D3_NS160_NR0_A1_S3
	<G>	sdu	LLIND_D3_NS191_NR0_A1_S3
	<H>	sdu	LLIND_D3_NS222_NR0_A1_S3
	<I>	sdu	LLIND_D3_NS253_NR0_A1_S3
	<J>	sdu	LLIND_D3_NS284_NR0_A1_S3
	<K>	sdu	LLIND_D3_NS315_NR0_A1_S3
	<L>	sdu	LLIND_D3_NS346_NR0_A1_S3
	<M>	sdu	LLIND_D3_NS377_NR0_A1_S3
	<N>	sdu	LLIND_D3_NS408_NR0_A1_S3
	<O>	sdu	LLIND_D3_NS439_NR0_A1_S3
	<P>	sdu	LLIND_D3_NS470_NR0_A1_S3
(29)		GRR_READY_IND	
(30)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKEQSUB
		radio_prio	GRR_RADIO_PRIO_1
		cause	GRR_DTACS_DEF

	reserved_data_req	NOT_USED
<A>	sdu	RR_NR6_A0_S3
	sdu	RR_NR37_A0_S3
<C>	sdu	RR_NR68_A0_S3
<D>	sdu	RR_NR99_A0_S3
<E>	sdu	RR_NR130_A0_S3
<F>	sdu	RR_NR161_A0_S3
<G>	sdu	RR_NR192_A0_S3
<H>	sdu	RR_NR223_A0_S3
<I>	sdu	RR_NR254_A0_S3
<J>	sdu	RR_NR285_A0_S3
<K>	sdu	RR_NR316_A0_S3
<L>	sdu	RR_NR347_A0_S3
<M>	sdu	RR_NR378_A0_S3
<N>	sdu	RR_NR409_A0_S3
<O>	sdu	RR_NR440_A0_S3
<P>	sdu	RR_NR471_A0_S3
(31)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(32)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS6_NR0_A1_S3
	sdu	RR_D3_NS37_NR0_A1_S3
<C>	sdu	RR_D3_NS68_NR0_A1_S3
<D>	sdu	RR_D3_NS99_NR0_A1_S3
<E>	sdu	RR_D3_NS130_NR0_A1_S3
<F>	sdu	RR_D3_NS161_NR0_A1_S3
<G>	sdu	RR_D3_NS192_NR0_A1_S3
<H>	sdu	RR_D3_NS223_NR0_A1_S3
<I>	sdu	RR_D3_NS254_NR0_A1_S3
<J>	sdu	RR_D3_NS285_NR0_A1_S3
<K>	sdu	RR_D3_NS316_NR0_A1_S3
<L>	sdu	RR_D3_NS347_NR0_A1_S3
<M>	sdu	RR_D3_NS378_NR0_A1_S3
<N>	sdu	RR_D3_NS409_NR0_A1_S3
<O>	sdu	RR_D3_NS440_NR0_A1_S3
<P>	sdu	RR_D3_NS471_NR0_A1_S3
(33)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS6_NR0_A1_S3
	sdu	LLIND_D3_NS37_NR0_A1_S3
<C>	sdu	LLIND_D3_NS68_NR0_A1_S3
<D>	sdu	LLIND_D3_NS99_NR0_A1_S3
<E>	sdu	LLIND_D3_NS130_NR0_A1_S3
<F>	sdu	LLIND_D3_NS161_NR0_A1_S3
<G>	sdu	LLIND_D3_NS192_NR0_A1_S3
<H>	sdu	LLIND_D3_NS223_NR0_A1_S3
<I>	sdu	LLIND_D3_NS254_NR0_A1_S3

	<J>	sdu	LLIND_D3_NS285_NR0_A1_S3
	<K>	sdu	LLIND_D3_NS316_NR0_A1_S3
	<L>	sdu	LLIND_D3_NS347_NR0_A1_S3
	<M>	sdu	LLIND_D3_NS378_NR0_A1_S3
	<N>	sdu	LLIND_D3_NS409_NR0_A1_S3
	<O>	sdu	LLIND_D3_NS440_NR0_A1_S3
	<P>	sdu	LLIND_D3_NS471_NR0_A1_S3
(34)		GRR_READY_IND	
(35)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKSUB
		radio_prio	GRR_RADIO_PRIO_1
		cause	GRR_DTACS_DEF
		reserved_data_req	NOT_USED
	<A>	sdu	RR_NR7_A0_S3
		sdu	RR_NR38_A0_S3
	<C>	sdu	RR_NR69_A0_S3
	<D>	sdu	RR_NR100_A0_S3
	<E>	sdu	RR_NR131_A0_S3
	<F>	sdu	RR_NR162_A0_S3
	<G>	sdu	RR_NR193_A0_S3
	<H>	sdu	RR_NR224_A0_S3
	<I>	sdu	RR_NR255_A0_S3
	<J>	sdu	RR_NR286_A0_S3
	<K>	sdu	RR_NR317_A0_S3
	<L>	sdu	RR_NR348_A0_S3
	<M>	sdu	RR_NR379_A0_S3
	<N>	sdu	RR_NR410_A0_S3
	<O>	sdu	RR_NR441_A0_S3
	<P>	sdu	RR_NR472_A0_S3
(36)		LL_GETDATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(37)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_D3_NS7_NR0_A1_S3
		sdu	RR_D3_NS38_NR0_A1_S3
	<C>	sdu	RR_D3_NS69_NR0_A1_S3
	<D>	sdu	RR_D3_NS100_NR0_A1_S3
	<E>	sdu	RR_D3_NS131_NR0_A1_S3
	<F>	sdu	RR_D3_NS162_NR0_A1_S3
	<G>	sdu	RR_D3_NS193_NR0_A1_S3
	<H>	sdu	RR_D3_NS224_NR0_A1_S3
	<I>	sdu	RR_D3_NS255_NR0_A1_S3
	<J>	sdu	RR_D3_NS286_NR0_A1_S3
	<K>	sdu	RR_D3_NS317_NR0_A1_S3
	<L>	sdu	RR_D3_NS348_NR0_A1_S3
	<M>	sdu	RR_D3_NS379_NR0_A1_S3
	<N>	sdu	RR_D3_NS410_NR0_A1_S3
	<O>	sdu	RR_D3_NS441_NR0_A1_S3
	<P>	sdu	RR_D3_NS472_NR0_A1_S3
(38)		LL_DATA_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1

	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS7_NR0_A1_S3
	sdu	LLIND_D3_NS38_NR0_A1_S3
<C>	sdu	LLIND_D3_NS69_NR0_A1_S3
<D>	sdu	LLIND_D3_NS100_NR0_A1_S3
<E>	sdu	LLIND_D3_NS131_NR0_A1_S3
<F>	sdu	LLIND_D3_NS162_NR0_A1_S3
<G>	sdu	LLIND_D3_NS193_NR0_A1_S3
<H>	sdu	LLIND_D3_NS224_NR0_A1_S3
<I>	sdu	LLIND_D3_NS255_NR0_A1_S3
<J>	sdu	LLIND_D3_NS286_NR0_A1_S3
<K>	sdu	LLIND_D3_NS317_NR0_A1_S3
<L>	sdu	LLIND_D3_NS348_NR0_A1_S3
<M>	sdu	LLIND_D3_NS379_NR0_A1_S3
<N>	sdu	LLIND_D3_NS410_NR0_A1_S3
<O>	sdu	LLIND_D3_NS441_NR0_A1_S3
<P>	sdu	LLIND_D3_NS472_NR0_A1_S3
(39)	GRR_READY_IND	
(40)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR8_A0_S3
	sdu	RR_NR39_A0_S3
<C>	sdu	RR_NR70_A0_S3
<D>	sdu	RR_NR101_A0_S3
<E>	sdu	RR_NR132_A0_S3
<F>	sdu	RR_NR163_A0_S3
<G>	sdu	RR_NR194_A0_S3
<H>	sdu	RR_NR225_A0_S3
<I>	sdu	RR_NR256_A0_S3
<J>	sdu	RR_NR287_A0_S3
<K>	sdu	RR_NR318_A0_S3
<L>	sdu	RR_NR349_A0_S3
<M>	sdu	RR_NR380_A0_S3
<N>	sdu	RR_NR411_A0_S3
<O>	sdu	RR_NR442_A0_S3
<P>	sdu	RR_NR473_A0_S3
(41)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
(42)	GRR_DATA_IND_TEST	
	tli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS8_NR0_A1_S3
	sdu	RR_D3_NS39_NR0_A1_S3
<C>	sdu	RR_D3_NS70_NR0_A1_S3
<D>	sdu	RR_D3_NS101_NR0_A1_S3
<E>	sdu	RR_D3_NS132_NR0_A1_S3

<F>	sdu	RR_D3_NS163_NR0_A1_S3
<G>	sdu	RR_D3_NS194_NR0_A1_S3
<H>	sdu	RR_D3_NS225_NR0_A1_S3
<I>	sdu	RR_D3_NS256_NR0_A1_S3
<J>	sdu	RR_D3_NS287_NR0_A1_S3
<K>	sdu	RR_D3_NS318_NR0_A1_S3
<L>	sdu	RR_D3_NS349_NR0_A1_S3
<M>	sdu	RR_D3_NS380_NR0_A1_S3
<N>	sdu	RR_D3_NS411_NR0_A1_S3
<O>	sdu	RR_D3_NS442_NR0_A1_S3
<P>	sdu	RR_D3_NS473_NR0_A1_S3
(43)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS8_NR0_A1_S3
	sdu	LLIND_D3_NS39_NR0_A1_S3
<C>	sdu	LLIND_D3_NS70_NR0_A1_S3
<D>	sdu	LLIND_D3_NS101_NR0_A1_S3
<E>	sdu	LLIND_D3_NS132_NR0_A1_S3
<F>	sdu	LLIND_D3_NS163_NR0_A1_S3
<G>	sdu	LLIND_D3_NS194_NR0_A1_S3
<H>	sdu	LLIND_D3_NS225_NR0_A1_S3
<I>	sdu	LLIND_D3_NS256_NR0_A1_S3
<J>	sdu	LLIND_D3_NS287_NR0_A1_S3
<K>	sdu	LLIND_D3_NS318_NR0_A1_S3
<L>	sdu	LLIND_D3_NS349_NR0_A1_S3
<M>	sdu	LLIND_D3_NS380_NR0_A1_S3
<N>	sdu	LLIND_D3_NS411_NR0_A1_S3
<O>	sdu	LLIND_D3_NS442_NR0_A1_S3
<P>	sdu	LLIND_D3_NS473_NR0_A1_S3
(44)	GRR_READY_IND	
(45)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR9_A0_S3
	sdu	RR_NR40_A0_S3
<C>	sdu	RR_NR71_A0_S3
<D>	sdu	RR_NR102_A0_S3
<E>	sdu	RR_NR133_A0_S3
<F>	sdu	RR_NR164_A0_S3
<G>	sdu	RR_NR195_A0_S3
<H>	sdu	RR_NR226_A0_S3
<I>	sdu	RR_NR257_A0_S3
<J>	sdu	RR_NR288_A0_S3
<K>	sdu	RR_NR319_A0_S3
<L>	sdu	RR_NR350_A0_S3

	<M>	sdu	RR_NR381_A0_S3
	<N>	sdu	RR_NR412_A0_S3
	<O>	sdu	RR_NR443_A0_S3
	<P>	sdu	RR_NR474_A0_S3
(46)		LL_GETDATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(47)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_D3_NS9_NR0_A1_S3
		sdu	RR_D3_NS40_NR0_A1_S3
	<C>	sdu	RR_D3_NS71_NR0_A1_S3
	<D>	sdu	RR_D3_NS102_NR0_A1_S3
	<E>	sdu	RR_D3_NS133_NR0_A1_S3
	<F>	sdu	RR_D3_NS164_NR0_A1_S3
	<G>	sdu	RR_D3_NS195_NR0_A1_S3
	<H>	sdu	RR_D3_NS226_NR0_A1_S3
	<I>	sdu	RR_D3_NS257_NR0_A1_S3
	<J>	sdu	RR_D3_NS288_NR0_A1_S3
	<K>	sdu	RR_D3_NS319_NR0_A1_S3
	<L>	sdu	RR_D3_NS350_NR0_A1_S3
	<M>	sdu	RR_D3_NS381_NR0_A1_S3
	<N>	sdu	RR_D3_NS412_NR0_A1_S3
	<O>	sdu	RR_D3_NS443_NR0_A1_S3
	<P>	sdu	RR_D3_NS474_NR0_A1_S3
(48)		LL_DATA_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reserved_data_ind1	NOT_USED
		reserved_data_ind2	NOT_USED
		reserved_data_ind3	NOT_USED
		reserved_data_ind4	NOT_USED
		reserved_data_ind5	NOT_USED
		reserved_data_ind6	NOT_USED
	<A>	sdu	LLIND_D3_NS9_NR0_A1_S3
		sdu	LLIND_D3_NS40_NR0_A1_S3
	<C>	sdu	LLIND_D3_NS71_NR0_A1_S3
	<D>	sdu	LLIND_D3_NS102_NR0_A1_S3
	<E>	sdu	LLIND_D3_NS133_NR0_A1_S3
	<F>	sdu	LLIND_D3_NS164_NR0_A1_S3
	<G>	sdu	LLIND_D3_NS195_NR0_A1_S3
	<H>	sdu	LLIND_D3_NS226_NR0_A1_S3
	<I>	sdu	LLIND_D3_NS257_NR0_A1_S3
	<J>	sdu	LLIND_D3_NS288_NR0_A1_S3
	<K>	sdu	LLIND_D3_NS319_NR0_A1_S3
	<L>	sdu	LLIND_D3_NS350_NR0_A1_S3
	<M>	sdu	LLIND_D3_NS381_NR0_A1_S3
	<N>	sdu	LLIND_D3_NS412_NR0_A1_S3
	<O>	sdu	LLIND_D3_NS443_NR0_A1_S3
	<P>	sdu	LLIND_D3_NS474_NR0_A1_S3
(49)		GRR_READY_IND	
(50)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKSUB

	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR10_A0_S3
	sdu	RR_NR41_A0_S3
<C>	sdu	RR_NR72_A0_S3
<D>	sdu	RR_NR103_A0_S3
<E>	sdu	RR_NR134_A0_S3
<F>	sdu	RR_NR165_A0_S3
<G>	sdu	RR_NR196_A0_S3
<H>	sdu	RR_NR227_A0_S3
<I>	sdu	RR_NR258_A0_S3
<J>	sdu	RR_NR289_A0_S3
<K>	sdu	RR_NR320_A0_S3
<L>	sdu	RR_NR351_A0_S3
<M>	sdu	RR_NR382_A0_S3
<N>	sdu	RR_NR413_A0_S3
<O>	sdu	RR_NR444_A0_S3
<P>	sdu	RR_NR475_A0_S3
(51)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(52)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS10_NR0_A1_S3
	sdu	RR_D3_NS41_NR0_A1_S3
<C>	sdu	RR_D3_NS72_NR0_A1_S3
<D>	sdu	RR_D3_NS103_NR0_A1_S3
<E>	sdu	RR_D3_NS134_NR0_A1_S3
<F>	sdu	RR_D3_NS165_NR0_A1_S3
<G>	sdu	RR_D3_NS196_NR0_A1_S3
<H>	sdu	RR_D3_NS227_NR0_A1_S3
<I>	sdu	RR_D3_NS258_NR0_A1_S3
<J>	sdu	RR_D3_NS289_NR0_A1_S3
<K>	sdu	RR_D3_NS320_NR0_A1_S3
<L>	sdu	RR_D3_NS351_NR0_A1_S3
<M>	sdu	RR_D3_NS382_NR0_A1_S3
<N>	sdu	RR_D3_NS413_NR0_A1_S3
<O>	sdu	RR_D3_NS444_NR0_A1_S3
<P>	sdu	RR_D3_NS475_NR0_A1_S3
(53)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS10_NR0_A1_S3
	sdu	LLIND_D3_NS41_NR0_A1_S3
<C>	sdu	LLIND_D3_NS72_NR0_A1_S3
<D>	sdu	LLIND_D3_NS103_NR0_A1_S3
<E>	sdu	LLIND_D3_NS134_NR0_A1_S3
<F>	sdu	LLIND_D3_NS165_NR0_A1_S3
<G>	sdu	LLIND_D3_NS196_NR0_A1_S3

<H>	sdu	LLIND_D3_NS227_NR0_A1_S3
<I>	sdu	LLIND_D3_NS258_NR0_A1_S3
<J>	sdu	LLIND_D3_NS289_NR0_A1_S3
<K>	sdu	LLIND_D3_NS320_NR0_A1_S3
<L>	sdu	LLIND_D3_NS351_NR0_A1_S3
<M>	sdu	LLIND_D3_NS382_NR0_A1_S3
<N>	sdu	LLIND_D3_NS413_NR0_A1_S3
<O>	sdu	LLIND_D3_NS444_NR0_A1_S3
<P>	sdu	LLIND_D3_NS475_NR0_A1_S3
(54)	GRR_READY_IND	
(55)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR11_A0_S3
	sdu	RR_NR42_A0_S3
<C>	sdu	RR_NR73_A0_S3
<D>	sdu	RR_NR104_A0_S3
<E>	sdu	RR_NR135_A0_S3
<F>	sdu	RR_NR166_A0_S3
<G>	sdu	RR_NR197_A0_S3
<H>	sdu	RR_NR228_A0_S3
<I>	sdu	RR_NR259_A0_S3
<J>	sdu	RR_NR290_A0_S3
<K>	sdu	RR_NR321_A0_S3
<L>	sdu	RR_NR352_A0_S3
<M>	sdu	RR_NR383_A0_S3
<N>	sdu	RR_NR414_A0_S3
<O>	sdu	RR_NR445_A0_S3
<P>	sdu	RR_NR476_A0_S3
(56)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(57)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS11_NR0_A1_S3
	sdu	RR_D3_NS42_NR0_A1_S3
<C>	sdu	RR_D3_NS73_NR0_A1_S3
<D>	sdu	RR_D3_NS104_NR0_A1_S3
<E>	sdu	RR_D3_NS135_NR0_A1_S3
<F>	sdu	RR_D3_NS166_NR0_A1_S3
<G>	sdu	RR_D3_NS197_NR0_A1_S3
<H>	sdu	RR_D3_NS228_NR0_A1_S3
<I>	sdu	RR_D3_NS259_NR0_A1_S3
<J>	sdu	RR_D3_NS290_NR0_A1_S3
<K>	sdu	RR_D3_NS321_NR0_A1_S3
<L>	sdu	RR_D3_NS352_NR0_A1_S3
<M>	sdu	RR_D3_NS383_NR0_A1_S3
<N>	sdu	RR_D3_NS414_NR0_A1_S3
<O>	sdu	RR_D3_NS445_NR0_A1_S3
<P>	sdu	RR_D3_NS476_NR0_A1_S3

(58)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS11_NR0_A1_S3
	sdu	LLIND_D3_NS42_NR0_A1_S3
<C>	sdu	LLIND_D3_NS73_NR0_A1_S3
<D>	sdu	LLIND_D3_NS104_NR0_A1_S3
<E>	sdu	LLIND_D3_NS135_NR0_A1_S3
<F>	sdu	LLIND_D3_NS166_NR0_A1_S3
<G>	sdu	LLIND_D3_NS197_NR0_A1_S3
<H>	sdu	LLIND_D3_NS228_NR0_A1_S3
<I>	sdu	LLIND_D3_NS259_NR0_A1_S3
<J>	sdu	LLIND_D3_NS290_NR0_A1_S3
<K>	sdu	LLIND_D3_NS321_NR0_A1_S3
<L>	sdu	LLIND_D3_NS352_NR0_A1_S3
<M>	sdu	LLIND_D3_NS383_NR0_A1_S3
<N>	sdu	LLIND_D3_NS414_NR0_A1_S3
<O>	sdu	LLIND_D3_NS445_NR0_A1_S3
<P>	sdu	LLIND_D3_NS476_NR0_A1_S3
(59)	GRR_READY_IND	
(60)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR12_A0_S3
	sdu	RR_NR43_A0_S3
<C>	sdu	RR_NR74_A0_S3
<D>	sdu	RR_NR105_A0_S3
<E>	sdu	RR_NR136_A0_S3
<F>	sdu	RR_NR167_A0_S3
<G>	sdu	RR_NR198_A0_S3
<H>	sdu	RR_NR229_A0_S3
<I>	sdu	RR_NR260_A0_S3
<J>	sdu	RR_NR291_A0_S3
<K>	sdu	RR_NR322_A0_S3
<L>	sdu	RR_NR353_A0_S3
<M>	sdu	RR_NR384_A0_S3
<N>	sdu	RR_NR415_A0_S3
<O>	sdu	RR_NR446_A0_S3
<P>	sdu	RR_NR477_A0_S3
(61)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(62)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS12_NR0_A1_S3
	sdu	RR_D3_NS43_NR0_A1_S3

<C>	sdu	RR_D3_NS74_NR0_A1_S3
<D>	sdu	RR_D3_NS105_NR0_A1_S3
<E>	sdu	RR_D3_NS136_NR0_A1_S3
<F>	sdu	RR_D3_NS167_NR0_A1_S3
<G>	sdu	RR_D3_NS198_NR0_A1_S3
<H>	sdu	RR_D3_NS229_NR0_A1_S3
<I>	sdu	RR_D3_NS260_NR0_A1_S3
<J>	sdu	RR_D3_NS291_NR0_A1_S3
<K>	sdu	RR_D3_NS322_NR0_A1_S3
<L>	sdu	RR_D3_NS353_NR0_A1_S3
<M>	sdu	RR_D3_NS384_NR0_A1_S3
<N>	sdu	RR_D3_NS415_NR0_A1_S3
<O>	sdu	RR_D3_NS446_NR0_A1_S3
<P>	sdu	RR_D3_NS477_NR0_A1_S3
(63)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS12_NR0_A1_S3
	sdu	LLIND_D3_NS43_NR0_A1_S3
<C>	sdu	LLIND_D3_NS74_NR0_A1_S3
<D>	sdu	LLIND_D3_NS105_NR0_A1_S3
<E>	sdu	LLIND_D3_NS136_NR0_A1_S3
<F>	sdu	LLIND_D3_NS167_NR0_A1_S3
<G>	sdu	LLIND_D3_NS198_NR0_A1_S3
<H>	sdu	LLIND_D3_NS229_NR0_A1_S3
<I>	sdu	LLIND_D3_NS260_NR0_A1_S3
<J>	sdu	LLIND_D3_NS291_NR0_A1_S3
<K>	sdu	LLIND_D3_NS322_NR0_A1_S3
<L>	sdu	LLIND_D3_NS353_NR0_A1_S3
<M>	sdu	LLIND_D3_NS384_NR0_A1_S3
<N>	sdu	LLIND_D3_NS415_NR0_A1_S3
<O>	sdu	LLIND_D3_NS446_NR0_A1_S3
<P>	sdu	LLIND_D3_NS477_NR0_A1_S3
(64)	GRR_READY_IND	
(65)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR13_A0_S3
	sdu	RR_NR44_A0_S3
<C>	sdu	RR_NR75_A0_S3
<D>	sdu	RR_NR106_A0_S3
<E>	sdu	RR_NR137_A0_S3
<F>	sdu	RR_NR168_A0_S3
<G>	sdu	RR_NR199_A0_S3
<H>	sdu	RR_NR230_A0_S3
<I>	sdu	RR_NR261_A0_S3

	<J>	sdu	RR_NR292_A0_S3
	<K>	sdu	RR_NR323_A0_S3
	<L>	sdu	RR_NR354_A0_S3
	<M>	sdu	RR_NR385_A0_S3
	<N>	sdu	RR_NR416_A0_S3
	<O>	sdu	RR_NR447_A0_S3
	<P>	sdu	RR_NR478_A0_S3
(66)		LL_GETDATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(67)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_D3_NS13_NR0_A1_S3
		sdu	RR_D3_NS44_NR0_A1_S3
	<C>	sdu	RR_D3_NS75_NR0_A1_S3
	<D>	sdu	RR_D3_NS106_NR0_A1_S3
	<E>	sdu	RR_D3_NS137_NR0_A1_S3
	<F>	sdu	RR_D3_NS168_NR0_A1_S3
	<G>	sdu	RR_D3_NS199_NR0_A1_S3
	<H>	sdu	RR_D3_NS230_NR0_A1_S3
	<I>	sdu	RR_D3_NS261_NR0_A1_S3
	<J>	sdu	RR_D3_NS292_NR0_A1_S3
	<K>	sdu	RR_D3_NS323_NR0_A1_S3
	<L>	sdu	RR_D3_NS354_NR0_A1_S3
	<M>	sdu	RR_D3_NS385_NR0_A1_S3
	<N>	sdu	RR_D3_NS416_NR0_A1_S3
	<O>	sdu	RR_D3_NS447_NR0_A1_S3
	<P>	sdu	RR_D3_NS478_NR0_A1_S3
(68)		LL_DATA_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reserved_data_ind1	NOT_USED
		reserved_data_ind2	NOT_USED
		reserved_data_ind3	NOT_USED
		reserved_data_ind4	NOT_USED
		reserved_data_ind5	NOT_USED
		reserved_data_ind6	NOT_USED
	<A>	sdu	LLIND_D3_NS13_NR0_A1_S3
		sdu	LLIND_D3_NS44_NR0_A1_S3
	<C>	sdu	LLIND_D3_NS75_NR0_A1_S3
	<D>	sdu	LLIND_D3_NS106_NR0_A1_S3
	<E>	sdu	LLIND_D3_NS137_NR0_A1_S3
	<F>	sdu	LLIND_D3_NS168_NR0_A1_S3
	<G>	sdu	LLIND_D3_NS199_NR0_A1_S3
	<H>	sdu	LLIND_D3_NS230_NR0_A1_S3
	<I>	sdu	LLIND_D3_NS261_NR0_A1_S3
	<J>	sdu	LLIND_D3_NS292_NR0_A1_S3
	<K>	sdu	LLIND_D3_NS323_NR0_A1_S3
	<L>	sdu	LLIND_D3_NS354_NR0_A1_S3
	<M>	sdu	LLIND_D3_NS385_NR0_A1_S3
	<N>	sdu	LLIND_D3_NS416_NR0_A1_S3
	<O>	sdu	LLIND_D3_NS447_NR0_A1_S3
	<P>	sdu	LLIND_D3_NS478_NR0_A1_S3
(69)		GRR_READY_IND	

(70)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR14_A0_S3
	sdu	RR_NR45_A0_S3
<C>	sdu	RR_NR76_A0_S3
<D>	sdu	RR_NR107_A0_S3
<E>	sdu	RR_NR138_A0_S3
<F>	sdu	RR_NR169_A0_S3
<G>	sdu	RR_NR200_A0_S3
<H>	sdu	RR_NR231_A0_S3
<I>	sdu	RR_NR262_A0_S3
<J>	sdu	RR_NR293_A0_S3
<K>	sdu	RR_NR324_A0_S3
<L>	sdu	RR_NR355_A0_S3
<M>	sdu	RR_NR386_A0_S3
<N>	sdu	RR_NR417_A0_S3
<O>	sdu	RR_NR448_A0_S3
<P>	sdu	RR_NR479_A0_S3
(71)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(72)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS14_NR0_A1_S3
	sdu	RR_D3_NS45_NR0_A1_S3
<C>	sdu	RR_D3_NS76_NR0_A1_S3
<D>	sdu	RR_D3_NS107_NR0_A1_S3
<E>	sdu	RR_D3_NS138_NR0_A1_S3
<F>	sdu	RR_D3_NS169_NR0_A1_S3
<G>	sdu	RR_D3_NS200_NR0_A1_S3
<H>	sdu	RR_D3_NS231_NR0_A1_S3
<I>	sdu	RR_D3_NS262_NR0_A1_S3
<J>	sdu	RR_D3_NS293_NR0_A1_S3
<K>	sdu	RR_D3_NS324_NR0_A1_S3
<L>	sdu	RR_D3_NS355_NR0_A1_S3
<M>	sdu	RR_D3_NS386_NR0_A1_S3
<N>	sdu	RR_D3_NS417_NR0_A1_S3
<O>	sdu	RR_D3_NS448_NR0_A1_S3
<P>	sdu	RR_D3_NS479_NR0_A1_S3
(73)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS14_NR0_A1_S3
	sdu	LLIND_D3_NS45_NR0_A1_S3
<C>	sdu	LLIND_D3_NS76_NR0_A1_S3

<D>	sdu	LLIND_D3_NS107_NR0_A1_S3
<E>	sdu	LLIND_D3_NS138_NR0_A1_S3
<F>	sdu	LLIND_D3_NS169_NR0_A1_S3
<G>	sdu	LLIND_D3_NS200_NR0_A1_S3
<H>	sdu	LLIND_D3_NS231_NR0_A1_S3
<I>	sdu	LLIND_D3_NS262_NR0_A1_S3
<J>	sdu	LLIND_D3_NS293_NR0_A1_S3
<K>	sdu	LLIND_D3_NS324_NR0_A1_S3
<L>	sdu	LLIND_D3_NS355_NR0_A1_S3
<M>	sdu	LLIND_D3_NS386_NR0_A1_S3
<N>	sdu	LLIND_D3_NS417_NR0_A1_S3
<O>	sdu	LLIND_D3_NS448_NR0_A1_S3
<P>	sdu	LLIND_D3_NS479_NR0_A1_S3
(74)	GRR_READY_IND	
(75)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR15_A0_S3
	sdu	RR_NR46_A0_S3
<C>	sdu	RR_NR77_A0_S3
<D>	sdu	RR_NR108_A0_S3
<E>	sdu	RR_NR139_A0_S3
<F>	sdu	RR_NR170_A0_S3
<G>	sdu	RR_NR201_A0_S3
<H>	sdu	RR_NR232_A0_S3
<I>	sdu	RR_NR263_A0_S3
<J>	sdu	RR_NR294_A0_S3
<K>	sdu	RR_NR325_A0_S3
<L>	sdu	RR_NR356_A0_S3
<M>	sdu	RR_NR387_A0_S3
<N>	sdu	RR_NR418_A0_S3
<O>	sdu	RR_NR449_A0_S3
<P>	sdu	RR_NR480_A0_S3
(76)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(77)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS15_NR0_A1_S3
	sdu	RR_D3_NS46_NR0_A1_S3
<C>	sdu	RR_D3_NS77_NR0_A1_S3
<D>	sdu	RR_D3_NS108_NR0_A1_S3
<E>	sdu	RR_D3_NS139_NR0_A1_S3
<F>	sdu	RR_D3_NS170_NR0_A1_S3
<G>	sdu	RR_D3_NS201_NR0_A1_S3
<H>	sdu	RR_D3_NS232_NR0_A1_S3
<I>	sdu	RR_D3_NS263_NR0_A1_S3
<J>	sdu	RR_D3_NS294_NR0_A1_S3
<K>	sdu	RR_D3_NS325_NR0_A1_S3
<L>	sdu	RR_D3_NS356_NR0_A1_S3
<M>	sdu	RR_D3_NS387_NR0_A1_S3
<N>	sdu	RR_D3_NS418_NR0_A1_S3

<O>	sdu	RR_D3_NS449_NR0_A1_S3
<P>	sdu	RR_D3_NS480_NR0_A1_S3
(78)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS15_NR0_A1_S3
	sdu	LLIND_D3_NS46_NR0_A1_S3
<C>	sdu	LLIND_D3_NS77_NR0_A1_S3
<D>	sdu	LLIND_D3_NS108_NR0_A1_S3
<E>	sdu	LLIND_D3_NS139_NR0_A1_S3
<F>	sdu	LLIND_D3_NS170_NR0_A1_S3
<G>	sdu	LLIND_D3_NS201_NR0_A1_S3
<H>	sdu	LLIND_D3_NS232_NR0_A1_S3
<I>	sdu	LLIND_D3_NS263_NR0_A1_S3
<J>	sdu	LLIND_D3_NS294_NR0_A1_S3
<K>	sdu	LLIND_D3_NS325_NR0_A1_S3
<L>	sdu	LLIND_D3_NS356_NR0_A1_S3
<M>	sdu	LLIND_D3_NS387_NR0_A1_S3
<N>	sdu	LLIND_D3_NS418_NR0_A1_S3
<O>	sdu	LLIND_D3_NS449_NR0_A1_S3
<P>	sdu	LLIND_D3_NS480_NR0_A1_S3
(79)	GRR_READY_IND	
(80)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR16_A0_S3
	sdu	RR_NR47_A0_S3
<C>	sdu	RR_NR78_A0_S3
<D>	sdu	RR_NR109_A0_S3
<E>	sdu	RR_NR140_A0_S3
<F>	sdu	RR_NR171_A0_S3
<G>	sdu	RR_NR202_A0_S3
<H>	sdu	RR_NR233_A0_S3
<I>	sdu	RR_NR264_A0_S3
<J>	sdu	RR_NR295_A0_S3
<K>	sdu	RR_NR326_A0_S3
<L>	sdu	RR_NR357_A0_S3
<M>	sdu	RR_NR388_A0_S3
<N>	sdu	RR_NR419_A0_S3
<O>	sdu	RR_NR450_A0_S3
<P>	sdu	RR_NR481_A0_S3
(81)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

(82)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS16_NR0_A1_S3
	sdu	RR_D3_NS47_NR0_A1_S3
<C>	sdu	RR_D3_NS78_NR0_A1_S3
<D>	sdu	RR_D3_NS109_NR0_A1_S3
<E>	sdu	RR_D3_NS140_NR0_A1_S3
<F>	sdu	RR_D3_NS171_NR0_A1_S3
<G>	sdu	RR_D3_NS202_NR0_A1_S3
<H>	sdu	RR_D3_NS233_NR0_A1_S3
<I>	sdu	RR_D3_NS264_NR0_A1_S3
<J>	sdu	RR_D3_NS295_NR0_A1_S3
<K>	sdu	RR_D3_NS326_NR0_A1_S3
<L>	sdu	RR_D3_NS357_NR0_A1_S3
<M>	sdu	RR_D3_NS388_NR0_A1_S3
<N>	sdu	RR_D3_NS419_NR0_A1_S3
<O>	sdu	RR_D3_NS450_NR0_A1_S3
<P>	sdu	RR_D3_NS481_NR0_A1_S3
(83)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS16_NR0_A1_S3
	sdu	LLIND_D3_NS47_NR0_A1_S3
<C>	sdu	LLIND_D3_NS78_NR0_A1_S3
<D>	sdu	LLIND_D3_NS109_NR0_A1_S3
<E>	sdu	LLIND_D3_NS140_NR0_A1_S3
<F>	sdu	LLIND_D3_NS171_NR0_A1_S3
<G>	sdu	LLIND_D3_NS202_NR0_A1_S3
<H>	sdu	LLIND_D3_NS233_NR0_A1_S3
<I>	sdu	LLIND_D3_NS264_NR0_A1_S3
<J>	sdu	LLIND_D3_NS295_NR0_A1_S3
<K>	sdu	LLIND_D3_NS326_NR0_A1_S3
<L>	sdu	LLIND_D3_NS357_NR0_A1_S3
<M>	sdu	LLIND_D3_NS388_NR0_A1_S3
<N>	sdu	LLIND_D3_NS419_NR0_A1_S3
<O>	sdu	LLIND_D3_NS450_NR0_A1_S3
<P>	sdu	LLIND_D3_NS481_NR0_A1_S3
(84)	GRR_READY_IND	
(85)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR17_A0_S3
	sdu	RR_NR48_A0_S3
<C>	sdu	RR_NR79_A0_S3
<D>	sdu	RR_NR110_A0_S3
<E>	sdu	RR_NR141_A0_S3

<F>	sdu	RR_NR172_A0_S3
<G>	sdu	RR_NR203_A0_S3
<H>	sdu	RR_NR234_A0_S3
<I>	sdu	RR_NR265_A0_S3
<J>	sdu	RR_NR296_A0_S3
<K>	sdu	RR_NR327_A0_S3
<L>	sdu	RR_NR358_A0_S3
<M>	sdu	RR_NR389_A0_S3
<N>	sdu	RR_NR420_A0_S3
<O>	sdu	RR_NR451_A0_S3
<P>	sdu	RR_NR482_A0_S3
(86)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(87)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS17_NR0_A1_S3
	sdu	RR_D3_NS48_NR0_A1_S3
<C>	sdu	RR_D3_NS79_NR0_A1_S3
<D>	sdu	RR_D3_NS110_NR0_A1_S3
<E>	sdu	RR_D3_NS141_NR0_A1_S3
<F>	sdu	RR_D3_NS172_NR0_A1_S3
<G>	sdu	RR_D3_NS203_NR0_A1_S3
<H>	sdu	RR_D3_NS234_NR0_A1_S3
<I>	sdu	RR_D3_NS265_NR0_A1_S3
<J>	sdu	RR_D3_NS296_NR0_A1_S3
<K>	sdu	RR_D3_NS327_NR0_A1_S3
<L>	sdu	RR_D3_NS358_NR0_A1_S3
<M>	sdu	RR_D3_NS389_NR0_A1_S3
<N>	sdu	RR_D3_NS420_NR0_A1_S3
<O>	sdu	RR_D3_NS451_NR0_A1_S3
<P>	sdu	RR_D3_NS482_NR0_A1_S3
(88)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS17_NR0_A1_S3
	sdu	LLIND_D3_NS48_NR0_A1_S3
<C>	sdu	LLIND_D3_NS79_NR0_A1_S3
<D>	sdu	LLIND_D3_NS110_NR0_A1_S3
<E>	sdu	LLIND_D3_NS141_NR0_A1_S3
<F>	sdu	LLIND_D3_NS172_NR0_A1_S3
<G>	sdu	LLIND_D3_NS203_NR0_A1_S3
<H>	sdu	LLIND_D3_NS234_NR0_A1_S3
<I>	sdu	LLIND_D3_NS265_NR0_A1_S3
<J>	sdu	LLIND_D3_NS296_NR0_A1_S3
<K>	sdu	LLIND_D3_NS327_NR0_A1_S3
<L>	sdu	LLIND_D3_NS358_NR0_A1_S3
<M>	sdu	LLIND_D3_NS389_NR0_A1_S3
<N>	sdu	LLIND_D3_NS420_NR0_A1_S3

<O>	sdu	LLIND_D3_NS451_NR0_A1_S3
<P>	sdu	LLIND_D3_NS482_NR0_A1_S3
(89)	GRR_READY_IND	
(90)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR18_A0_S3
	sdu	RR_NR49_A0_S3
<C>	sdu	RR_NR80_A0_S3
<D>	sdu	RR_NR111_A0_S3
<E>	sdu	RR_NR142_A0_S3
<F>	sdu	RR_NR173_A0_S3
<G>	sdu	RR_NR204_A0_S3
<H>	sdu	RR_NR235_A0_S3
<I>	sdu	RR_NR266_A0_S3
<J>	sdu	RR_NR297_A0_S3
<K>	sdu	RR_NR328_A0_S3
<L>	sdu	RR_NR359_A0_S3
<M>	sdu	RR_NR390_A0_S3
<N>	sdu	RR_NR421_A0_S3
<O>	sdu	RR_NR452_A0_S3
<P>	sdu	RR_NR483_A0_S3
(91)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(92)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS18_NR0_A1_S3
	sdu	RR_D3_NS49_NR0_A1_S3
<C>	sdu	RR_D3_NS80_NR0_A1_S3
<D>	sdu	RR_D3_NS111_NR0_A1_S3
<E>	sdu	RR_D3_NS142_NR0_A1_S3
<F>	sdu	RR_D3_NS173_NR0_A1_S3
<G>	sdu	RR_D3_NS204_NR0_A1_S3
<H>	sdu	RR_D3_NS235_NR0_A1_S3
<I>	sdu	RR_D3_NS266_NR0_A1_S3
<J>	sdu	RR_D3_NS297_NR0_A1_S3
<K>	sdu	RR_D3_NS328_NR0_A1_S3
<L>	sdu	RR_D3_NS359_NR0_A1_S3
<M>	sdu	RR_D3_NS390_NR0_A1_S3
<N>	sdu	RR_D3_NS421_NR0_A1_S3
<O>	sdu	RR_D3_NS452_NR0_A1_S3
<P>	sdu	RR_D3_NS483_NR0_A1_S3
(93)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED

	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS18_NR0_A1_S3
	sdu	LLIND_D3_NS49_NR0_A1_S3
<C>	sdu	LLIND_D3_NS80_NR0_A1_S3
<D>	sdu	LLIND_D3_NS111_NR0_A1_S3
<E>	sdu	LLIND_D3_NS142_NR0_A1_S3
<F>	sdu	LLIND_D3_NS173_NR0_A1_S3
<G>	sdu	LLIND_D3_NS204_NR0_A1_S3
<H>	sdu	LLIND_D3_NS235_NR0_A1_S3
<I>	sdu	LLIND_D3_NS266_NR0_A1_S3
<J>	sdu	LLIND_D3_NS297_NR0_A1_S3
<K>	sdu	LLIND_D3_NS328_NR0_A1_S3
<L>	sdu	LLIND_D3_NS359_NR0_A1_S3
<M>	sdu	LLIND_D3_NS390_NR0_A1_S3
<N>	sdu	LLIND_D3_NS421_NR0_A1_S3
<O>	sdu	LLIND_D3_NS452_NR0_A1_S3
<P>	sdu	LLIND_D3_NS483_NR0_A1_S3
(94)	GRR_READY_IND	
(95)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR19_A0_S3
	sdu	RR_NR50_A0_S3
<C>	sdu	RR_NR81_A0_S3
<D>	sdu	RR_NR112_A0_S3
<E>	sdu	RR_NR143_A0_S3
<F>	sdu	RR_NR174_A0_S3
<G>	sdu	RR_NR205_A0_S3
<H>	sdu	RR_NR236_A0_S3
<I>	sdu	RR_NR267_A0_S3
<J>	sdu	RR_NR298_A0_S3
<K>	sdu	RR_NR329_A0_S3
<L>	sdu	RR_NR360_A0_S3
<M>	sdu	RR_NR391_A0_S3
<N>	sdu	RR_NR422_A0_S3
<O>	sdu	RR_NR453_A0_S3
<P>	sdu	RR_NR484_A0_S3
(96)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(97)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS19_NR0_A1_S3
	sdu	RR_D3_NS50_NR0_A1_S3
<C>	sdu	RR_D3_NS81_NR0_A1_S3
<D>	sdu	RR_D3_NS112_NR0_A1_S3
<E>	sdu	RR_D3_NS143_NR0_A1_S3
<F>	sdu	RR_D3_NS174_NR0_A1_S3
<G>	sdu	RR_D3_NS205_NR0_A1_S3
<H>	sdu	RR_D3_NS236_NR0_A1_S3
<I>	sdu	RR_D3_NS267_NR0_A1_S3
<J>	sdu	RR_D3_NS298_NR0_A1_S3

<K>	sdu	RR_D3_NS329_NR0_A1_S3
<L>	sdu	RR_D3_NS360_NR0_A1_S3
<M>	sdu	RR_D3_NS391_NR0_A1_S3
<N>	sdu	RR_D3_NS422_NR0_A1_S3
<O>	sdu	RR_D3_NS453_NR0_A1_S3
<P>	sdu	RR_D3_NS484_NR0_A1_S3
(98)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS19_NR0_A1_S3
	sdu	LLIND_D3_NS50_NR0_A1_S3
<C>	sdu	LLIND_D3_NS81_NR0_A1_S3
<D>	sdu	LLIND_D3_NS112_NR0_A1_S3
<E>	sdu	LLIND_D3_NS143_NR0_A1_S3
<F>	sdu	LLIND_D3_NS174_NR0_A1_S3
<G>	sdu	LLIND_D3_NS205_NR0_A1_S3
<H>	sdu	LLIND_D3_NS236_NR0_A1_S3
<I>	sdu	LLIND_D3_NS267_NR0_A1_S3
<J>	sdu	LLIND_D3_NS298_NR0_A1_S3
<K>	sdu	LLIND_D3_NS329_NR0_A1_S3
<L>	sdu	LLIND_D3_NS360_NR0_A1_S3
<M>	sdu	LLIND_D3_NS391_NR0_A1_S3
<N>	sdu	LLIND_D3_NS422_NR0_A1_S3
<O>	sdu	LLIND_D3_NS453_NR0_A1_S3
<P>	sdu	LLIND_D3_NS484_NR0_A1_S3
(99)	GRR_READY_IND	
(100)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR20_A0_S3
	sdu	RR_NR51_A0_S3
<C>	sdu	RR_NR82_A0_S3
<D>	sdu	RR_NR113_A0_S3
<E>	sdu	RR_NR144_A0_S3
<F>	sdu	RR_NR175_A0_S3
<G>	sdu	RR_NR206_A0_S3
<H>	sdu	RR_NR237_A0_S3
<I>	sdu	RR_NR268_A0_S3
<J>	sdu	RR_NR299_A0_S3
<K>	sdu	RR_NR330_A0_S3
<L>	sdu	RR_NR361_A0_S3
<M>	sdu	RR_NR392_A0_S3
<N>	sdu	RR_NR423_A0_S3
<O>	sdu	RR_NR454_A0_S3
<P>	sdu	RR_NR485_A0_S3

(101)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(102)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS20_NR0_A1_S3
	sdu	RR_D3_NS51_NR0_A1_S3
<C>	sdu	RR_D3_NS82_NR0_A1_S3
<D>	sdu	RR_D3_NS113_NR0_A1_S3
<E>	sdu	RR_D3_NS144_NR0_A1_S3
<F>	sdu	RR_D3_NS175_NR0_A1_S3
<G>	sdu	RR_D3_NS206_NR0_A1_S3
<H>	sdu	RR_D3_NS237_NR0_A1_S3
<I>	sdu	RR_D3_NS268_NR0_A1_S3
<J>	sdu	RR_D3_NS299_NR0_A1_S3
<K>	sdu	RR_D3_NS330_NR0_A1_S3
<L>	sdu	RR_D3_NS361_NR0_A1_S3
<M>	sdu	RR_D3_NS392_NR0_A1_S3
<N>	sdu	RR_D3_NS423_NR0_A1_S3
<O>	sdu	RR_D3_NS454_NR0_A1_S3
<P>	sdu	RR_D3_NS485_NR0_A1_S3
(103)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS20_NR0_A1_S3
	sdu	LLIND_D3_NS51_NR0_A1_S3
<C>	sdu	LLIND_D3_NS82_NR0_A1_S3
<D>	sdu	LLIND_D3_NS113_NR0_A1_S3
<E>	sdu	LLIND_D3_NS144_NR0_A1_S3
<F>	sdu	LLIND_D3_NS175_NR0_A1_S3
<G>	sdu	LLIND_D3_NS206_NR0_A1_S3
<H>	sdu	LLIND_D3_NS237_NR0_A1_S3
<I>	sdu	LLIND_D3_NS268_NR0_A1_S3
<J>	sdu	LLIND_D3_NS299_NR0_A1_S3
<K>	sdu	LLIND_D3_NS330_NR0_A1_S3
<L>	sdu	LLIND_D3_NS361_NR0_A1_S3
<M>	sdu	LLIND_D3_NS392_NR0_A1_S3
<N>	sdu	LLIND_D3_NS423_NR0_A1_S3
<O>	sdu	LLIND_D3_NS454_NR0_A1_S3
<P>	sdu	LLIND_D3_NS485_NR0_A1_S3
(104)	GRR_READY_IND	
(105)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR21_A0_S3
	sdu	RR_NR52_A0_S3

<C>	sdu	RR_NR83_A0_S3
<D>	sdu	RR_NR114_A0_S3
<E>	sdu	RR_NR145_A0_S3
<F>	sdu	RR_NR176_A0_S3
<G>	sdu	RR_NR207_A0_S3
<H>	sdu	RR_NR238_A0_S3
<I>	sdu	RR_NR269_A0_S3
<J>	sdu	RR_NR300_A0_S3
<K>	sdu	RR_NR331_A0_S3
<L>	sdu	RR_NR362_A0_S3
<M>	sdu	RR_NR393_A0_S3
<N>	sdu	RR_NR424_A0_S3
<O>	sdu	RR_NR455_A0_S3
<P>	sdu	RR_NR486_A0_S3
(106)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(107)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS21_NR0_A1_S3
	sdu	RR_D3_NS52_NR0_A1_S3
<C>	sdu	RR_D3_NS83_NR0_A1_S3
<D>	sdu	RR_D3_NS114_NR0_A1_S3
<E>	sdu	RR_D3_NS145_NR0_A1_S3
<F>	sdu	RR_D3_NS176_NR0_A1_S3
<G>	sdu	RR_D3_NS207_NR0_A1_S3
<H>	sdu	RR_D3_NS238_NR0_A1_S3
<I>	sdu	RR_D3_NS269_NR0_A1_S3
<J>	sdu	RR_D3_NS300_NR0_A1_S3
<K>	sdu	RR_D3_NS331_NR0_A1_S3
<L>	sdu	RR_D3_NS362_NR0_A1_S3
<M>	sdu	RR_D3_NS393_NR0_A1_S3
<N>	sdu	RR_D3_NS424_NR0_A1_S3
<O>	sdu	RR_D3_NS455_NR0_A1_S3
<P>	sdu	RR_D3_NS486_NR0_A1_S3
(108)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS21_NR0_A1_S3
	sdu	LLIND_D3_NS52_NR0_A1_S3
<C>	sdu	LLIND_D3_NS83_NR0_A1_S3
<D>	sdu	LLIND_D3_NS114_NR0_A1_S3
<E>	sdu	LLIND_D3_NS145_NR0_A1_S3
<F>	sdu	LLIND_D3_NS176_NR0_A1_S3
<G>	sdu	LLIND_D3_NS207_NR0_A1_S3
<H>	sdu	LLIND_D3_NS238_NR0_A1_S3
<I>	sdu	LLIND_D3_NS269_NR0_A1_S3
<J>	sdu	LLIND_D3_NS300_NR0_A1_S3
<K>	sdu	LLIND_D3_NS331_NR0_A1_S3
<L>	sdu	LLIND_D3_NS362_NR0_A1_S3

<M>	sdu	LLIND_D3_NS393_NR0_A1_S3
<N>	sdu	LLIND_D3_NS424_NR0_A1_S3
<O>	sdu	LLIND_D3_NS455_NR0_A1_S3
<P>	sdu	LLIND_D3_NS486_NR0_A1_S3
(109)	GRR_READY_IND	
(110)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR22_A0_S3
	sdu	RR_NR53_A0_S3
<C>	sdu	RR_NR84_A0_S3
<D>	sdu	RR_NR115_A0_S3
<E>	sdu	RR_NR146_A0_S3
<F>	sdu	RR_NR177_A0_S3
<G>	sdu	RR_NR208_A0_S3
<H>	sdu	RR_NR239_A0_S3
<I>	sdu	RR_NR270_A0_S3
<J>	sdu	RR_NR301_A0_S3
<K>	sdu	RR_NR332_A0_S3
<L>	sdu	RR_NR363_A0_S3
<M>	sdu	RR_NR394_A0_S3
<N>	sdu	RR_NR425_A0_S3
<O>	sdu	RR_NR456_A0_S3
<P>	sdu	RR_NR487_A0_S3
(111)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(112)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS22_NR0_A1_S3
	sdu	RR_D3_NS53_NR0_A1_S3
<C>	sdu	RR_D3_NS84_NR0_A1_S3
<D>	sdu	RR_D3_NS115_NR0_A1_S3
<E>	sdu	RR_D3_NS146_NR0_A1_S3
<F>	sdu	RR_D3_NS177_NR0_A1_S3
<G>	sdu	RR_D3_NS208_NR0_A1_S3
<H>	sdu	RR_D3_NS239_NR0_A1_S3
<I>	sdu	RR_D3_NS270_NR0_A1_S3
<J>	sdu	RR_D3_NS301_NR0_A1_S3
<K>	sdu	RR_D3_NS332_NR0_A1_S3
<L>	sdu	RR_D3_NS363_NR0_A1_S3
<M>	sdu	RR_D3_NS394_NR0_A1_S3
<N>	sdu	RR_D3_NS425_NR0_A1_S3
<O>	sdu	RR_D3_NS456_NR0_A1_S3
<P>	sdu	RR_D3_NS487_NR0_A1_S3
(113)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED

	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS22_NR0_A1_S3
	sdu	LLIND_D3_NS53_NR0_A1_S3
<C>	sdu	LLIND_D3_NS84_NR0_A1_S3
<D>	sdu	LLIND_D3_NS115_NR0_A1_S3
<E>	sdu	LLIND_D3_NS146_NR0_A1_S3
<F>	sdu	LLIND_D3_NS177_NR0_A1_S3
<G>	sdu	LLIND_D3_NS208_NR0_A1_S3
<H>	sdu	LLIND_D3_NS239_NR0_A1_S3
<I>	sdu	LLIND_D3_NS270_NR0_A1_S3
<J>	sdu	LLIND_D3_NS301_NR0_A1_S3
<K>	sdu	LLIND_D3_NS332_NR0_A1_S3
<L>	sdu	LLIND_D3_NS363_NR0_A1_S3
<M>	sdu	LLIND_D3_NS394_NR0_A1_S3
<N>	sdu	LLIND_D3_NS425_NR0_A1_S3
<O>	sdu	LLIND_D3_NS456_NR0_A1_S3
<P>	sdu	LLIND_D3_NS487_NR0_A1_S3
(114)	GRR_READY_IND	
(115)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR23_A0_S3
	sdu	RR_NR54_A0_S3
<C>	sdu	RR_NR85_A0_S3
<D>	sdu	RR_NR116_A0_S3
<E>	sdu	RR_NR147_A0_S3
<F>	sdu	RR_NR178_A0_S3
<G>	sdu	RR_NR209_A0_S3
<H>	sdu	RR_NR240_A0_S3
<I>	sdu	RR_NR271_A0_S3
<J>	sdu	RR_NR302_A0_S3
<K>	sdu	RR_NR333_A0_S3
<L>	sdu	RR_NR364_A0_S3
<M>	sdu	RR_NR395_A0_S3
<N>	sdu	RR_NR426_A0_S3
<O>	sdu	RR_NR457_A0_S3
<P>	sdu	RR_NR488_A0_S3
(116)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(117)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS23_NR0_A1_S3
	sdu	RR_D3_NS54_NR0_A1_S3
<C>	sdu	RR_D3_NS85_NR0_A1_S3
<D>	sdu	RR_D3_NS116_NR0_A1_S3
<E>	sdu	RR_D3_NS147_NR0_A1_S3
<F>	sdu	RR_D3_NS178_NR0_A1_S3
<G>	sdu	RR_D3_NS209_NR0_A1_S3
<H>	sdu	RR_D3_NS240_NR0_A1_S3

<I>	sdu	RR_D3_NS271_NR0_A1_S3
<J>	sdu	RR_D3_NS302_NR0_A1_S3
<K>	sdu	RR_D3_NS333_NR0_A1_S3
<L>	sdu	RR_D3_NS364_NR0_A1_S3
<M>	sdu	RR_D3_NS395_NR0_A1_S3
<N>	sdu	RR_D3_NS426_NR0_A1_S3
<O>	sdu	RR_D3_NS457_NR0_A1_S3
<P>	sdu	RR_D3_NS488_NR0_A1_S3
(118)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS23_NR0_A1_S3
	sdu	LLIND_D3_NS54_NR0_A1_S3
<C>	sdu	LLIND_D3_NS85_NR0_A1_S3
<D>	sdu	LLIND_D3_NS116_NR0_A1_S3
<E>	sdu	LLIND_D3_NS147_NR0_A1_S3
<F>	sdu	LLIND_D3_NS178_NR0_A1_S3
<G>	sdu	LLIND_D3_NS209_NR0_A1_S3
<H>	sdu	LLIND_D3_NS240_NR0_A1_S3
<I>	sdu	LLIND_D3_NS271_NR0_A1_S3
<J>	sdu	LLIND_D3_NS302_NR0_A1_S3
<K>	sdu	LLIND_D3_NS333_NR0_A1_S3
<L>	sdu	LLIND_D3_NS364_NR0_A1_S3
<M>	sdu	LLIND_D3_NS395_NR0_A1_S3
<N>	sdu	LLIND_D3_NS426_NR0_A1_S3
<O>	sdu	LLIND_D3_NS457_NR0_A1_S3
<P>	sdu	LLIND_D3_NS488_NR0_A1_S3
(119)	GRR_READY_IND	
(120)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR24_A0_S3
	sdu	RR_NR55_A0_S3
<C>	sdu	RR_NR86_A0_S3
<D>	sdu	RR_NR117_A0_S3
<E>	sdu	RR_NR148_A0_S3
<F>	sdu	RR_NR179_A0_S3
<G>	sdu	RR_NR210_A0_S3
<H>	sdu	RR_NR241_A0_S3
<I>	sdu	RR_NR272_A0_S3
<J>	sdu	RR_NR303_A0_S3
<K>	sdu	RR_NR334_A0_S3
<L>	sdu	RR_NR365_A0_S3
<M>	sdu	RR_NR396_A0_S3
<N>	sdu	RR_NR427_A0_S3

	<O>	sdu	RR_NR458_A0_S3
	<P>	sdu	RR_NR489_A0_S3
(121)		LL_GETDATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(122)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_D3_NS24_NR0_A1_S3
		sdu	RR_D3_NS55_NR0_A1_S3
	<C>	sdu	RR_D3_NS86_NR0_A1_S3
	<D>	sdu	RR_D3_NS117_NR0_A1_S3
	<E>	sdu	RR_D3_NS148_NR0_A1_S3
	<F>	sdu	RR_D3_NS179_NR0_A1_S3
	<G>	sdu	RR_D3_NS210_NR0_A1_S3
	<H>	sdu	RR_D3_NS241_NR0_A1_S3
	<I>	sdu	RR_D3_NS272_NR0_A1_S3
	<J>	sdu	RR_D3_NS303_NR0_A1_S3
	<K>	sdu	RR_D3_NS334_NR0_A1_S3
	<L>	sdu	RR_D3_NS365_NR0_A1_S3
	<M>	sdu	RR_D3_NS396_NR0_A1_S3
	<N>	sdu	RR_D3_NS427_NR0_A1_S3
	<O>	sdu	RR_D3_NS458_NR0_A1_S3
	<P>	sdu	RR_D3_NS489_NR0_A1_S3
(123)		LL_DATA_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reserved_data_ind1	NOT_USED
		reserved_data_ind2	NOT_USED
		reserved_data_ind3	NOT_USED
		reserved_data_ind4	NOT_USED
		reserved_data_ind5	NOT_USED
		reserved_data_ind6	NOT_USED
	<A>	sdu	LLIND_D3_NS24_NR0_A1_S3
		sdu	LLIND_D3_NS55_NR0_A1_S3
	<C>	sdu	LLIND_D3_NS86_NR0_A1_S3
	<D>	sdu	LLIND_D3_NS117_NR0_A1_S3
	<E>	sdu	LLIND_D3_NS148_NR0_A1_S3
	<F>	sdu	LLIND_D3_NS179_NR0_A1_S3
	<G>	sdu	LLIND_D3_NS210_NR0_A1_S3
	<H>	sdu	LLIND_D3_NS241_NR0_A1_S3
	<I>	sdu	LLIND_D3_NS272_NR0_A1_S3
	<J>	sdu	LLIND_D3_NS303_NR0_A1_S3
	<K>	sdu	LLIND_D3_NS334_NR0_A1_S3
	<L>	sdu	LLIND_D3_NS365_NR0_A1_S3
	<M>	sdu	LLIND_D3_NS396_NR0_A1_S3
	<N>	sdu	LLIND_D3_NS427_NR0_A1_S3
	<O>	sdu	LLIND_D3_NS458_NR0_A1_S3
	<P>	sdu	LLIND_D3_NS489_NR0_A1_S3
(124)		GRR_READY_IND	
(125)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKEQSUB
		radio_prio	GRR_RADIO_PRIO_1
		cause	GRR_DTACS_DEF

	reserved_data_req	NOT_USED
<A>	sdu	RR_NR25_A0_S3
	sdu	RR_NR56_A0_S3
<C>	sdu	RR_NR87_A0_S3
<D>	sdu	RR_NR118_A0_S3
<E>	sdu	RR_NR149_A0_S3
<F>	sdu	RR_NR180_A0_S3
<G>	sdu	RR_NR211_A0_S3
<H>	sdu	RR_NR242_A0_S3
<I>	sdu	RR_NR273_A0_S3
<J>	sdu	RR_NR304_A0_S3
<K>	sdu	RR_NR335_A0_S3
<L>	sdu	RR_NR366_A0_S3
<M>	sdu	RR_NR397_A0_S3
<N>	sdu	RR_NR428_A0_S3
<O>	sdu	RR_NR459_A0_S3
<P>	sdu	RR_NR490_A0_S3
(126)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(127)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS25_NR0_A1_S3
	sdu	RR_D3_NS56_NR0_A1_S3
<C>	sdu	RR_D3_NS87_NR0_A1_S3
<D>	sdu	RR_D3_NS118_NR0_A1_S3
<E>	sdu	RR_D3_NS149_NR0_A1_S3
<F>	sdu	RR_D3_NS180_NR0_A1_S3
<G>	sdu	RR_D3_NS211_NR0_A1_S3
<H>	sdu	RR_D3_NS242_NR0_A1_S3
<I>	sdu	RR_D3_NS273_NR0_A1_S3
<J>	sdu	RR_D3_NS304_NR0_A1_S3
<K>	sdu	RR_D3_NS335_NR0_A1_S3
<L>	sdu	RR_D3_NS366_NR0_A1_S3
<M>	sdu	RR_D3_NS397_NR0_A1_S3
<N>	sdu	RR_D3_NS428_NR0_A1_S3
<O>	sdu	RR_D3_NS459_NR0_A1_S3
<P>	sdu	RR_D3_NS490_NR0_A1_S3
(128)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS25_NR0_A1_S3
	sdu	LLIND_D3_NS56_NR0_A1_S3
<C>	sdu	LLIND_D3_NS87_NR0_A1_S3
<D>	sdu	LLIND_D3_NS118_NR0_A1_S3
<E>	sdu	LLIND_D3_NS149_NR0_A1_S3
<F>	sdu	LLIND_D3_NS180_NR0_A1_S3
<G>	sdu	LLIND_D3_NS211_NR0_A1_S3
<H>	sdu	LLIND_D3_NS242_NR0_A1_S3
<I>	sdu	LLIND_D3_NS273_NR0_A1_S3

	<J>	sdu	LLIND_D3_NS304_NR0_A1_S3
	<K>	sdu	LLIND_D3_NS335_NR0_A1_S3
	<L>	sdu	LLIND_D3_NS366_NR0_A1_S3
	<M>	sdu	LLIND_D3_NS397_NR0_A1_S3
	<N>	sdu	LLIND_D3_NS428_NR0_A1_S3
	<O>	sdu	LLIND_D3_NS459_NR0_A1_S3
	<P>	sdu	LLIND_D3_NS490_NR0_A1_S3
(129)		GRR_READY_IND	
(130)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKSUB
		radio_prio	GRR_RADIO_PRIO_1
		cause	GRR_DTACS_DEF
		reserved_data_req	NOT_USED
	<A>	sdu	RR_NR26_A0_S3
		sdu	RR_NR57_A0_S3
	<C>	sdu	RR_NR88_A0_S3
	<D>	sdu	RR_NR119_A0_S3
	<E>	sdu	RR_NR150_A0_S3
	<F>	sdu	RR_NR181_A0_S3
	<G>	sdu	RR_NR212_A0_S3
	<H>	sdu	RR_NR243_A0_S3
	<I>	sdu	RR_NR274_A0_S3
	<J>	sdu	RR_NR305_A0_S3
	<K>	sdu	RR_NR336_A0_S3
	<L>	sdu	RR_NR367_A0_S3
	<M>	sdu	RR_NR398_A0_S3
	<N>	sdu	RR_NR429_A0_S3
	<O>	sdu	RR_NR460_A0_S3
	<P>	sdu	RR_NR491_A0_S3
(131)		LL_GETDATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(132)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_D3_NS26_NR0_A1_S3
		sdu	RR_D3_NS57_NR0_A1_S3
	<C>	sdu	RR_D3_NS88_NR0_A1_S3
	<D>	sdu	RR_D3_NS119_NR0_A1_S3
	<E>	sdu	RR_D3_NS150_NR0_A1_S3
	<F>	sdu	RR_D3_NS181_NR0_A1_S3
	<G>	sdu	RR_D3_NS212_NR0_A1_S3
	<H>	sdu	RR_D3_NS243_NR0_A1_S3
	<I>	sdu	RR_D3_NS274_NR0_A1_S3
	<J>	sdu	RR_D3_NS305_NR0_A1_S3
	<K>	sdu	RR_D3_NS336_NR0_A1_S3
	<L>	sdu	RR_D3_NS367_NR0_A1_S3
	<M>	sdu	RR_D3_NS398_NR0_A1_S3
	<N>	sdu	RR_D3_NS429_NR0_A1_S3
	<O>	sdu	RR_D3_NS460_NR0_A1_S3
	<P>	sdu	RR_D3_NS491_NR0_A1_S3
(133)		LL_DATA_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1

	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS26_NR0_A1_S3
	sdu	LLIND_D3_NS57_NR0_A1_S3
<C>	sdu	LLIND_D3_NS88_NR0_A1_S3
<D>	sdu	LLIND_D3_NS119_NR0_A1_S3
<E>	sdu	LLIND_D3_NS150_NR0_A1_S3
<F>	sdu	LLIND_D3_NS181_NR0_A1_S3
<G>	sdu	LLIND_D3_NS212_NR0_A1_S3
<H>	sdu	LLIND_D3_NS243_NR0_A1_S3
<I>	sdu	LLIND_D3_NS274_NR0_A1_S3
<J>	sdu	LLIND_D3_NS305_NR0_A1_S3
<K>	sdu	LLIND_D3_NS336_NR0_A1_S3
<L>	sdu	LLIND_D3_NS367_NR0_A1_S3
<M>	sdu	LLIND_D3_NS398_NR0_A1_S3
<N>	sdu	LLIND_D3_NS429_NR0_A1_S3
<O>	sdu	LLIND_D3_NS460_NR0_A1_S3
<P>	sdu	LLIND_D3_NS491_NR0_A1_S3
(134)	GRR_READY_IND	
(135)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR27_A0_S3
	sdu	RR_NR58_A0_S3
<C>	sdu	RR_NR89_A0_S3
<D>	sdu	RR_NR120_A0_S3
<E>	sdu	RR_NR151_A0_S3
<F>	sdu	RR_NR182_A0_S3
<G>	sdu	RR_NR213_A0_S3
<H>	sdu	RR_NR244_A0_S3
<I>	sdu	RR_NR275_A0_S3
<J>	sdu	RR_NR306_A0_S3
<K>	sdu	RR_NR337_A0_S3
<L>	sdu	RR_NR368_A0_S3
<M>	sdu	RR_NR399_A0_S3
<N>	sdu	RR_NR430_A0_S3
<O>	sdu	RR_NR461_A0_S3
<P>	sdu	RR_NR492_A0_S3
(136)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(137)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS27_NR0_A1_S3
	sdu	RR_D3_NS58_NR0_A1_S3
<C>	sdu	RR_D3_NS89_NR0_A1_S3
<D>	sdu	RR_D3_NS120_NR0_A1_S3
<E>	sdu	RR_D3_NS151_NR0_A1_S3

<F>	sdu	RR_D3_NS182_NR0_A1_S3
<G>	sdu	RR_D3_NS213_NR0_A1_S3
<H>	sdu	RR_D3_NS244_NR0_A1_S3
<I>	sdu	RR_D3_NS275_NR0_A1_S3
<J>	sdu	RR_D3_NS306_NR0_A1_S3
<K>	sdu	RR_D3_NS337_NR0_A1_S3
<L>	sdu	RR_D3_NS368_NR0_A1_S3
<M>	sdu	RR_D3_NS399_NR0_A1_S3
<N>	sdu	RR_D3_NS430_NR0_A1_S3
<O>	sdu	RR_D3_NS461_NR0_A1_S3
<P>	sdu	RR_D3_NS492_NR0_A1_S3
(138)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS27_NR0_A1_S3
	sdu	LLIND_D3_NS58_NR0_A1_S3
<C>	sdu	LLIND_D3_NS89_NR0_A1_S3
<D>	sdu	LLIND_D3_NS120_NR0_A1_S3
<E>	sdu	LLIND_D3_NS151_NR0_A1_S3
<F>	sdu	LLIND_D3_NS182_NR0_A1_S3
<G>	sdu	LLIND_D3_NS213_NR0_A1_S3
<H>	sdu	LLIND_D3_NS244_NR0_A1_S3
<I>	sdu	LLIND_D3_NS275_NR0_A1_S3
<J>	sdu	LLIND_D3_NS306_NR0_A1_S3
<K>	sdu	LLIND_D3_NS337_NR0_A1_S3
<L>	sdu	LLIND_D3_NS368_NR0_A1_S3
<M>	sdu	LLIND_D3_NS399_NR0_A1_S3
<N>	sdu	LLIND_D3_NS430_NR0_A1_S3
<O>	sdu	LLIND_D3_NS461_NR0_A1_S3
<P>	sdu	LLIND_D3_NS492_NR0_A1_S3
(139)	GRR_READY_IND	
(140)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR28_A0_S3
	sdu	RR_NR59_A0_S3
<C>	sdu	RR_NR90_A0_S3
<D>	sdu	RR_NR121_A0_S3
<E>	sdu	RR_NR152_A0_S3
<F>	sdu	RR_NR183_A0_S3
<G>	sdu	RR_NR214_A0_S3
<H>	sdu	RR_NR245_A0_S3
<I>	sdu	RR_NR276_A0_S3
<J>	sdu	RR_NR307_A0_S3
<K>	sdu	RR_NR338_A0_S3
<L>	sdu	RR_NR369_A0_S3

	<M>	sdu	RR_NR400_A0_S3
	<N>	sdu	RR_NR431_A0_S3
	<O>	sdu	RR_NR462_A0_S3
	<P>	sdu	RR_NR493_A0_S3
(141)		LL_GETDATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
(142)		GRR_DATA_IND_TEST	
		tlli	TLLI_LOCAL_1
	<A>	sdu	RR_D3_NS28_NR0_A1_S3
		sdu	RR_D3_NS59_NR0_A1_S3
	<C>	sdu	RR_D3_NS90_NR0_A1_S3
	<D>	sdu	RR_D3_NS121_NR0_A1_S3
	<E>	sdu	RR_D3_NS152_NR0_A1_S3
	<F>	sdu	RR_D3_NS183_NR0_A1_S3
	<G>	sdu	RR_D3_NS214_NR0_A1_S3
	<H>	sdu	RR_D3_NS245_NR0_A1_S3
	<I>	sdu	RR_D3_NS276_NR0_A1_S3
	<J>	sdu	RR_D3_NS307_NR0_A1_S3
	<K>	sdu	RR_D3_NS338_NR0_A1_S3
	<L>	sdu	RR_D3_NS369_NR0_A1_S3
	<M>	sdu	RR_D3_NS400_NR0_A1_S3
	<N>	sdu	RR_D3_NS431_NR0_A1_S3
	<O>	sdu	RR_D3_NS462_NR0_A1_S3
	<P>	sdu	RR_D3_NS493_NR0_A1_S3
(143)		LL_DATA_IND	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		reserved_data_ind1	NOT_USED
		reserved_data_ind2	NOT_USED
		reserved_data_ind3	NOT_USED
		reserved_data_ind4	NOT_USED
		reserved_data_ind5	NOT_USED
		reserved_data_ind6	NOT_USED
	<A>	sdu	LLIND_D3_NS28_NR0_A1_S3
		sdu	LLIND_D3_NS59_NR0_A1_S3
	<C>	sdu	LLIND_D3_NS90_NR0_A1_S3
	<D>	sdu	LLIND_D3_NS121_NR0_A1_S3
	<E>	sdu	LLIND_D3_NS152_NR0_A1_S3
	<F>	sdu	LLIND_D3_NS183_NR0_A1_S3
	<G>	sdu	LLIND_D3_NS214_NR0_A1_S3
	<H>	sdu	LLIND_D3_NS245_NR0_A1_S3
	<I>	sdu	LLIND_D3_NS276_NR0_A1_S3
	<J>	sdu	LLIND_D3_NS307_NR0_A1_S3
	<K>	sdu	LLIND_D3_NS338_NR0_A1_S3
	<L>	sdu	LLIND_D3_NS369_NR0_A1_S3
	<M>	sdu	LLIND_D3_NS400_NR0_A1_S3
	<N>	sdu	LLIND_D3_NS431_NR0_A1_S3
	<O>	sdu	LLIND_D3_NS462_NR0_A1_S3
	<P>	sdu	LLIND_D3_NS493_NR0_A1_S3
(144)		GRR_READY_IND	
(145)		GRR_DATA_REQ	
		sapi	LL_SAPI_3
		tlli	TLLI_LOCAL_1
		grr_qos	GRRREQ_QOS_PEAKSUB

	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR29_A0_S3
	sdu	RR_NR60_A0_S3
<C>	sdu	RR_NR91_A0_S3
<D>	sdu	RR_NR122_A0_S3
<E>	sdu	RR_NR153_A0_S3
<F>	sdu	RR_NR184_A0_S3
<G>	sdu	RR_NR215_A0_S3
<H>	sdu	RR_NR246_A0_S3
<I>	sdu	RR_NR277_A0_S3
<J>	sdu	RR_NR308_A0_S3
<K>	sdu	RR_NR339_A0_S3
<L>	sdu	RR_NR370_A0_S3
<M>	sdu	RR_NR401_A0_S3
<N>	sdu	RR_NR432_A0_S3
<O>	sdu	RR_NR463_A0_S3
<P>	sdu	RR_NR494_A0_S3
(146)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(147)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS29_NR0_A1_S3
	sdu	RR_D3_NS60_NR0_A1_S3
<C>	sdu	RR_D3_NS91_NR0_A1_S3
<D>	sdu	RR_D3_NS122_NR0_A1_S3
<E>	sdu	RR_D3_NS153_NR0_A1_S3
<F>	sdu	RR_D3_NS184_NR0_A1_S3
<G>	sdu	RR_D3_NS215_NR0_A1_S3
<H>	sdu	RR_D3_NS246_NR0_A1_S3
<I>	sdu	RR_D3_NS277_NR0_A1_S3
<J>	sdu	RR_D3_NS308_NR0_A1_S3
<K>	sdu	RR_D3_NS339_NR0_A1_S3
<L>	sdu	RR_D3_NS370_NR0_A1_S3
<M>	sdu	RR_D3_NS401_NR0_A1_S3
<N>	sdu	RR_D3_NS432_NR0_A1_S3
<O>	sdu	RR_D3_NS463_NR0_A1_S3
<P>	sdu	RR_D3_NS494_NR0_A1_S3
(148)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS29_NR0_A1_S3
	sdu	LLIND_D3_NS60_NR0_A1_S3
<C>	sdu	LLIND_D3_NS91_NR0_A1_S3
<D>	sdu	LLIND_D3_NS122_NR0_A1_S3
<E>	sdu	LLIND_D3_NS153_NR0_A1_S3
<F>	sdu	LLIND_D3_NS184_NR0_A1_S3
<G>	sdu	LLIND_D3_NS215_NR0_A1_S3

<H>	sdu	LLIND_D3_NS246_NR0_A1_S3
<I>	sdu	LLIND_D3_NS277_NR0_A1_S3
<J>	sdu	LLIND_D3_NS308_NR0_A1_S3
<K>	sdu	LLIND_D3_NS339_NR0_A1_S3
<L>	sdu	LLIND_D3_NS370_NR0_A1_S3
<M>	sdu	LLIND_D3_NS401_NR0_A1_S3
<N>	sdu	LLIND_D3_NS432_NR0_A1_S3
<O>	sdu	LLIND_D3_NS463_NR0_A1_S3
<P>	sdu	LLIND_D3_NS494_NR0_A1_S3
(149)	GRR_READY_IND	
(150)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR30_A0_S3
	sdu	RR_NR61_A0_S3
<C>	sdu	RR_NR92_A0_S3
<D>	sdu	RR_NR123_A0_S3
<E>	sdu	RR_NR154_A0_S3
<F>	sdu	RR_NR185_A0_S3
<G>	sdu	RR_NR216_A0_S3
<H>	sdu	RR_NR247_A0_S3
<I>	sdu	RR_NR278_A0_S3
<J>	sdu	RR_NR309_A0_S3
<K>	sdu	RR_NR340_A0_S3
<L>	sdu	RR_NR371_A0_S3
<M>	sdu	RR_NR402_A0_S3
<N>	sdu	RR_NR433_A0_S3
<O>	sdu	RR_NR464_A0_S3
<P>	sdu	RR_NR495_A0_S3
(151)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(152)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
<A>	sdu	RR_D3_NS30_NR0_A1_S3
	sdu	RR_D3_NS61_NR0_A1_S3
<C>	sdu	RR_D3_NS92_NR0_A1_S3
<D>	sdu	RR_D3_NS123_NR0_A1_S3
<E>	sdu	RR_D3_NS154_NR0_A1_S3
<F>	sdu	RR_D3_NS185_NR0_A1_S3
<G>	sdu	RR_D3_NS216_NR0_A1_S3
<H>	sdu	RR_D3_NS247_NR0_A1_S3
<I>	sdu	RR_D3_NS278_NR0_A1_S3
<J>	sdu	RR_D3_NS309_NR0_A1_S3
<K>	sdu	RR_D3_NS340_NR0_A1_S3
<L>	sdu	RR_D3_NS371_NR0_A1_S3
<M>	sdu	RR_D3_NS402_NR0_A1_S3
<N>	sdu	RR_D3_NS433_NR0_A1_S3
<O>	sdu	RR_D3_NS464_NR0_A1_S3
<P>	sdu	RR_D3_NS495_NR0_A1_S3

(153)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
<A>	sdu	LLIND_D3_NS30_NR0_A1_S3
	sdu	LLIND_D3_NS61_NR0_A1_S3
<C>	sdu	LLIND_D3_NS92_NR0_A1_S3
<D>	sdu	LLIND_D3_NS123_NR0_A1_S3
<E>	sdu	LLIND_D3_NS154_NR0_A1_S3
<F>	sdu	LLIND_D3_NS185_NR0_A1_S3
<G>	sdu	LLIND_D3_NS216_NR0_A1_S3
<H>	sdu	LLIND_D3_NS247_NR0_A1_S3
<I>	sdu	LLIND_D3_NS278_NR0_A1_S3
<J>	sdu	LLIND_D3_NS309_NR0_A1_S3
<K>	sdu	LLIND_D3_NS340_NR0_A1_S3
<L>	sdu	LLIND_D3_NS371_NR0_A1_S3
<M>	sdu	LLIND_D3_NS402_NR0_A1_S3
<N>	sdu	LLIND_D3_NS433_NR0_A1_S3
<O>	sdu	LLIND_D3_NS464_NR0_A1_S3
<P>	sdu	LLIND_D3_NS495_NR0_A1_S3
(154)	GRR_READY_IND	
(155)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	RR_NR31_A0_S3
	sdu	RR_NR62_A0_S3
<C>	sdu	RR_NR93_A0_S3
<D>	sdu	RR_NR124_A0_S3
<E>	sdu	RR_NR155_A0_S3
<F>	sdu	RR_NR186_A0_S3
<G>	sdu	RR_NR217_A0_S3
<H>	sdu	RR_NR248_A0_S3
<I>	sdu	RR_NR279_A0_S3
<J>	sdu	RR_NR310_A0_S3
<K>	sdu	RR_NR341_A0_S3
<L>	sdu	RR_NR372_A0_S3
<M>	sdu	RR_NR403_A0_S3
<N>	sdu	RR_NR434_A0_S3
<O>	sdu	RR_NR465_A0_S3
<P>	sdu	RR_NR496_A0_S3

History:

22-Mar-2001 GS Initial

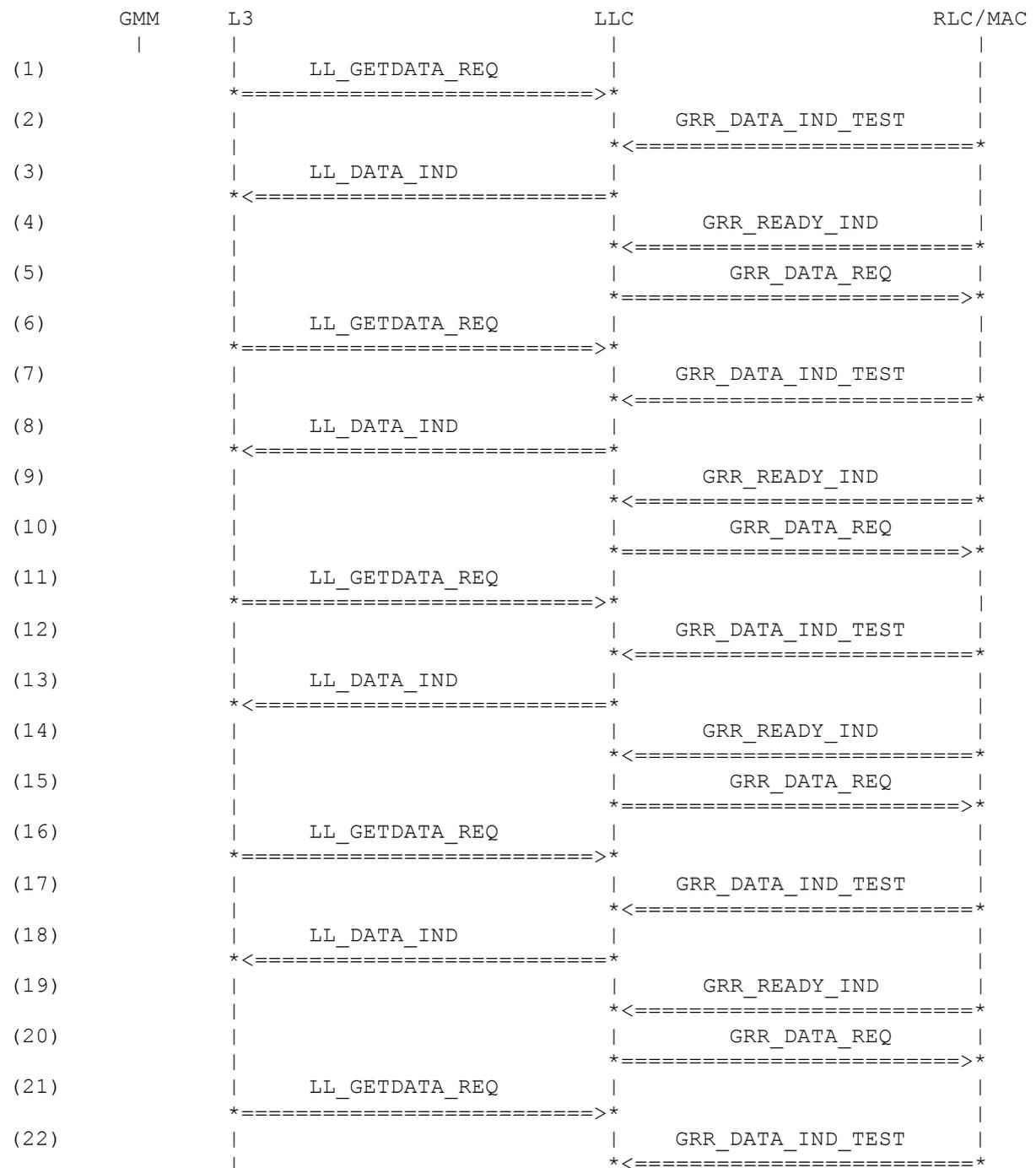
3.16.12 LLC842: ABM - Receive of 8 I-Frames, PDUs forwarded to L3

Description:

LLC receives 8 I-Frames from the RLC/MAC layer. LLC sends an RR to the peer entity and forwards the PDU to L3. If variant P is used, altogether 8 I-frames are transmitted.

Preamble:

LLC840P



(4)	GRR_READY_IND		
(5)	GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKESSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR497_A0_S3
(6)	LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(7)	GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 RR_D3_NS497_NR0_A1_S3
(8)	LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS497_NR0_A1_S3
(9)	GRR_READY_IND		
(10)	GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKESSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR498_A0_S3
(11)	LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(12)	GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 RR_D3_NS498_NR0_A1_S3
(13)	LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS498_NR0_A1_S3
(14)	GRR_READY_IND		

(15)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR499_A0_S3
(16)	LL_GETDATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(17)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_D3_NS499_NR0_A1_S3
(18)	LL_DATA_IND sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS499_NR0_A1_S3
(19)	GRR_READY_IND	
(20)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR500_A0_S3
(21)	LL_GETDATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(22)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_D3_NS500_NR0_A1_S3
(23)	LL_DATA_IND sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS500_NR0_A1_S3
(24)	GRR_READY_IND	
(25)	GRR_DATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1

	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR501_A0_S3
(26)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(27)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS501_NR0_A1_S3
(28)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS501_NR0_A1_S3
(29)	GRR_READY_IND	
(30)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR502_A0_S3
(31)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(32)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS502_NR0_A1_S3
(33)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS502_NR0_A1_S3
(34)	GRR_READY_IND	
(35)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF

	reserved_data_req	NOT_USED
	sdu	RR_NR503_A0_S3
(36)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(37)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS503_NR0_A1_S3
(38)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS503_NR0_A1_S3
(39)	GRR_READY_IND	
(40)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR504_A0_S3

History:

26-Mar-2001 GS Initial

3.16.13 LLC843: ABM - Receive of 8 I-Frames; PDUs forwarded to L3

Description:

LLC receives 8 I-Frames from the RLC/MAC layer. LLC sends an RR to the peer entity and forwards the PDU to L3. If variant P is used, altogether 8 I-frames are transmitted.

Preamble:

LLC842

	GMM	L3	LLC	RLC/MAC
(1)				
			GRR_DATA_IND_TEST	
			<=====	
(2)			GRR_READY_IND	
			<=====	
(3)			GRR_DATA_REQ	
			=====>	
(4)			GRR_DATA_IND_TEST	
			<=====	
(5)			GRR_READY_IND	
			<=====	
(6)			GRR_DATA_REQ	
			=====>	
(7)			GRR_DATA_IND_TEST	
			<=====	
(8)			GRR_READY_IND	
			<=====	
(9)			GRR_DATA_REQ	
			=====>	
(10)			GRR_DATA_IND_TEST	
			<=====	
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	
(13)			GRR_DATA_IND_TEST	
			<=====	
(14)			GRR_READY_IND	
			<=====	
(15)			GRR_DATA_REQ	
			=====>	
(16)			GRR_DATA_IND_TEST	
			<=====	
(17)			GRR_READY_IND	
			<=====	
(18)			GRR_DATA_REQ	
			=====>	
(19)			GRR_DATA_IND_TEST	
			<=====	
(20)			GRR_READY_IND	
			<=====	
(21)			GRR_DATA_REQ	
			=====>	
(22)			GRR_DATA_IND_TEST	
			<=====	

(23)		GRR_READY_IND	
		*<=====	
(24)		GRR_DATA_REQ	
		*=====>	
(25)		GRR_DATA_IND_TEST	
		*<=====	
(26)		GRR_READY_IND	
		*<=====	
(27)		GRR_DATA_REQ	
		*=====>	
(28)		GRR_DATA_IND_TEST	
		*<=====	
(29)		GRR_READY_IND	
		*<=====	
(30)		GRR_DATA_REQ	
		*=====>	
(31)		GRR_DATA_IND_TEST	
		*<=====	
(32)		GRR_READY_IND	
		*<=====	
(33)		GRR_DATA_REQ	
		*=====>	
(34)		GRR_DATA_IND_TEST	
		*<=====	
(35)		GRR_READY_IND	
		*<=====	
(36)		GRR_DATA_REQ	
		*=====>	
(37)		GRR_DATA_IND_TEST	
		*<=====	
(38)		GRR_READY_IND	
		*<=====	
(39)		GRR_DATA_REQ	
		*=====>	
(40)		GRR_DATA_IND_TEST	
		*<=====	
(41)		GRR_READY_IND	
		*<=====	
(42)		GRR_DATA_REQ	
		*=====>	
(43)		GRR_DATA_IND_TEST	
		*<=====	
(44)		GRR_READY_IND	
		*<=====	
(45)		GRR_DATA_REQ	
		*=====>	
(46)		GRR_DATA_IND_TEST	
		*<=====	
(47)		GRR_READY_IND	
		*<=====	
(48)		GRR_DATA_REQ	
		*=====>	
(49)		LL_GETDATA_REQ	
		*=====>	
(50)		LL_DATA_IND	
		*<=====	
(51)		LL_GETDATA_REQ	
		*=====>	
(52)		LL_DATA_IND	

```
(53)      *<=====
          |      LL_GETDATA_REQ      |
          *=====>*
(54)      |      LL_DATA_IND          |
          *<=====>*
(55)      |      LL_GETDATA_REQ      |
          *=====>*
(56)      |      LL_DATA_IND          |
          *<=====>*
(57)      |      LL_GETDATA_REQ      |
          *=====>*
(58)      |      LL_DATA_IND          |
          *<=====>*
(59)      |      LL_GETDATA_REQ      |
          *=====>*
(60)      |      LL_DATA_IND          |
          *<=====>*
(61)      |      LL_GETDATA_REQ      |
          *=====>*
(62)      |      LL_DATA_IND          |
          *<=====>*
(63)      |      LL_GETDATA_REQ      |
          *=====>*
(64)      |      LL_DATA_IND          |
          *<=====>*
(65)      |      LL_GETDATA_REQ      |
          *=====>*
(66)      |      LL_DATA_IND          |
          *<=====>*
(67)      |      LL_GETDATA_REQ      |
          *=====>*
(68)      |      LL_DATA_IND          |
          *<=====>*
(69)      |      LL_GETDATA_REQ      |
          *=====>*
(70)      |      LL_DATA_IND          |
          *<=====>*
(71)      |      LL_GETDATA_REQ      |
          *=====>*
(72)      |      LL_DATA_IND          |
          *<=====>*
(73)      |      LL_GETDATA_REQ      |
          *=====>*
(74)      |      LL_DATA_IND          |
          *<=====>*
(75)      |      LL_GETDATA_REQ      |
          *=====>*
(76)      |      LL_DATA_IND          |
          *<=====>*
(77)      |      LL_GETDATA_REQ      |
          *=====>*
(78)      |      LL_DATA_IND          |
          *<=====>*
(79)      |      LL_GETDATA_REQ      |
          *=====>*
(80)      |      LL_DATA_IND          |
          *<=====>*
```

Parametrization:

<u>Primitive</u>	<u>Parameter</u>	<u>Value</u>
(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 RR_D3_NS504_NR0_A1_S3
(2) GRR_READY_IND		
(3) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR505_A0_S3
(4) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 RR_D3_NS505_NR0_A1_S3
(5) GRR_READY_IND		
(6) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR506_A0_S3
(7) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 RR_D3_NS506_NR0_A1_S3
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR507_A0_S3
(10)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_D3_NS507_NR0_A1_S3
(11)	GRR_READY_IND	
(12)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR508_A0_S3

(13)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_D3_NS508_NR0_A1_S3
(14)	GRR_READY_IND	
(15)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR509_A0_S3
(16)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_D3_NS509_NR0_A1_S3
(17)	GRR_READY_IND	
(18)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR510_A0_S3
(19)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_D3_NS510_NR0_A1_S3
(20)	GRR_READY_IND	
(21)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR511_A0_S3
(22)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_D3_NS511_NR0_A1_S3
(23)	GRR_READY_IND	
(24)	GRR_DATA_REQ sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED RR_NR0_A0_S3
(25)	GRR_DATA_IND_TEST tlli sdu	TLLI_LOCAL_1 RR_D3_NS0_NR0_A1_S3

(26)	GRR_READY_IND	
(27)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR1_A0_S3
(28)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS1_NR0_A1_S3
(29)	GRR_READY_IND	
(30)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR2_A0_S3
(31)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS2_NR0_A1_S3
(32)	GRR_READY_IND	
(33)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR3_A0_S3
(34)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS3_NR0_A1_S3
(35)	GRR_READY_IND	
(36)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR4_A0_S3
(37)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS4_NR0_A1_S3
(38)	GRR_READY_IND	
(39)	GRR_DATA_REQ	
	sapi	LL_SAPI_3

	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR5_A0_S3
(40)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS5_NR0_A1_S3
(41)	GRR_READY_IND	
(42)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR6_A0_S3
(43)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS6_NR0_A1_S3
(44)	GRR_READY_IND	
(45)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR7_A0_S3
(46)	GRR_DATA_IND_TEST	
	tlli	TLLI_LOCAL_1
	sdu	RR_D3_NS7_NR0_A1_S3
(47)	GRR_READY_IND	
(48)	GRR_DATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	RR_NR8_A0_S3
(49)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(50)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED

	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS504_NR0_A1_S3
(51)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(52)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS505_NR0_A1_S3
(53)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(54)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS506_NR0_A1_S3
(55)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(56)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS507_NR0_A1_S3
(57)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(58)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED

	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS508_NR0_A1_S3
(59)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(60)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS509_NR0_A1_S3
(61)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(62)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS510_NR0_A1_S3
(63)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(64)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS511_NR0_A1_S3
(65)	LL_GETDATA_REQ	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(66)	LL_DATA_IND	
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLIND_D3_NS0_NR0_A1_S3

(67)	LL_GETDATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(68)	LL_DATA_IND sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS1_NR0_A1_S3
(69)	LL_GETDATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(70)	LL_DATA_IND sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS2_NR0_A1_S3
(71)	LL_GETDATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(72)	LL_DATA_IND sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS3_NR0_A1_S3
(73)	LL_GETDATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(74)	LL_DATA_IND sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS4_NR0_A1_S3

(75)	LL_GETDATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(76)	LL_DATA_IND sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS5_NR0_A1_S3
(77)	LL_GETDATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(78)	LL_DATA_IND sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS6_NR0_A1_S3
(79)	LL_GETDATA_REQ sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(80)	LL_DATA_IND sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLIND_D3_NS7_NR0_A1_S3

History:

26-Mar-2001	GS	Initial
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3.17 Bidirectional transmission of frames (LLC900-LC949)

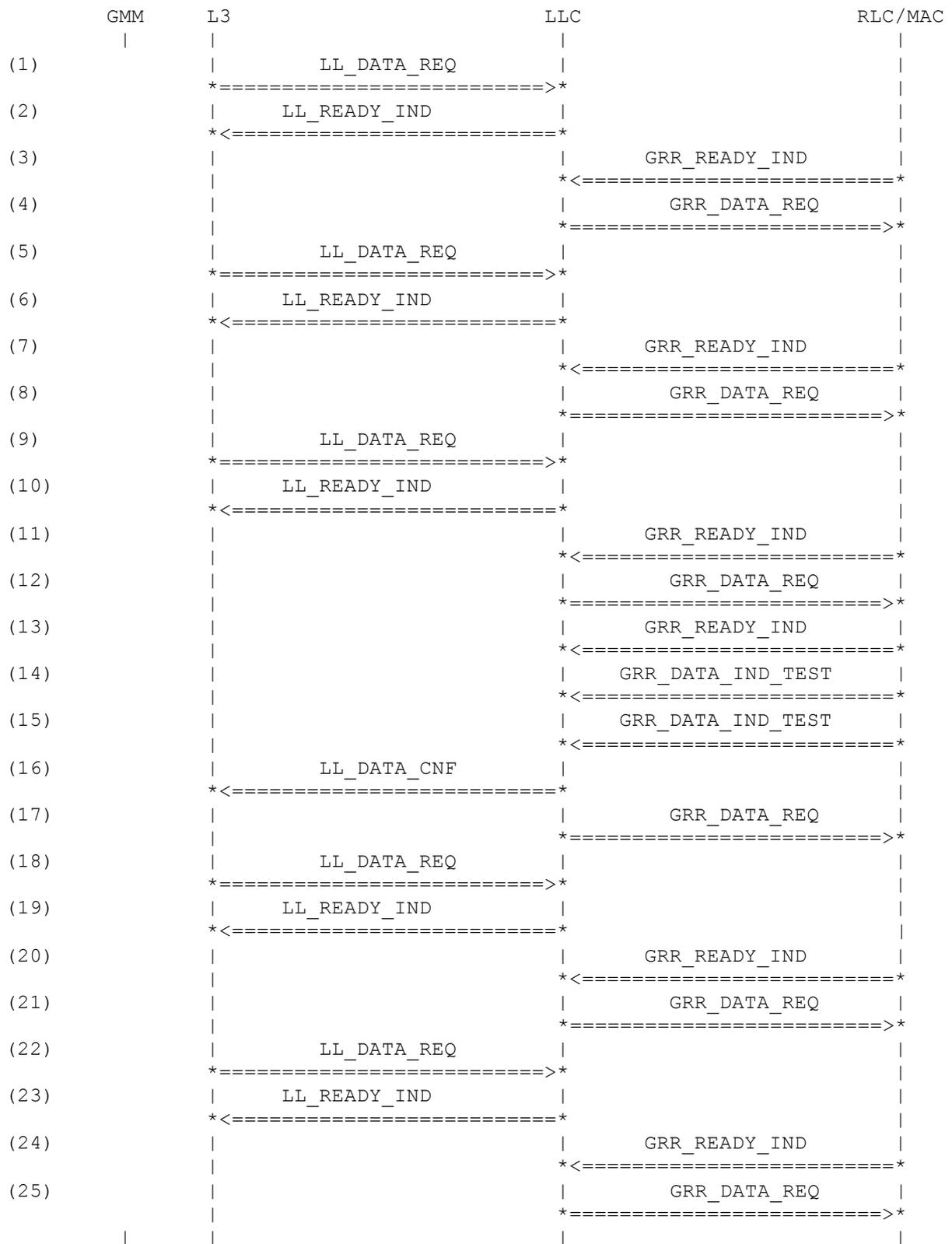
3.17.1 LLC900: ABM - Send some frames; Receive of missing frame; L3 not ready

Description:

LLC receives the retransmitted data frame and then the next new one with A bit set to 1. All in sequence acknowledge frames are forwarded to LL and a RR is send to the peer.

Preamble:

LLC810



Parametrization:

Primitive	Parameter	Value
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(1) LL_DATA_REQ

	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS0_NR2_SAPI3
(5) LL_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(6) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(7) GRR_READY_IND		
(8) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS1_NR2_SAPI3
(9) LL_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED

	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(10) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(11) GRR_READY_IND		
(12) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS2_NR2_A1_SAPI3
(13) GRR_READY_IND		
(14) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS2_NR0_SDU100_SAPI3
(15) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS4_NR2_SDU100_A1_SAPI3
(16) LL_DATA_CNF		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reference1	NOT_USED
(17) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRRIND_RR_NR5_A0_SAPI3
(18) LL_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(19) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(20) GRR_READY_IND		

(21) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_NS3_NR5_SAPI3

(22) LL_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL5
radio_prio	LL_RADIO_PRIO_3
reserved_data_req1	NOT_USED
reference1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE
reserved_data_req4	NOT_USED
reserved_data_req5	NOT_USED
sdu	LLREQ_SDU100

(23) LL_READY_IND

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1

(24) GRR_READY_IND

(25) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKESSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_NS4_NR5_SAPI3

History:

08-Nov-2000	GS	Initial
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3.17.2 LLC910: Sequential Establishment of all acknowledge SAPIs

Description:

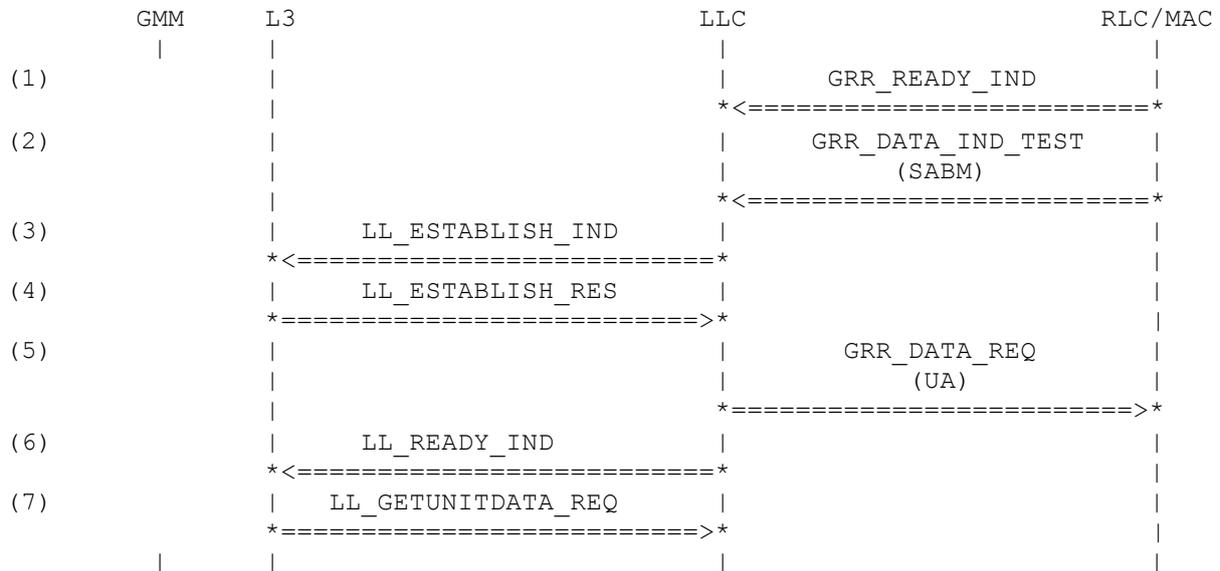
LLC is in state ADM and receives a SABM command. The establishment of the logical link is indicated to SNDCP by the uses of a LL_ESTABLISH_IND and an acknowledge is send to the peer.

Variants:

<A>....<D>

Preamble:

<A>LLC001
 LLC910A
 <C>LLC910B
 <D>LLC910C



Parametrization:

Primitive	Parameter	Value
(1) GRR_READY_IND		
(2) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI3
<C>	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI5
<D>	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI9
		SGSN_SABM_EMPTY_L3_XID_SAPI11
(3) LL_ESTABLISH_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_DEF_SAPI3
	n201_u	N201_U_DEF_SAPI5
<C>	n201_u	N201_U_DEF_SAPI9
<D>	n201_u	N201_U_DEF_SAPI11
	n201_j	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(4) LL_ESTABLISH_RES		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(5) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_UA1_EMPTY_L3_XID_SAPI3
	sdu	MS_UA1_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_UA1_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_UA1_EMPTY_L3_XID_SAPI11
(6) LL_READY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(7) LL_GETUNITDATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

History:

11-Dec-2001	GS	Initial
01-Feb-2002	GS	Ready indication moved.

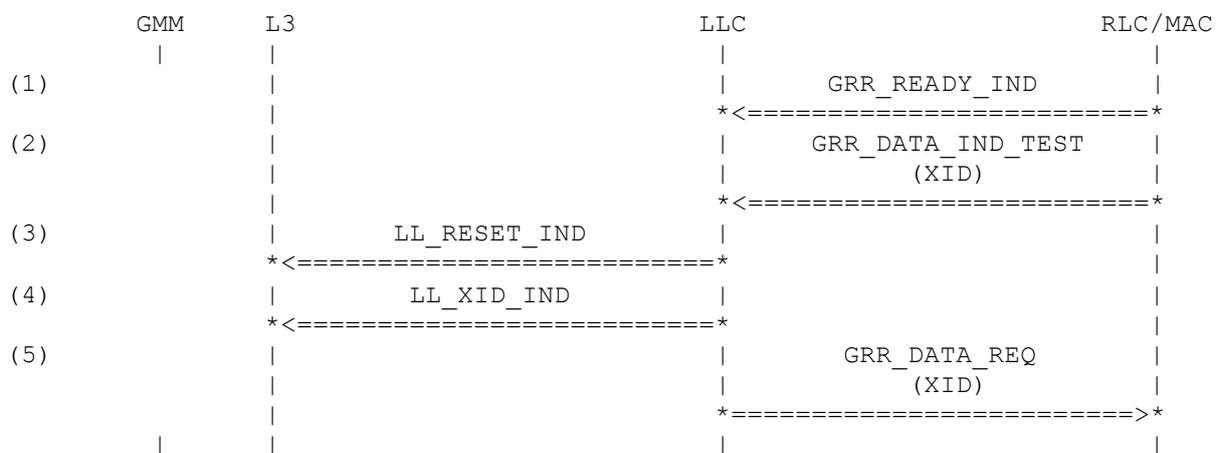
3.17.3 LLC911: Data Transfer on all acknowledged SAPIs; XID Reset is received.

Description:

LLC is in on all SAPIs supporting acknowledge mode in state ABM. Data is transfered until an XID command including a 'LLC Reset' is recieved. An LL_RESET_IND is send to SNDTCP and all SAPIs are switched back to ADM and using default values. Additional the XID command includes a new value for N201-U. This new value is indicated to SNDTCP.

Preamble:

LLC910D



Parametrization:

Primitive	Parameter	Value
(65) GRR_READY_IND		
(66) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 XID1_RE_VE0_NU400_SAPI3
(67) LL_RESET_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(68) LL_XID_IND	sapi tlli n201_u n201_j xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_400 NOT_USED LL_XID_INVALID NOT_USED
(69) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_VE0_NU400_SAPI3

History:

12-Dec-2001 GS Initial

3.17.4 LLC912: Acknowledged Data Transfer not possible on all SAPIs

Description:

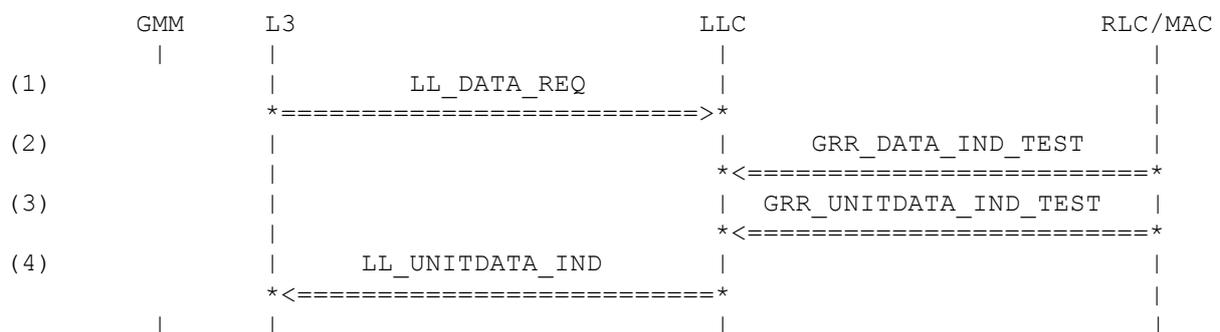
LLC is in on all SAPIs supporting a knowledge mode in state ADM and ready to receive data.

Variants:

<A>....<D>

Preamble:

LLC911

**Parametrization:**

Primitive	Parameter	Value
(1) LL_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU100
(2) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_RR_NR2_A0_SAPI3
	sdu	GRRIND_RR_NR2_A0_SAPI5
<C>	sdu	GRRIND_RR_NR1_A0_SAPI9
<D>	sdu	GRRIND_RR_NR0_A1_SAPI11
(3) GRR_UNITDATA_IND_TEST		
	tlli	TLLI_LOCAL_1
<A>	sdu	GRRIND_DESCLIST100_SAPI3
	sdu	GRRIND_DESCLIST100_SAPI5

<C>	sdu	GRRIND_DESCLIST100_SAPI9
<D>	sdu	GRRIND_DESCLIST100_SAPI11
(4) LL_UNITDATA_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	reserved_unitdata_ind1	NOT_USED
	reserved_unitdata_ind2	NOT_USED
	reserved_unitdata_ind3	NOT_USED
	reserved_unitdata_ind4	NOT_USED
	reserved_unitdata_ind5	NOT_USED
	cipher	LL_CIPHER_OFF
<A>	sdu	LLIND_SDU100_SAPI3
	sdu	LLIND_SDU100_SAPI5
<C>	sdu	LLIND_SDU100_SAPI9
<D>	sdu	LLIND_SDU100_SAPI11

History:

12-Dec-2001	GS	Initial
27-May-2003	UT	GRR_READY_IND removed

3.18 Primitive collision issues (LLC950-LC999)

3.18.1 LLC950: Establish Collision

Description:

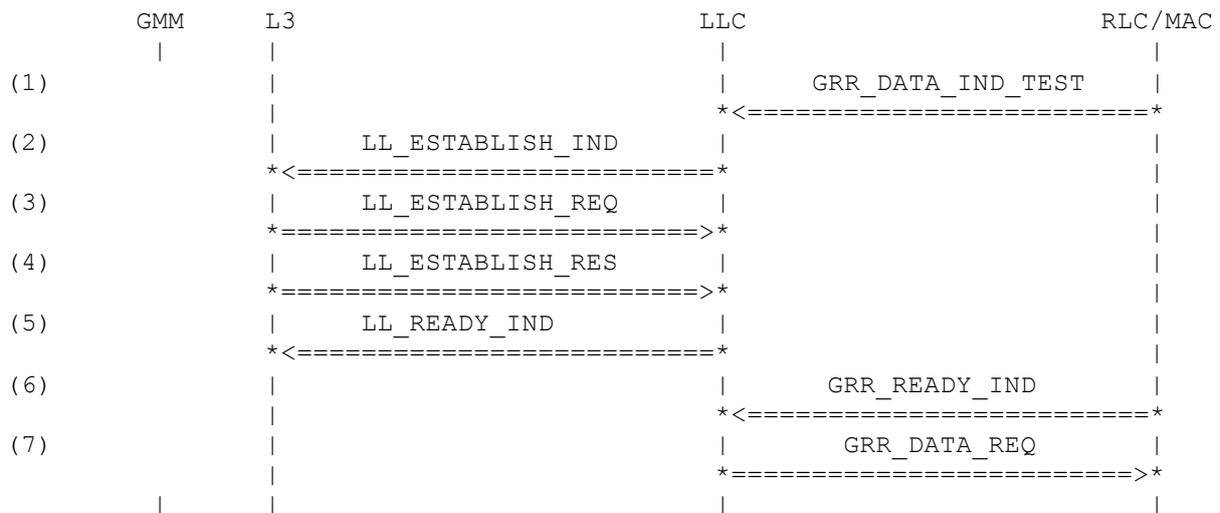
LLC is in state ADM and receives a SABM command. The establishment of the logical link is indicated to SNDCP by the uses of a LL_ESTABLISH_IND. While waiting for the response an LL_ESTABLISH_REQ is received from Layer 3. This request shall be ignored.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI3
<C>	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI5
<D>	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI9
	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI11
(2) LL_ESTABLISH_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_DEF_SAPI3
	n201_u	N201_U_DEF_SAPI5
<C>	n201_u	N201_U_DEF_SAPI9
<D>	n201_u	N201_U_DEF_SAPI11
	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(3) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(4) LL_ESTABLISH_RES		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU

(5) LL_READY_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1

(6) GRR_READY_IND

(7) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_UA1_EMPTY_L3_XID_SAPI3
	sdu	MS_UA1_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_UA1_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_UA1_EMPTY_L3_XID_SAPI11

History:

04-Dec-2001 GS Initial

3.18.2 LLC951: Establish Indication / XID Request Collision

Description:

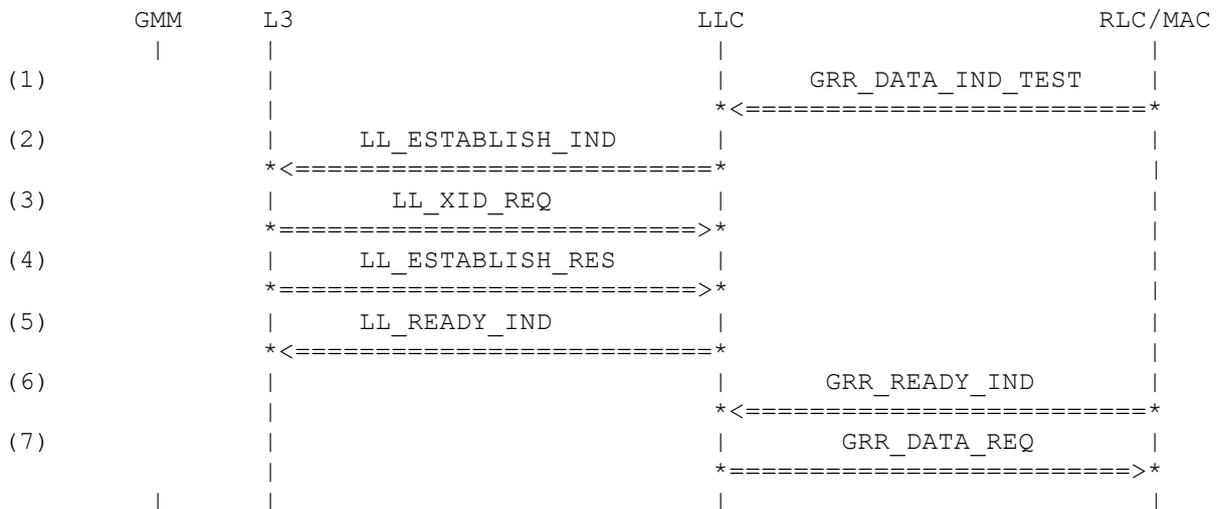
LLC is in state ADM and receives a SABM command. The establishment of the logical link is indicated to SNDCP by the uses of a LL_ESTABLISH_IND. While waiting for the response an LL_ESTABLISH_REQ is received from Layer 3. This request shall be ignored.

Preamble:

LLC001

Variants:

<A>....<D>



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST		
<A>	tlli	TLLI_LOCAL_1
	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI3
<C>	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI5
<D>	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI9
	sdu	SGSN_SABM_EMPTY_L3_XID_SAPI11
(2) LL_ESTABLISH_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_DEF_SAPI3
	n201_u	N201_U_DEF_SAPI5
<C>	n201_u	N201_U_DEF_SAPI9
<D>	n201_u	N201_U_DEF_SAPI11
	n201_i	N201_I_DEF
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(3) LL_XID_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(4) LL_ESTABLISH_RES		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(5) LL_READY_IND		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
(6) GRR_READY_IND		
(7) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF

	reserved_data_req	NOT_USED
<A>	sdu	MS_UA1_EMPTY_L3_XID_SAPI3
	sdu	MS_UA1_EMPTY_L3_XID_SAPI5
<C>	sdu	MS_UA1_EMPTY_L3_XID_SAPI9
<D>	sdu	MS_UA1_EMPTY_L3_XID_SAPI11

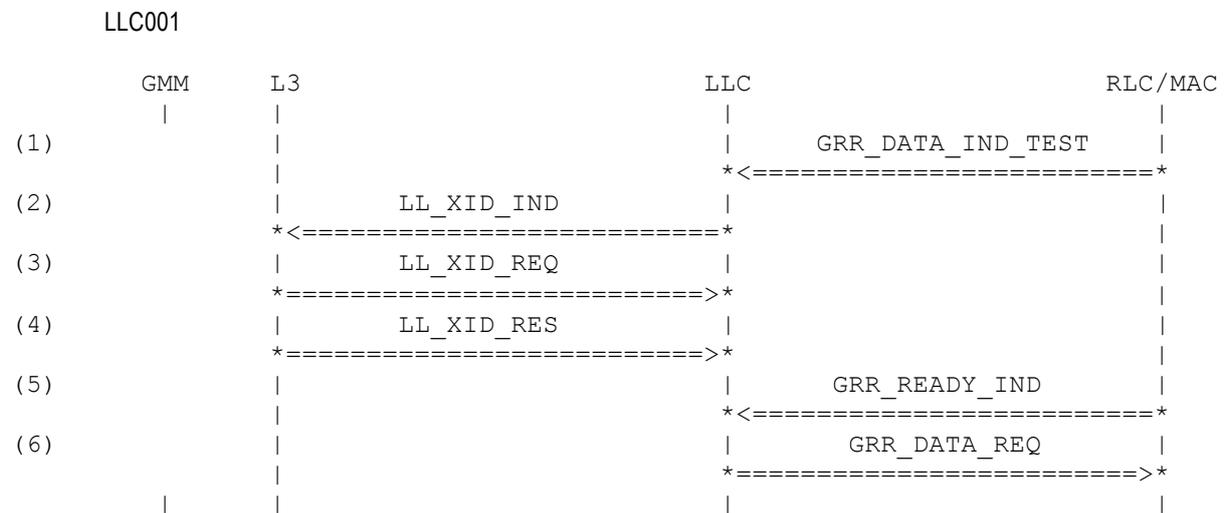
History: 04-Dec-2001 GS Initial

3.18.3 LLC952: XID Collision

Description:

LLC is in state ADM and receives a XID command including an empty block of layer 3 XID parameters. The parameters are forwarded to SNDCP. While waiting for the response an LL_XID_REQ is received from layer 3. The request shall be ignored and the parameters included in following response will be send to the peer.

Preamble:



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	XID1_VE0_NU400_L3_SAPI3
(2) LL_XID_IND	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	n201_u	N201_U_400
	n201_j	NOT_USED
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(3) LL_XID_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(4) LL_XID_RES	sapi	LL_SAPI_3

	tlli sdu	TLLI_LOCAL_1 EMPTY_SDU
(5) GRR_READY_IND		
(6) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID1_VE0_NU400_L3_SAPI3

History: 06-Dec-2001 GS Initial

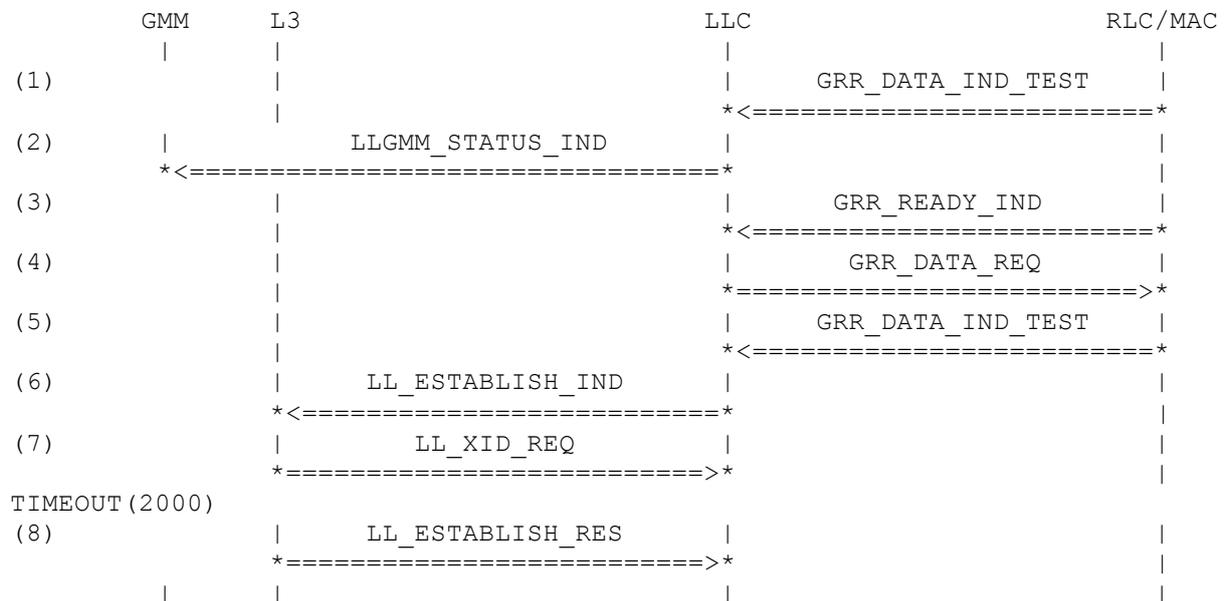
3.18.4 LLC955: Re-Establish / XID Collision

Description:

LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC is in state 'ABM' and is ready to re-establish ABM mode of operation. LLC transmits the SABM command to its peer to re-establish ABM. The peer responds with UA, ABM is re-established. LLC indicates the re-establishment to SMDCP and is waiting for the response. In the meantime a LL_XID_IND arrives and has to be ignored. After receiving the following establish response LLC is re-established.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_DM0_SAPI3

(2) LLGMM_STATUS_IND	error_cause LLGMM_ERRCS_DM0_RECEIVED_REEST	
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_SABM_NOXID_SAPI3
(5) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_NOXID_SAPI3
(6) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_DEF_SAPI3 N201_I_DEF LL_XID_INVALID EMPTY_SDU
(7) LL_XID_REQ	sapi tlli sdu	LL_SAPI_3 TLLI_LOCAL_1 EMPTY_SDU
(8) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 LL_XID_INVALID EMPTY_SDU

History: 07-Dec-2001 GS Initial

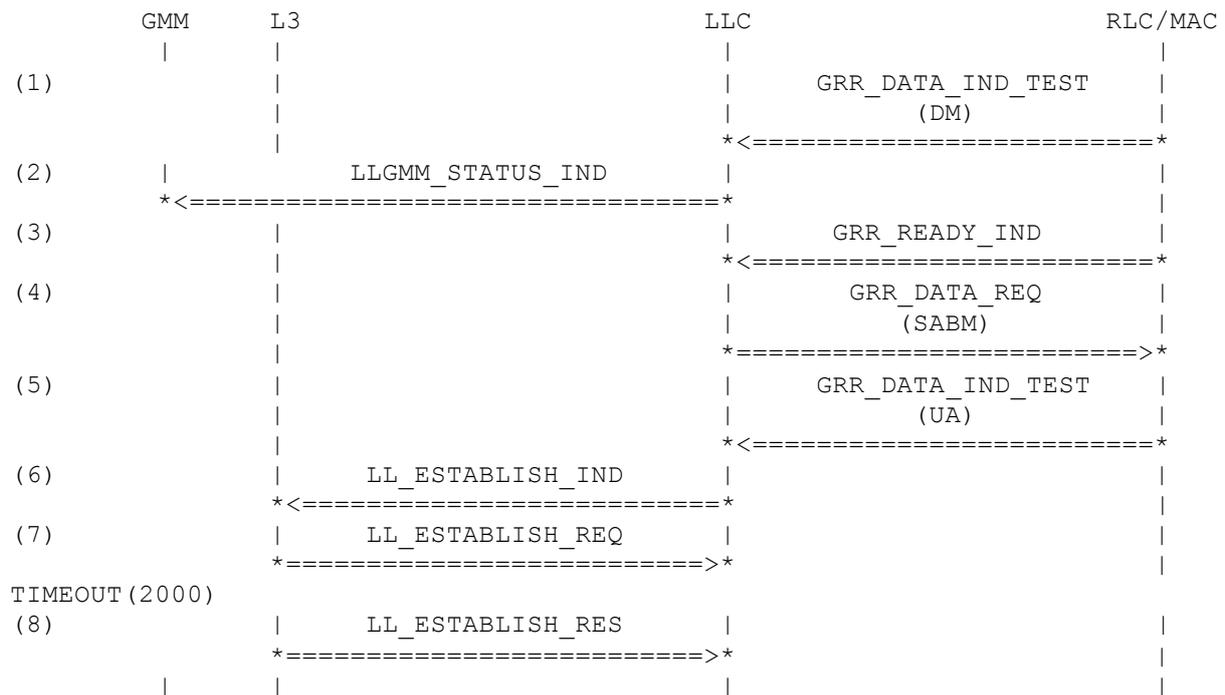
3.18.5 LLC956: L3 and Peer Re-Establish Collision

Description:

LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC is in state 'ABM' and is ready to re-establish ABM mode of operation. LLC transmits the SABM command to its peer to re-establish ABM. The peer responds with UA, ABM is re-established. LLC indicates the re-establishment to SMDCP and is waiting for the response. In the meantime a LL_ESTABLISH_REQ arrives and has to be ignored. After receiving the following establish response LLC is re-established.

Preamble:

LLC400A

**Parametrization:**

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_DM0_SAPI3
(2) LLGMM_STATUS_IND	error_cause LLGMM_ERRCS_DM0_RECEIVED_REEST	
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_SABM_NOXID_SAPI3
(5) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_NOXID_SAPI3
(6) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_DEF_SAPI3 N201_I_DEF LL_XID_INVALID EMPTY_SDU
(7) LL_ESTABLISH_REQ	sapi	LL_SAPI_3

	tli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(8) LL_ESTABLISH_RES	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	xid_valid	LL_XID_INVALID
	sdu	EMPTY_SDU

History:

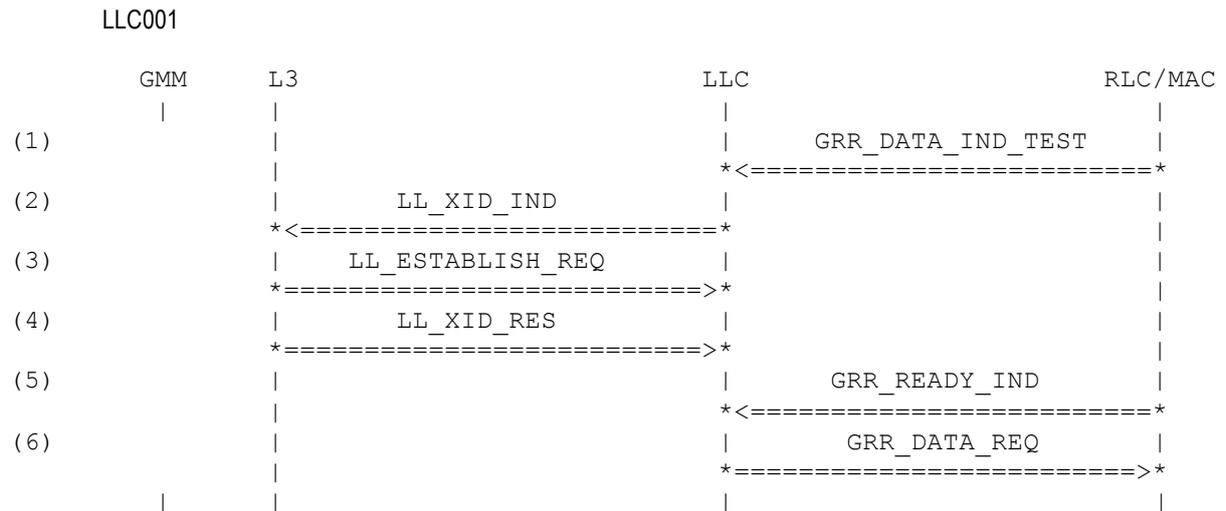
10-Dec-2001 GS Initial

3.18.6 LLC960: XID / Establish Request Collision

Description:

LLC is in state ADM and receives a XID command including an empty block of layer 3 XID parameters. The parameters are forwarded to SNDCP. While waiting for the response an LL_ESTABLISH_REQ is received from layer 3. The request shall be ignored and the parameters included in following response will be send to the peer.

Preamble:



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	XID1_VE0_NU400_L3_SAPI3
(2) LL_XID_IND	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	n201_u	N201_U_400
	n201_i	NOT_USED
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(3) LL_ESTABLISH_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	sdu	EMPTY_SDU

(4) LL_XID_RES

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
sdu	EMPTY_SDU

(5) GRR_READY_IND

(6) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAQSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	XID1_VE0_NU400_L3_SAPI3

History:

07-Dec-2001	GS	Initial
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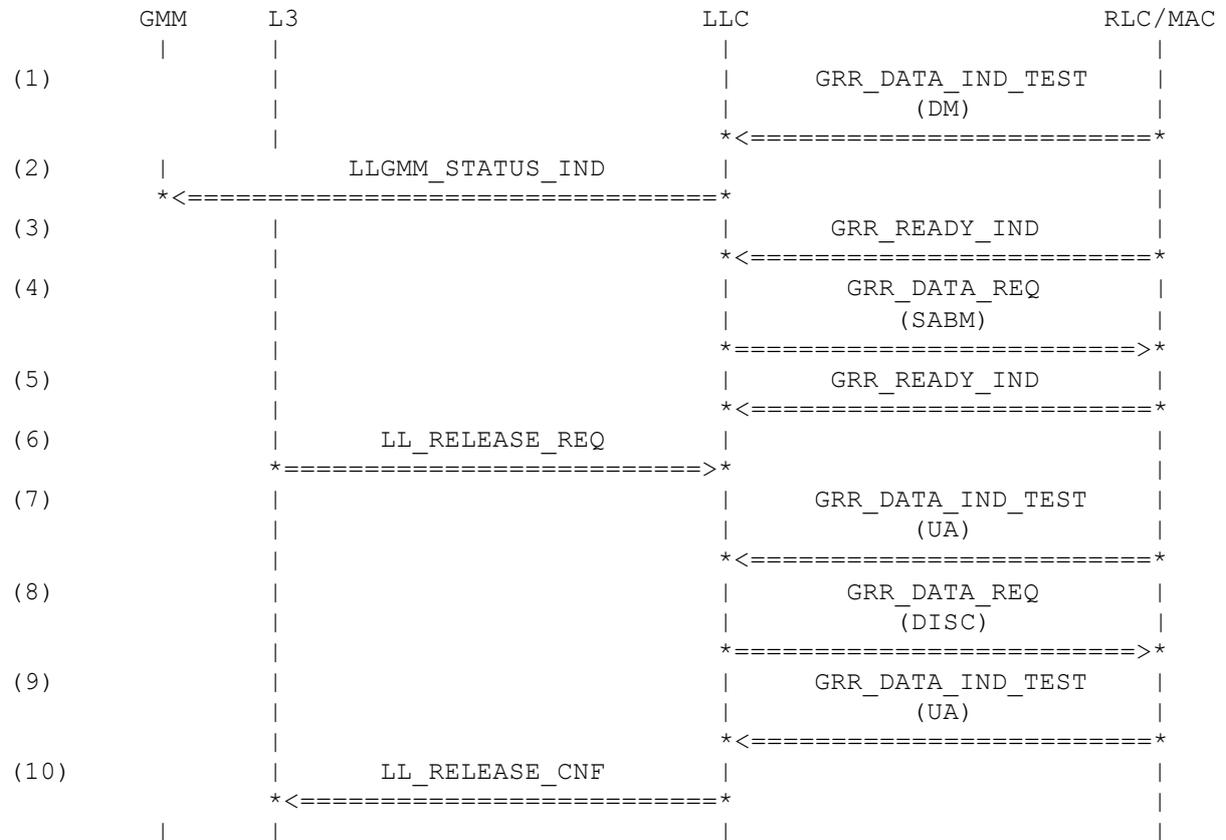
3.18.7 LLC970: Re-Establish / Non-Local Release Request Collision

Description:

In state 'ABM' LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC transmits the SABM command to its peer to re-establish ABM. While waiting for a response layer 3 requests LLC to release to logical link. The request is stored and after the peer responds with an UA, the disconnect procedure is started. After completion of the procedure the release is confirmed to layer 3.

Preamble:

LLC400A



Parametrization:

<u>Primitive</u>	<u>Parameter</u>	<u>Value</u>
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM0_SAPI3
(2) LLGMM_STATUS_IND	error_cause	
	LLGMM_ERRCS_DM0_RECEIVED_REEST	
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_SABM_NOXID_SAPI3
(5) GRR_READY_IND		
(6) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_NOXID_SAPI3
(8) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DISC_SAPI3
(9) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_NOXID_SAPI3
(10) LL_RELEASE_CNF	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

10-Dec-2001 GS Initial

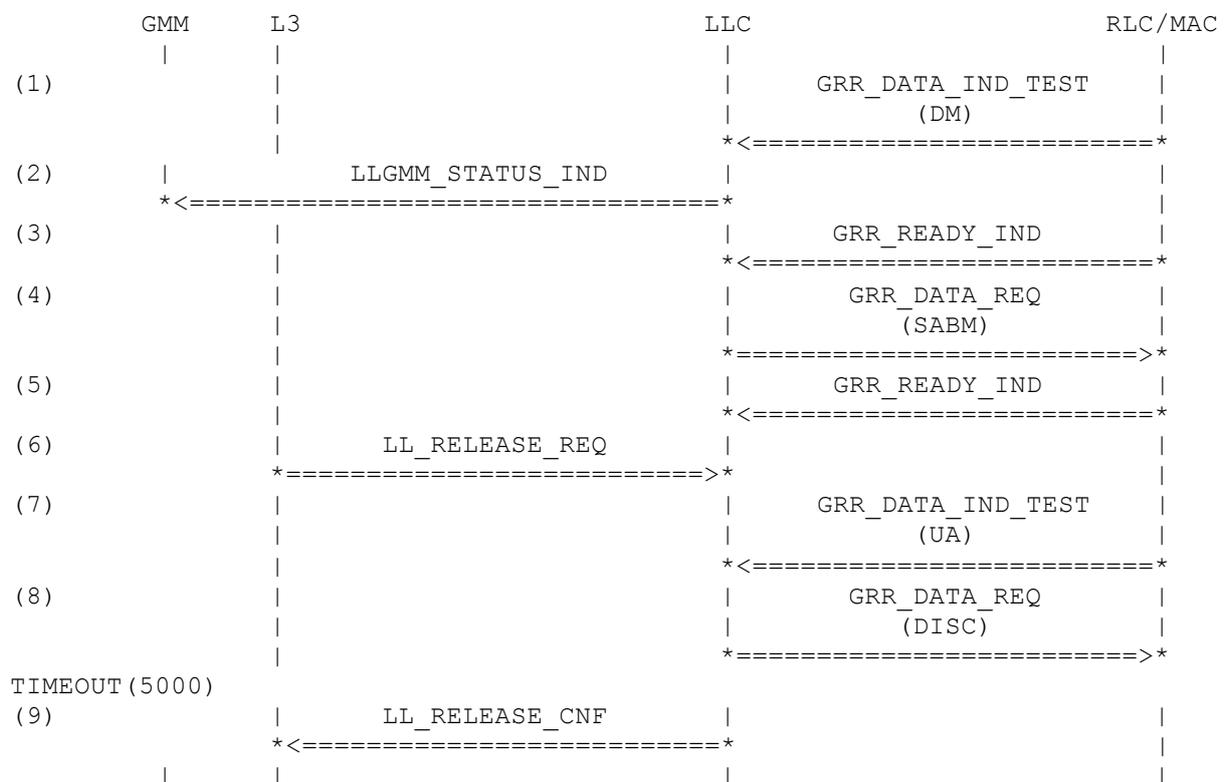
3.18.8 LLC971: Re-Establish / Non-Local Release Request Collision with T200 timeout

Description:

In state 'ABM' LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC transmits the SABM command to its peer to re-establish ABM. While waiting for a response layer 3 requests LLC to release to logical link. The request is stored and after the peer responds with an UA, the disconnect procedure is started. After next T200 timeout while waiting for an UA the release is confirmed to layer 3 and no DISC is re-transmitted to the peer.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	SGSN_DM0_SAPI3
(2) LLGMM_STATUS_IND	error_cause	LLGMM_ERRCS_DM0_RECEIVED_REEST
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRI0_1

	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_SABM_NOXID_SAPI3
(5) GRR_READY_IND		
(6) LL_RELEASE_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(7) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_NOXID_SAPI3
(8) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DISC_SAPI3
(9) LL_RELEASE_CNF		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

10-Dec-2001	GS	Initial
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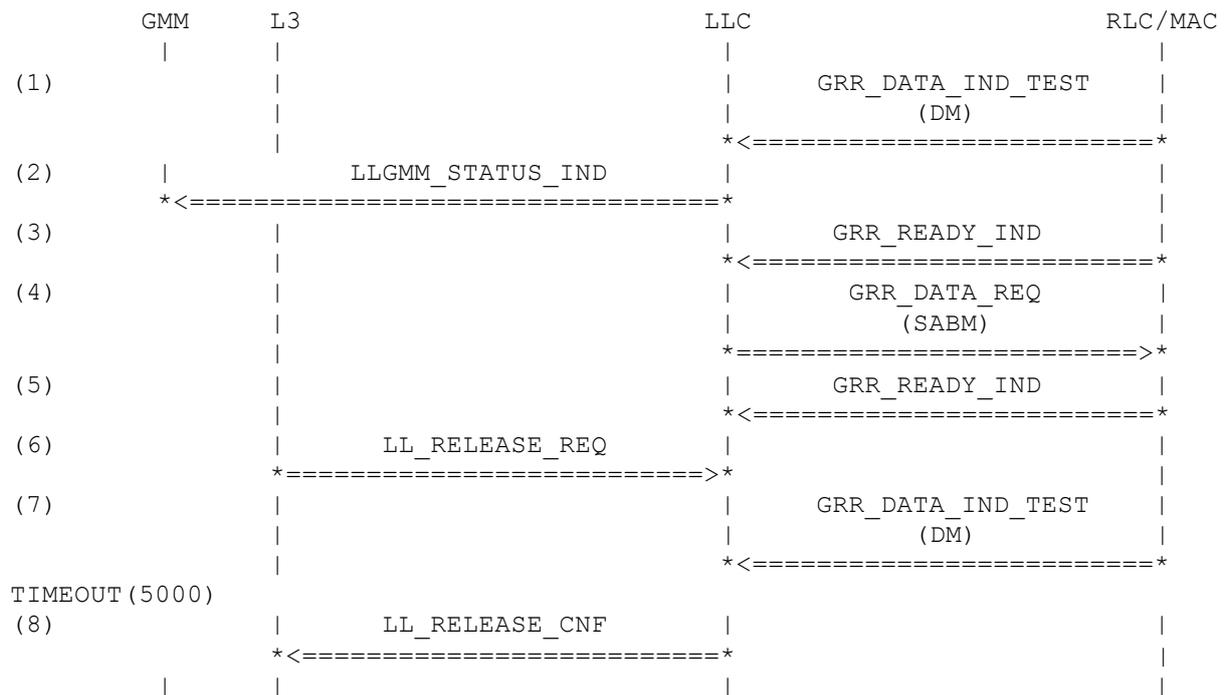
3.18.9 LLC972: Re-Establish / Non-Local Release Request Collision with DM received

Description:

In state 'ABM' LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC transmits the SABM command to its peer to re-establish ABM. While waiting for a response layer 3 requests LLC to release to logical link. The request is stored and after the peer responds with an DM, the disconnect of the logical link is confirmed to layer 3.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM0_SAPI3
(2) LLGMM_STATUS_IND	error_cause	LLGMM_ERRCS_DM0_RECEIVED_REEST
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_SABM_NOXID_SAPI3
(5) GRR_READY_IND		
(6) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM1_SAPI3
(8) LL_RELEASE_CNF	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

10-Dec-2001 GS Initial

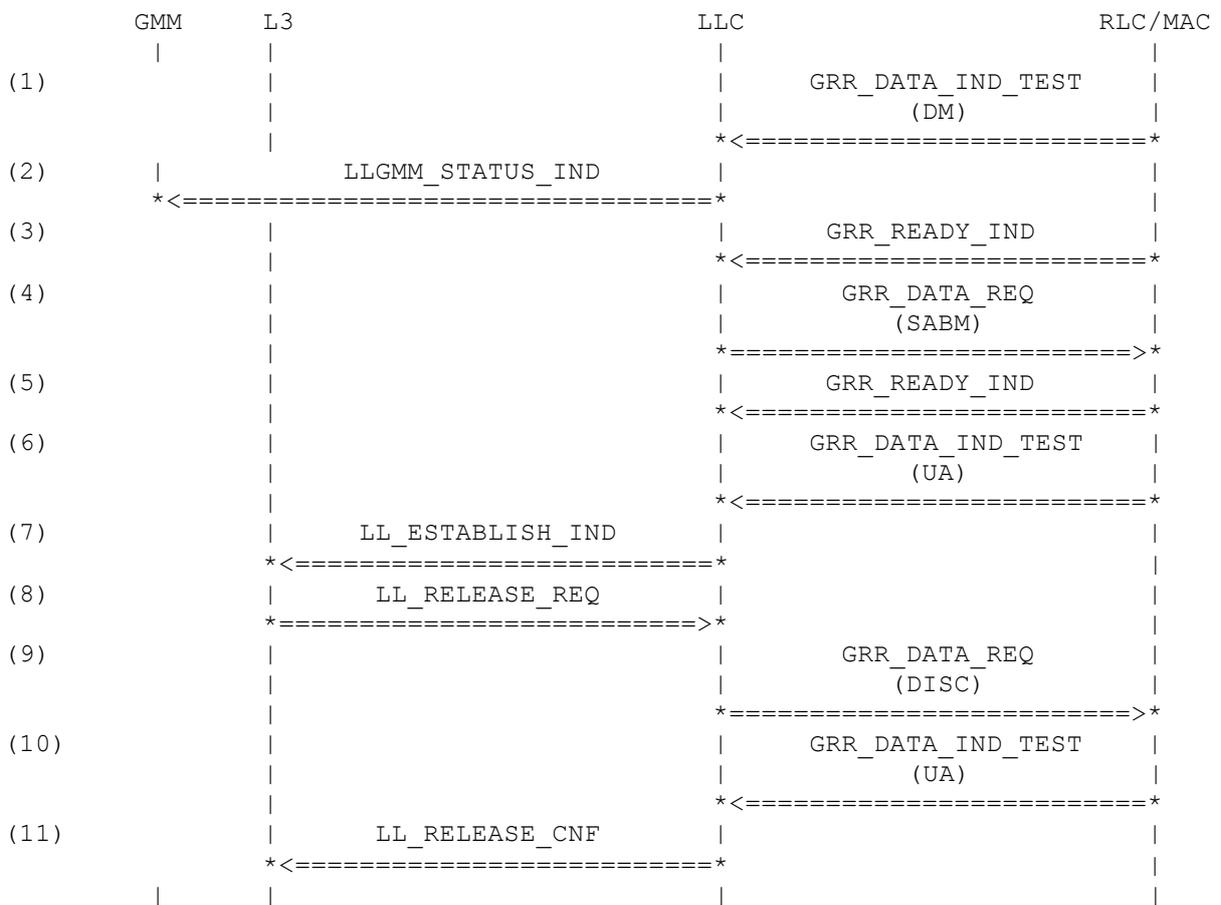
3.18.10 LLC973: Local Re-Establish Indication / Non-Local Release Request Collision

Description:

In state 'ABM' LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC transmits the SABM command to its peer and receives an UA. LLC now is re-established and back in 'ABM'. This is indicated to layer 3 by an LL_ESTABLISH_IND. While waiting for the response from layer 3 LLC receives from there a non-local request to release to logical link. LLC starts the disconnect procedure and after completion the release is confirmed to layer 3.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_DM0_SAPI3
(2) LLGMM_STATUS_IND	error_cause LLGMM_ERRCS_DM0_RECEIVED_REEST	
(3) GRR_READY_IND		

(4) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_SABM_NOXID_SAPI3
(5) GRR_READY_IND		
(6) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_NOXID_SAPI3
(9) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_DEF_SAPI3 N201_I_DEF LL_XID_INVALID EMPTY_SDU
(10) LL_RELEASE_REQ	sapi tlli local	LL_SAPI_3 TLLI_LOCAL_1 LL_REL_NOTLOCAL
(11) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_DISC_SAPI3
(12) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_NOXID_SAPI3
(13) LL_RELEASE_CNF	sapi tlli reserved_release_cnf	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED

History:

10-Dec-2001	GS	Initial
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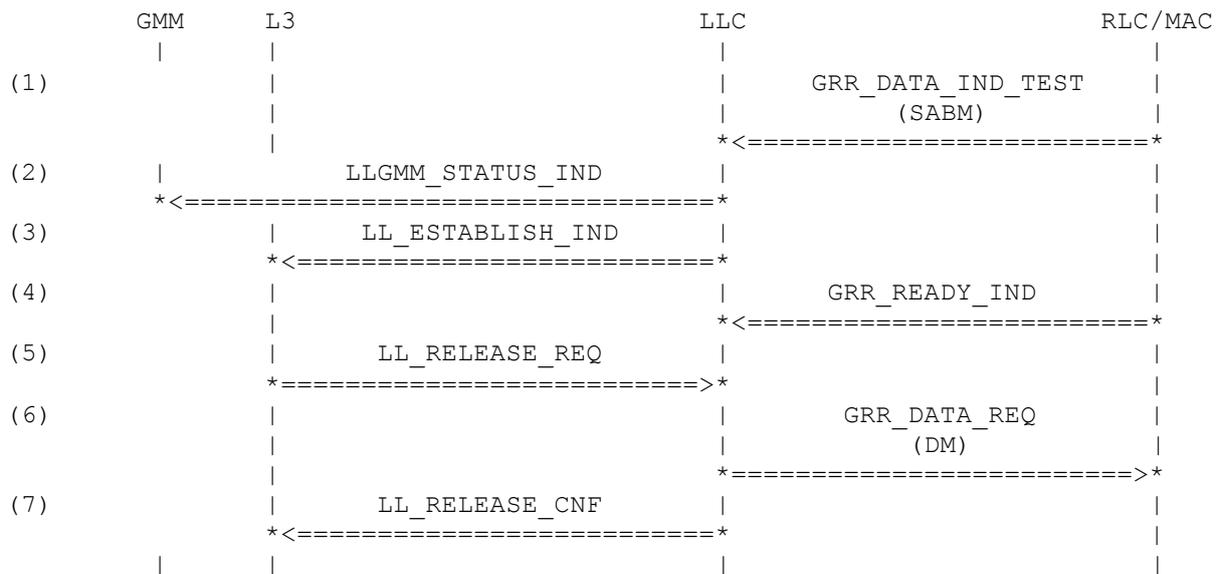
3.18.11 LLC975: Remote Re-Establish Indication / Non-Local Release Request Collision

Description:

In state 'ABM' LLC receives an SABM command which is indicating a re-establish condition at the peer. LLC sends an establish indication to layer 3 and a status indication to GMM. While waiting for the response LLC receives from layer 3 a non-local request to release to logical link. LLC aborts the re-establish procedure by sending a DM to the peer and confirms the release to layer 3.

Preamble:

LLC400A

**Parametrization:**

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_SABM_NOXID_SAPI3
(2) LLGMM_STATUS_IND	error_cause	LLGMM_ERRCS_PEER_REEST
(3) LL_ESTABLISH_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_DEF_SAPI3
	n201_i	N201_I_DEF
	xid_valid	LL_XID_INVALID
	sdu	EMPTY_SDU
(4) GRR_READY_IND		
(5) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_NOTLOCAL
(6) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_DM1_SAPI3
(7) LL_RELEASE_CNF	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

10-Dec-2001	GS	Initial
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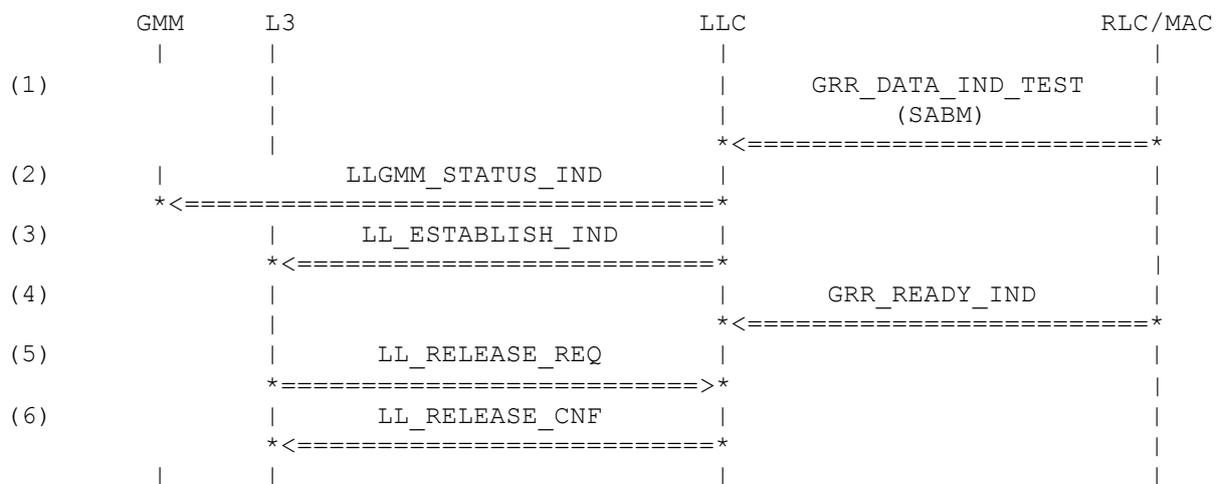
3.18.12 LLC980: Remote Re-Establish Indication / Local Release Request Collision

Description:

In state 'ABM' LLC receives an SABM command which is indicating a re-establish condition at the peer. LLC sends an establish indication to layer 3 and a status indication to GMM. While waiting for the response LLC receives from layer 3 a request to release to logical link locally. LLC aborts the re-establish procedure confirmed the release to layer 3.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tli	TLLI_LOCAL_1
	sdu	SGSN_SABM_NOXID_SAPI3
(2) LLGMM_STATUS_IND	error_cause	LLGMM_ERRCS_PEER_REEST
(3) LL_ESTABLISH_IND	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	n201_u	N201_U_DEF_SAPI3
	n201_j	N201_I_DEF
	xid_valid	LL_XID_INVALID
	sdu	EMPTY_SDU
(4) GRR_READY_IND		
(5) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	local	LL_REL_LOCAL
(6) LL_RELEASE_CNF	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

11-Dec-2001 GS Initial

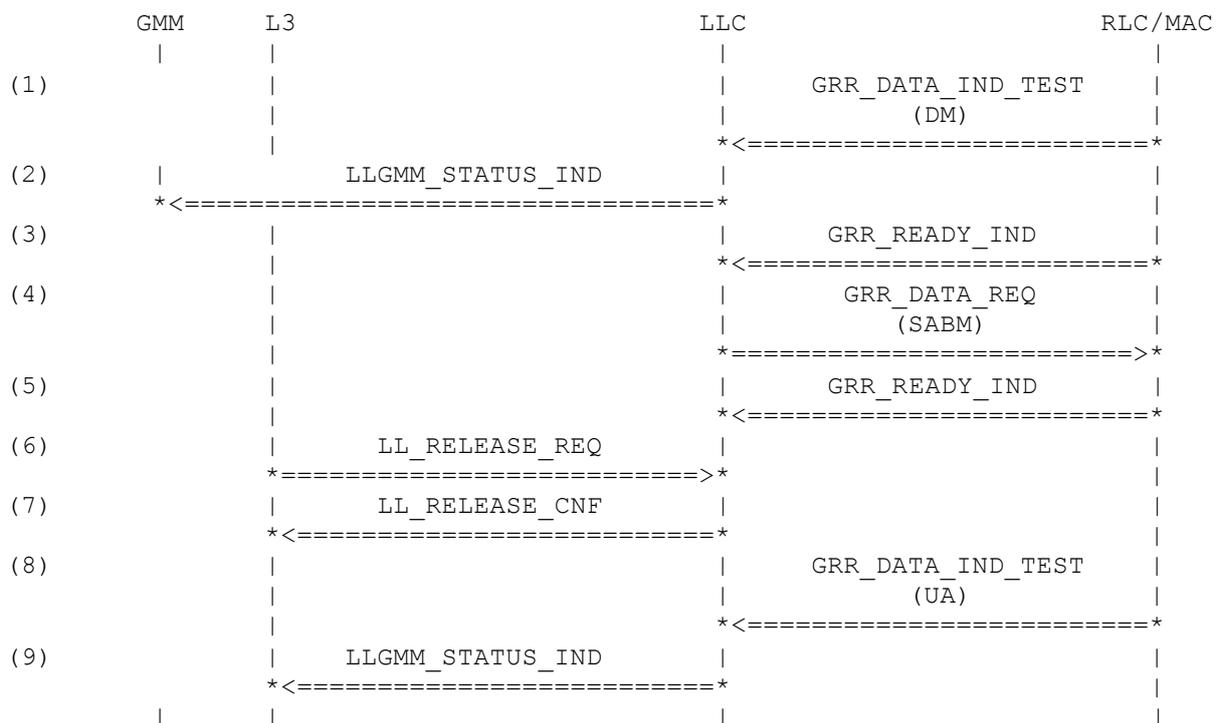
3.18.13 LLC981: Local Re-Establish Indication / Local Release Request Collision

Description:

In state 'ABM' LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC transmits the SABM command to its peer. While waiting for the UA response LLC receives from layer 3 a request to release to logical link locally. After receiving the UA from the peer LLC confirms the release to layer 3.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM0_SAPI3
(2) LLGMM_STATUS_IND	error_cause	LLGMM_ERRCS_DM0_RECEIVED_REEST
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_SABM_NOXID_SAPI3

(5) GRR_READY_IND

(6) LL_RELEASE_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
local	LL_REL_LOCAL

(7) LL_RELEASE_CNF

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
reserved_release_cnf	NOT_USED

(8) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	SGSN_UA1_NOXID_SAPI3

(9) LLGMM_STATUS_IND

error_cause	LLGMM_ERRCS_MULT_ASS_TLLI
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History:

11-Dec-2001	GS	Initial
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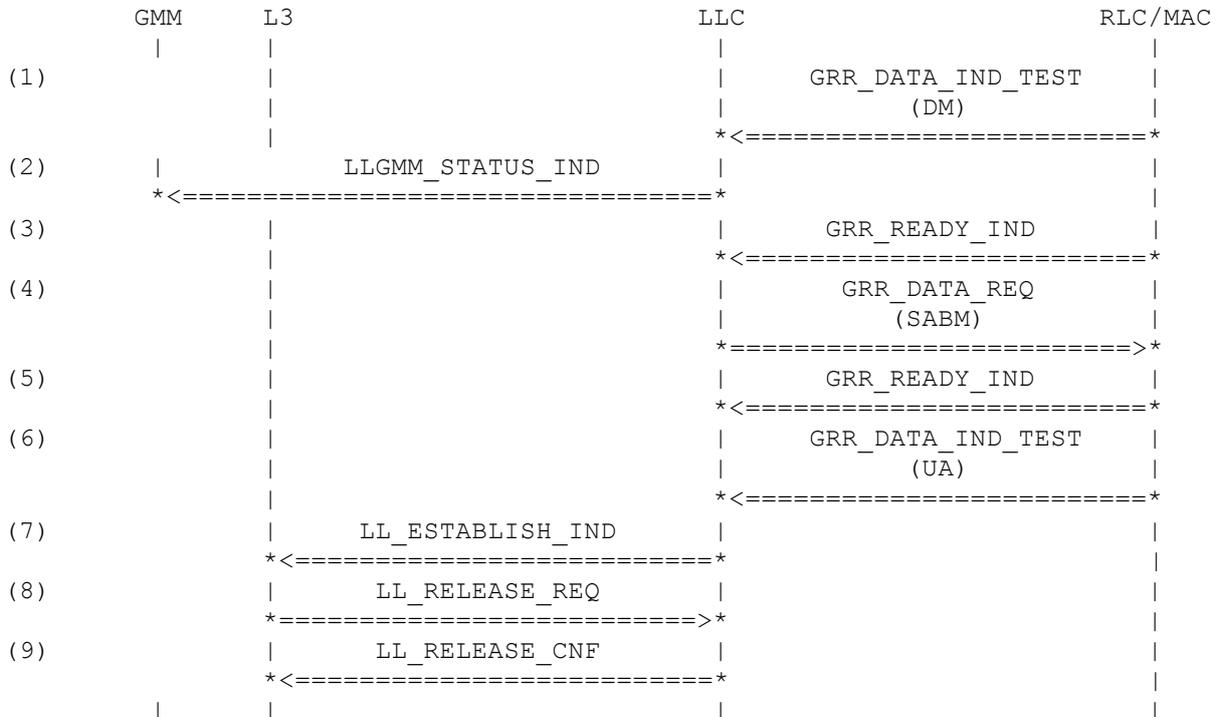
3.18.14 LLC982: Local Re-Establish Indication / Local Release Request Collision

Description:

In state 'ABM' LLC receives an unsolicited DM response with F bit set to 0 for SAPI 3. LLC transmits the SABM command to its peer and receives an UA. LLC now is re-established and back in 'ABM'. This is indicated to layer 3 by an LL_ESTABLISH_IND. While waiting for the response from layer 3 LLC receives from there a non-local request to release to logical link. LLC starts the disconnect procedure and after completion the release is confirmed to layer 3.

Preamble:

LLC400A



Parametrization:

Primitive	Parameter	Value
(1) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM0_SAPI3
(2) LLGMM_STATUS_IND	error_cause	
	LLGMM_ERRCS_DM0_RECEIVED_REEST	
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_SABM_NOXID_SAPI3
(5) GRR_READY_IND		
(6) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_NOXID_SAPI3
(7) LL_ESTABLISH_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_DEF_SAPI3
	n201_i	N201_I_DEF
	xid_valid	LL_XID_INVALID
	sdu	EMPTY_SDU
(8) LL_RELEASE_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	local	LL_REL_LOCAL
(9) LL_RELEASE_CNF	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_release_cnf	NOT_USED

History:

10-Dec-2001	GS	Initial
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3.19 GCF related test cases

3.19.1 LLC009: Layer-3 initiated ABM establishment, UA response with xid parameter

Description:

LLC receives the primitive LL_ESTABLISH_REQ from SMDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with UA. - This test case is used as a preamble.

Preamble:

LLC000

Variants:

<A>....

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_I 520)			
COMMAND	(LLC CONFIG SAPI 3 T200 200)			
COMMAND	(LLC CONFIG SAPI 9 N201_I 520)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)		LL_ESTABLISH_REQ		
		=====>		
(8)			GRR_READY_IND	
			<=====	
(9)			GRR_DATA_REQ	
			=====>	
(10)			GRR_DATA_IND_TEST	
			<=====	
(11)		LL_ESTABLISH_CNF		
		<=====		
(12)		LL_READY_IND		
		<=====		

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID

(6) LL_UNITREADY_IND	sapi tlli	LL_SAPI_11 LL_TLLI_INVALID
(7) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi tlli sdu	LL_SAPI_9 TLLI_LOCAL_1 EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED
<A>	sdu	
	MS_SABM_N201I_520_T200_200_SAPI3 sdu	MS_SABM_N201I_520_SAPI9
(10) GRR_DATA_IND_TEST		
<A>	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_N201U_400_SAPI3
	sdu	SGSN_UA1_N201_I520_KU1_SAPI9
(11) LL_ESTABLISH_CNF		
<A>	sapi	LL_SAPI_3
	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
<A>	n201_u	N201_U_400
	n201_u	NOT_USED
<A>	n201_i	N201_I_520
	n201_i xid_valid sdu	N201_I_520 LL_XID_VALID EMPTY_SDU
(12) LL_READY_IND		
<A>	sapi	LL_SAPI_3
	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1

History:

12-Jan-2003 UT

3.19.2 LLC010: Layer-3 initiated ABM establishment, UA response with xid parameter

Description:

LLC receives the primitive LL_ESTABLISH_REQ from SMDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with UA. - This test case is used as preamble.

Preamble:

LLC000

Variants:

<A>....

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_I 520)			
COMMAND	(LLC CONFIG SAPI 9 N201_I 520)			
(1)		LLGMM_ASSIGN_REQ		
		=====	>	
(2)		LL_UNITREADY_IND		
		*<=====	*	
(3)		LL_UNITREADY_IND		
		*<=====	*	
(4)		LL_UNITREADY_IND		
		*<=====	*	
(5)		LL_UNITREADY_IND		
		*<=====	*	
(6)		LL_UNITREADY_IND		
		*<=====	*	
(7)		LL_ESTABLISH_REQ		
		=====	>	
(8)			GRR_READY_IND	
			*<=====	*
(9)			GRR_DATA_REQ	
			=====	>
(10)			GRR_DATA_IND_TEST	
			*<=====	*
(11)		LL_ESTABLISH_CNF		
		*<=====	*	
(12)		LL_READY_IND		
		*<=====	*	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID

(6) LL_UNITREADY_IND	sapi tlli	LL_SAPI_11 LL_TLLI_INVALID
(7) LL_ESTABLISH_REQ	sapi	LL_SAPI_3
<A>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi	LL_SAPI_3
<A>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_N201I_520_SAPI3
	sdu	MS_SABM_N201I_520_SAPI9
(10) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
<A>	sdu	SGSN_UA1_N201U_400_SAPI3
	sdu	SGSN_UA1_N201I_520_U400_SAPI9
(11) LL_ESTABLISH_CNF	sapi	LL_SAPI_3
<A>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
<A>	n201_u	N201_U_400
	n201_u	N201_U_400
<A>	n201_i	N201_I_520
	n201_i	N201_I_520
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(12) LL_READY_IND	sapi	LL_SAPI_3
<A>	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1

History:

12-Jan-2003 UT

3.19.3 LLC011: Negotiation of XID parameters on SAPI 3 initiated by SNDCP

Description:

LLC is assigned and ready to send data. LLC receives a LL_XID_REQ and therefore sends a XID command message to its peer entity. The peer answers with a XID response. The layer 3 XID parameters are forwarded to SNDCP in an LL_XID_CNF.- Variant A: Network Answers with empty xid frame. Variant B: Network answers with same parameter as included in XID command.

Preamble:

LLC000

Variants:

<A>....

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_U 400)			
COMMAND	(LLC CONFIG SAPI 3 N201_I 520)			
(1)		LLGMM_ASSIGN_REQ		
		=====	>	
(2)		LL_UNITREADY_IND		
		*<=====	*	
(3)		LL_UNITREADY_IND		
		*<=====	*	
(4)		LL_UNITREADY_IND		
		*<=====	*	
(5)		LL_UNITREADY_IND		
		*<=====	*	
(6)		LL_UNITREADY_IND		
		*<=====	*	
(7)		LL_XID_REQ		
		=====	>	
(8)			GRR_READY_IND	
			*<=====	*
(9)			GRR_DATA_REQ	
			=====	>
(10)			GRR_DATA_IND_TEST	
			*<=====	*
(11)		LL_XID_CNF		
		*<=====	*	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID

(7) LL_XID_REQ	sapi tlli sdu	LL_SAPI_3 TLLI_LOCAL_1 EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED XID_N201_I_U_520_400_EL3_SAPI3
(10) GRR_DATA_IND_TEST	tlli sdu <A> sdu	TLLI_LOCAL_1 XID_CR0_EMPTY_L3_SAPI3 XID_EL3_N201_I_U_520_400_SAPI3
(11) LL_XID_CNF	sapi tlli n201_u n201_i sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_400 N201_I_520 EMPTY_SDU

History:

13-Feb-2003	UT	Initial
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3.19.4 LLC012: GCF390 – TC 46.2.2.2.1, TC 46.2.2.2.2

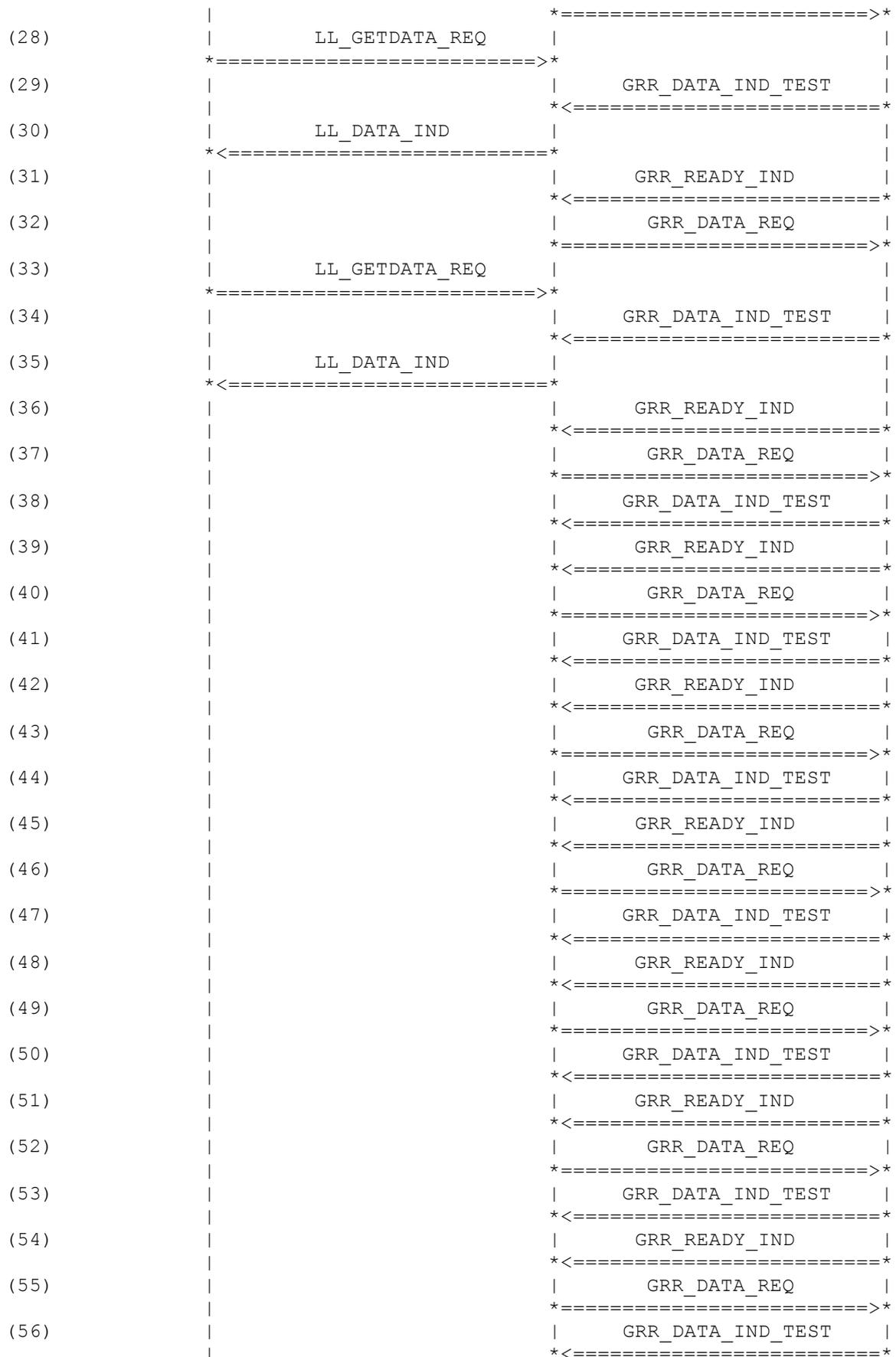
Description:

LLC receives the primitive LL_ESTABLISH_REQ from SNDCCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with UA. After ABM reestablishment LLC receives I+S frame from GRR. LLC receives the primitive LL_ESTABLISH_REQ from SNDCCP and ABM is re-established. All I+S frames LLC receives from peer are acknowledged with a supervisory frame.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
	COMMAND	(LLC CONFIG SAPI 3 N201_I 520)		
	COMMAND	(LLC CONFIG SAPI 3 MD 0)		
	COMMAND	(LLC CONFIG SAPI 3 MU 0)		
	COMMAND	(LLC CONFIG SAPI 9 N201_I 520)		
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)		LL_ESTABLISH_REQ		
		=====>		
(8)			GRR_READY_IND	
			<=====	
(9)			GRR_DATA_REQ	
			=====>	
(10)			GRR_DATA_IND_TEST	
			<=====	
(11)		LL_ESTABLISH_CNF		
		<=====		
(12)		LL_READY_IND		
		<=====		
(13)		LL_GETDATA_REQ		
		=====>		
(14)			GRR_DATA_IND_TEST	
			<=====	
(15)		LL_DATA_IND		
		<=====		
(16)		LL_ESTABLISH_REQ		
		=====>		
(17)		LLGMM_STATUS_IND		
		<=====		
(18)			GRR_READY_IND	
			<=====	
(19)			GRR_DATA_REQ	
			=====>	
(20)			GRR_DATA_IND_TEST	
			<=====	
(21)		LL_ESTABLISH_CNF		
		<=====		
(22)		LL_READY_IND		
		<=====		
(23)		LL_GETDATA_REQ		
		=====>		
(24)			GRR_DATA_IND_TEST	
			<=====	
(25)		LL_DATA_IND		
		<=====		
(26)			GRR_READY_IND	
			<=====	
(27)			GRR_DATA_REQ	



```

(57)          |          |          GRR_READY_IND          |
              |          | *<=====*                      |
(58)          |          |          GRR_DATA_REQ          |
              |          | *=====>*                      |
(59)          |          |          GRR_DATA_IND_TEST     |
              |          | *<=====*                      |
(60)          |          |          GRR_READY_IND          |
              |          | *<=====*                      |
(61)          |          |          GRR_DATA_REQ          |
              |          | *=====>*                      |
              |          |          |                      |

```

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) LL_ESTABLISH_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	
	MS_SABM_N201I_520_MD0_MU0_SAPI3	
(10) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	

SGSN_UA1_N201_I520_MD0_MU0_KD4_KU4_SAPI3

(11) LL_ESTABLISH_CNF

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
n201_u	NOT_USED
n201_i	N201_I_520
xid_valid	LL_XID_VALID
sdu	EMPTY_SDU

(12) LL_READY_IND

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1

(13) LL_GETDATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1

(14) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	GRRIND_NS0_NR0_SDU100_A1_SAPI3

(15) LL_DATA_IND

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
reserved_data_ind1	NOT_USED
reserved_data_ind2	NOT_USED
reserved_data_ind3	NOT_USED
reserved_data_ind4	NOT_USED
reserved_data_ind5	NOT_USED
reserved_data_ind6	NOT_USED
sdu	LLDATAIND_SDU100_N0A1_SAPI3

(16) LL_ESTABLISH_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
sdu	EMPTY_SDU

(17) LLGMM_STATUS_IND

error_cause	LLGMM_ERRCS_L3_REEST
-------------	----------------------

(18) GRR_READY_IND

(19) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAQSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	MS_SABM_N201I_520_MD0_MU0_SAPI3

(20) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	SGSN_UA1_EMPTY_L3_XID_SAPI3

(21) LL_ESTABLISH_CNF

sapi	LL_SAPI_3
------	-----------

	tlli	TLLI_LOCAL_1
	n201_u	NOT_USED
	n201_i	N201_I_520
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(22) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(23) LL_GETDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(24) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS0_NR0_SDU100_A1_SAPI3
(25) LL_DATA_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N0A1_SAPI3
(26) GRR_READY_IND		
(27) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRRIND_RR_NR1_A0_SAPI3
(28) LL_GETDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(29) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS1_NR0_SDU100_A1_SAPI3
(30) LL_DATA_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N1A1_SAPI3
(31) GRR_READY_IND		

(32) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR2_A0_SAPI3
(33) LL_GETDATA_REQ	sapi tli	LL_SAPI_3 TLLI_LOCAL_1
(34) GRR_DATA_IND_TEST	tli sdu GRRIND_NS2_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(35) LL_DATA_IND	sapi tli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N2A1_SAPI3
(36) GRR_READY_IND		
(37) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR3_A0_SAPI3
(38) GRR_DATA_IND_TEST	tli sdu GRRIND_NS3_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(39) GRR_READY_IND		
(40) GRR_DATA_REQ	sapi tli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR4_A0_SAPI3
(41) GRR_DATA_IND_TEST	tli sdu GRRIND_NS4_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(42) GRR_READY_IND		

(43) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRRIND_RR_NR5_A0_SAPI3

(44) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	GRRIND_NS5_NR0_SDU100_A1_SAPI3

(45) GRR_READY_IND

(46) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRRIND_RR_NR6_A0_SAPI3

(47) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	GRRIND_NS6_NR0_SDU100_A1_SAPI3

(48) GRR_READY_IND

(49) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRRIND_RR_NR7_A0_SAPI3

(50) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	GRRIND_NS7_NR0_SDU100_A1_SAPI3

(51) GRR_READY_IND

(52) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRRIND_RR_NR8_A0_SAPI3

(53) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	GRRIND_NS8_NR0_SDU100_A1_SAPI3

(54) GRR_READY_IND

(55) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRRIND_RR_NR9_A0_SAPI3

(56) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	GRRIND_NS9_NR0_SDU100_A1_SAPI3

(57) GRR_READY_IND

(58) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRRIND_RR_NR10_A0_SAPI3

(59) GRR_DATA_IND_TEST

tlli	TLLI_LOCAL_1
sdu	GRRIND_NS10_NR0_SDU100_A1_SAPI3

(60) GRR_READY_IND

(61) GRR_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKEKSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRRIND_RR_NR11_A0_SAPI3

History:

12-Jan-2003	UT	Initial
10-Apr-2003	UT	Enhancement, data transfer

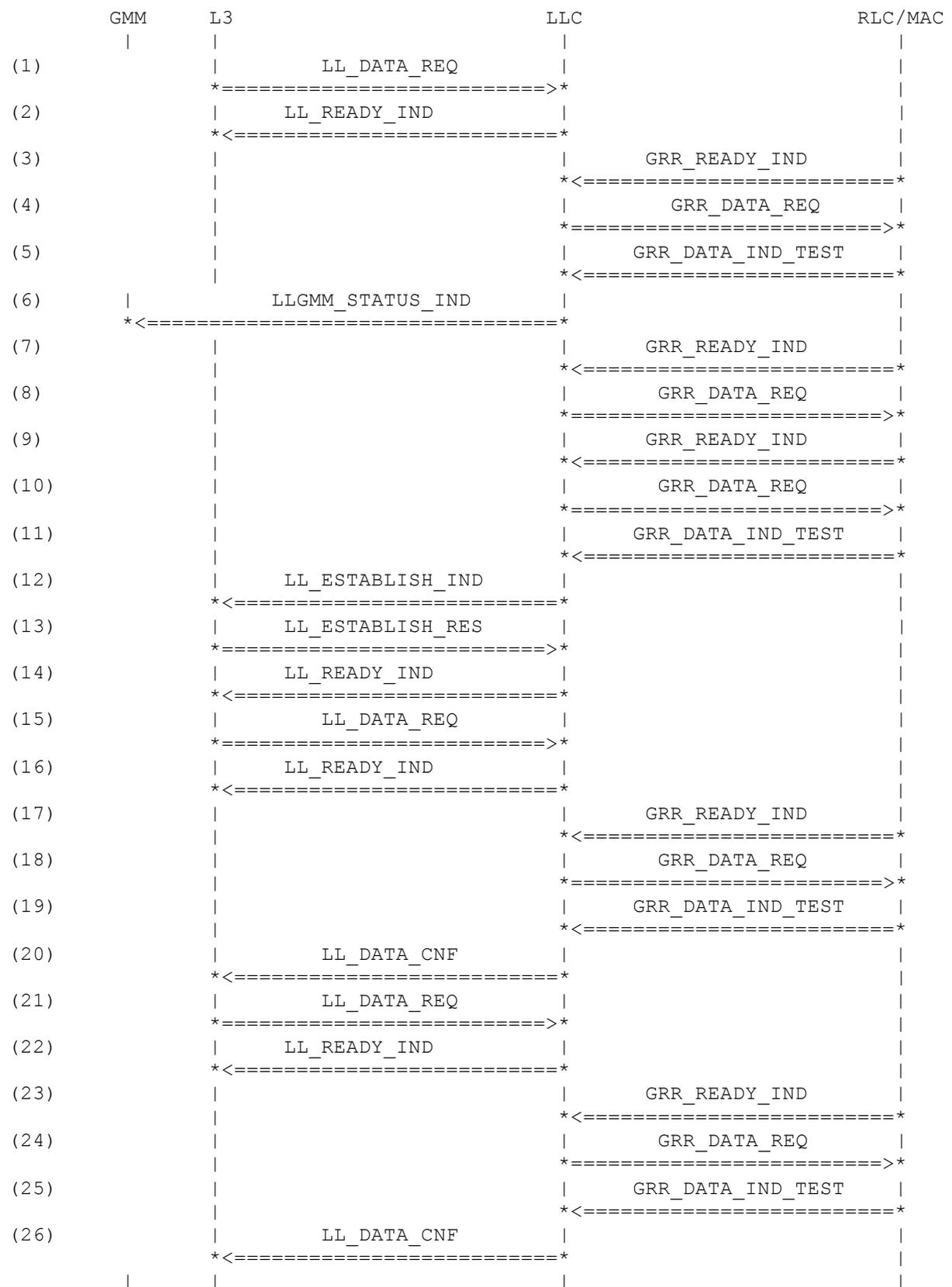
3.19.5 LLC013: GCF390 – TC 46.1.2.5.1 Sending FRMR due to undefined command control field

Description:

After establishing a link, LLC receives one octet data from SMDCP. After transmission of this frame to the peer, LLC receives a supervisory frame from the peer within the GRR_DATA_IND_TEST. The first byte of the S frame control field is set to 1110 0000. The MS sends an FRMR due to an undefined command control field. After the link is re-established, MS resumes data transmission.

Preamble:

LLC010A



Parametrization:

Primitive	Parameter	Value
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(1) LL_DATA_REQ

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
ll_qos	LLREQ_QOS_DEL4_REL5
radio_prio	LL_RADIO_PRIO_3
reserved_data_req1	NOT_USED
reference1	NOT_USED
seg_pos	NOT_USED
attached_counter	LLC_NO_ATTACHE
reserved_data_req4	NOT_USED
reserved_data_req5	NOT_USED
sdu	LLREQ_SDU1

(2) LL_READY_IND

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1

(3) GRR_READY_IND

(4) GRR_DATA_REQ

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	GRR_DATAREQ_1OCT_NS0_NR0_SAPI3

(5) GRR_DATA_IND_TEST

tli	TLLI_LOCAL_1
sdu	SGSN_UNDEFCOMMCTRL0_SAPI3

(6) LLGMM_STATUS_IND

error_cause	LLGMM_ERRCS_FRMR_COND_REEST
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(7) GRR_READY_IND

(8) GRR_DATA_REQ

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	MS_FRMR_UNDEFCTRL0_VS1_REEST_SAPI3

(9) GRR_READY_IND

(10) GRR_DATA_REQ

sapi	LL_SAPI_3
tli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAKSUB
radio_prio	GRR_RADIO_PRIO_1
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	MS_SABM_NOL3_N201I_520_SAPI3

(11) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_NOL3_N201_I520_SAPI3
(12) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_400 N201_I_520 LL_XID_INVALID EMPTY_SDU
(13) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 LL_XID_INVALID EMPTY_SDU
(14) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(15) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU500
(16) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(17) GRR_READY_IND		
(18) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu GRR_DATAREQ_NS0_NR0_A1_500_SAPI3	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED
(19) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_RR_NR1_A0_SAPI3
(20) LL_DATA_CNF	sapi tlli reference1	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED
(21) LL_DATA_REQ		

	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(22) LL_READY_IND		
	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
(23) GRR_READY_IND		
(24) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	
	GRR_DATAREQ_NS1_NR0_A1_500_SAPI3	
(25) GRR_DATA_IND_TEST		
	tli	TLLI_LOCAL_1
	sdu	GRRIND_RR_NR2_A0_SAPI3
(26) LL_DATA_CNF		
	sapi	LL_SAPI_3
	tli	TLLI_LOCAL_1
	reference1	NOT_USED

History:

03-Mar-2003 UT Initial

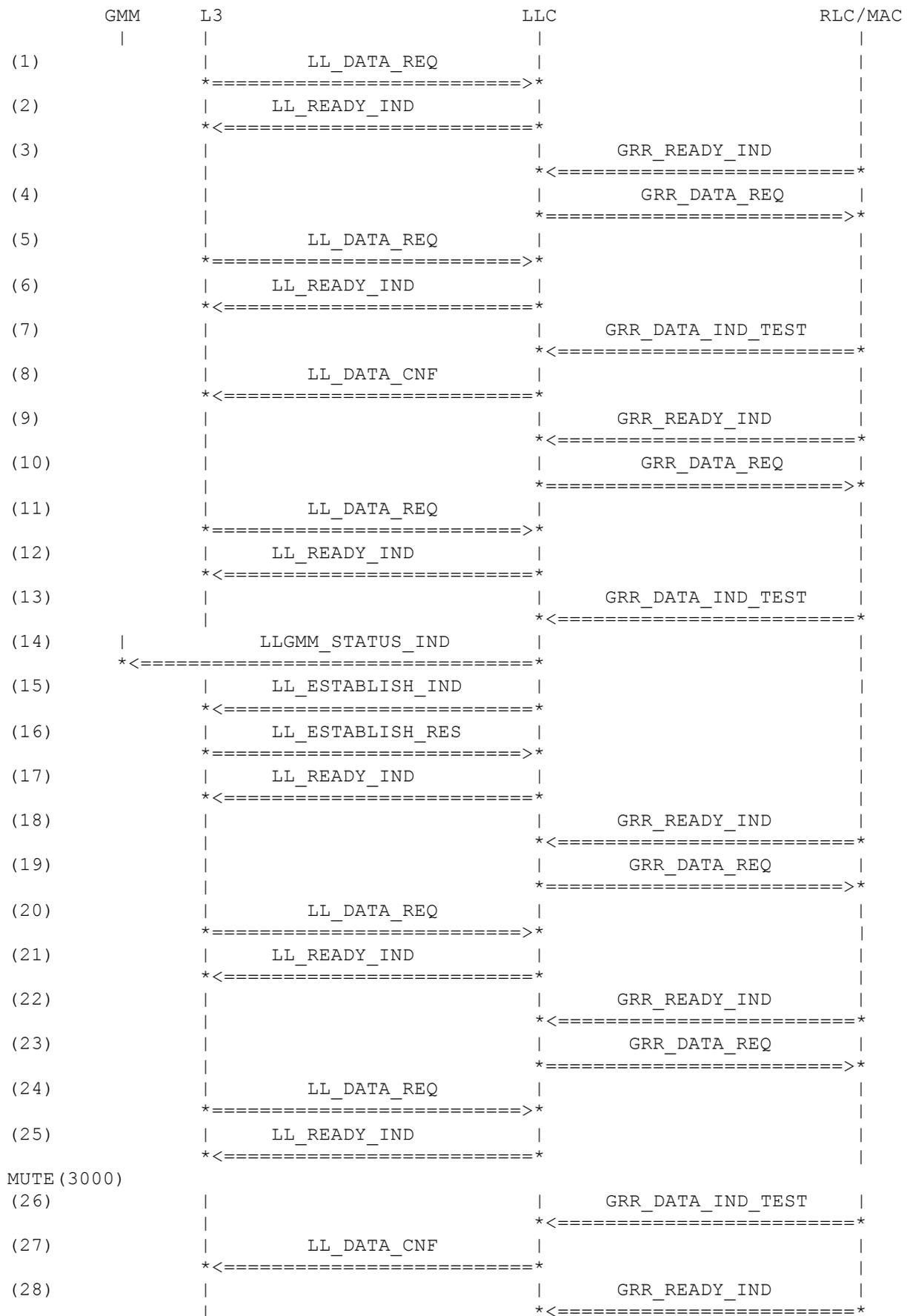
3.19.6 LLC014: GCF390 – TC 46.1.2.2.4.1 Reestablishment due to reception of SABM

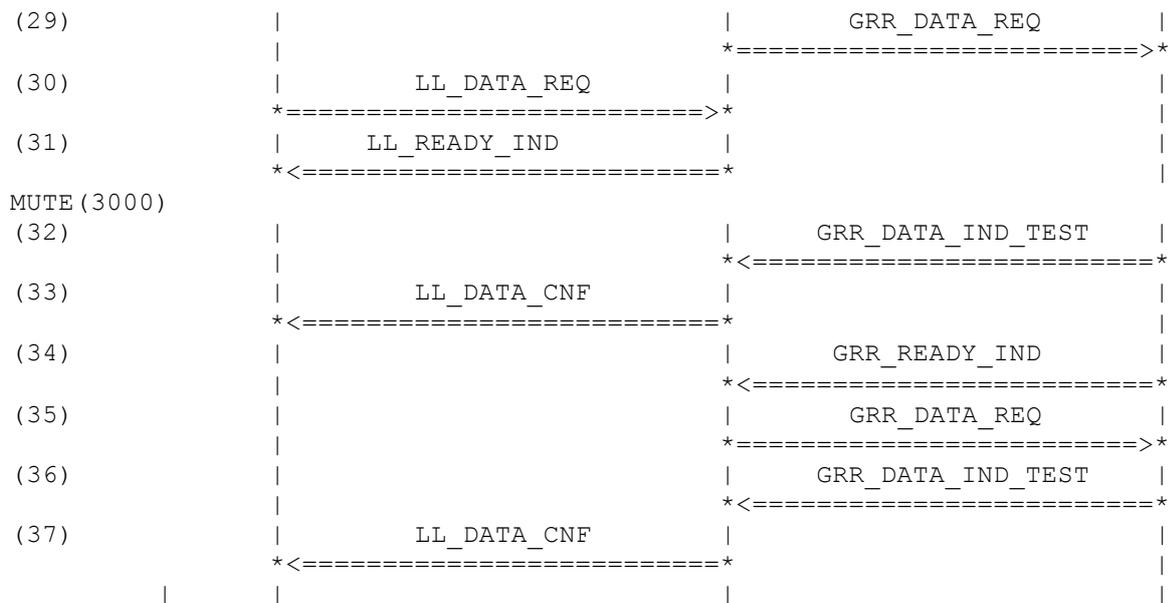
Description:

After establishing a link, LLC receives data from SNDCP. After sending 1 frame to the peer, which is acknowledged by a RR, LLC receives a SABM from the peer and LLC responds with an UA. After the link is re-established, the MS resumes data transmission.

Preamble:

LLC009B



**Parametrization:**

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
sdu	LLREQ_SDU500	
(2) LL_READY_IND	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS0_NR0_A1_500_SAPI9
(5) LL_DATA_REQ	sapi	LL_SAPI_9
	tli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED

	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(6) LL_READY_IND	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_RR_NR1_A0_SAPI9
(8) LL_DATA_CNF	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	reference1	NOT_USED
(9) GRR_READY_IND		
(10) GRR_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS1_NR0_A1_500_SAPI9
(11) LL_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DELE4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(12) LL_READY_IND	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(13) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_SABM_NOXID_SAPI9
(14) LLGMM_STATUS_IND	error_cause	LLGMM_ERRCS_PEER_REEST
(15) LL_ESTABLISH_IND	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_DEF_SAPI9
	n201_i	N201_I_520

	xid_valid sdu	LL_XID_INVALID EMPTY_SDU
(16) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_9 TLLI_LOCAL_1 LL_XID_INVALID EMPTY_SDU
(17) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(18) GRR_READY_IND		
(19) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_UA1_NOXID_SAPI9
(20) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_9 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU500
(21) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(22) GRR_READY_IND		
(23) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu GRR_DATAREQ_NS0_NR0_A1_500_SAPI9	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED
(24) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1	LL_SAPI_9 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED

	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(25) LL_READY_IND	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(26) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_RR_NR1_A0_SAPI9
(27) LL_DATA_CNF	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	reference1	NOT_USED
(28) GRR_READY_IND		
(29) GRR_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS1_NR0_A1_500_SAPI9
(30) LL_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(31) LL_READY_IND	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(32) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_RR_NR2_A0_SAPI9
(33) LL_DATA_CNF	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	reference1	NOT_USED
(34) GRR_READY_IND		

(35) GRR_DATA_REQ

sapi	LL_SAPI_9
tli	TLLI_LOCAL_1
grr_qos	GRRREQ_QOS_PEAQSUB
radio_prio	GRR_RADIO_PRIO_3
cause	GRR_DTACS_DEF
reserved_data_req	NOT_USED
sdu	
GRR_DATAREQ_NS2_NR0_A1_500_SAPI9	

(36) GRR_DATA_IND_TEST

tli	TLLI_LOCAL_1
sdu	GRRIND_RR_NR3_A0_SAPI9

(37) LL_DATA_CNF

sapi	LL_SAPI_9
tli	TLLI_LOCAL_1
reference1	NOT_USED

History:

04-Mar-2003	UT	Initial
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3.19.7 LLC016: GCF390 – TC 46.1.2.2.3.3 SACK frame

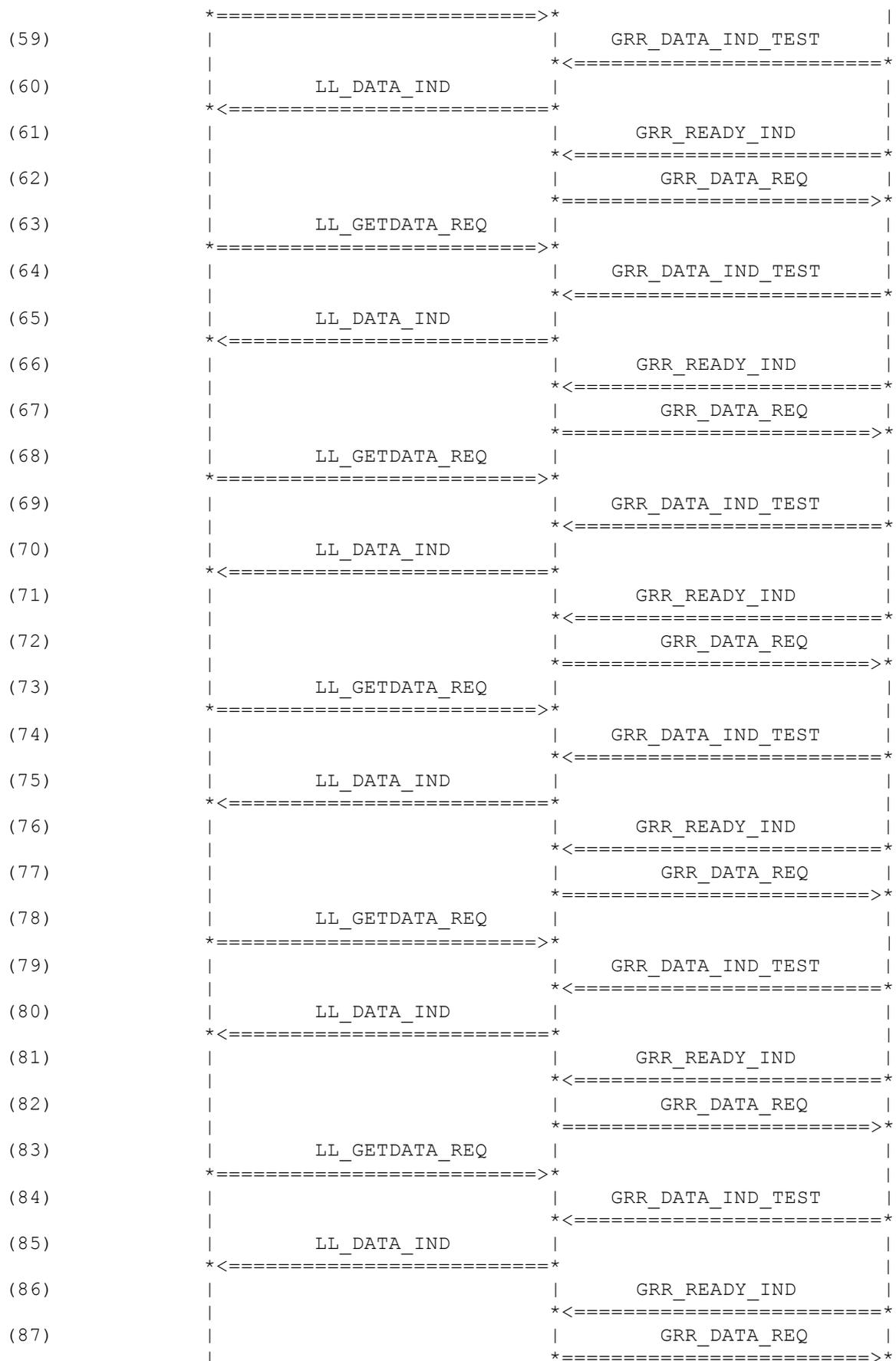
Description:

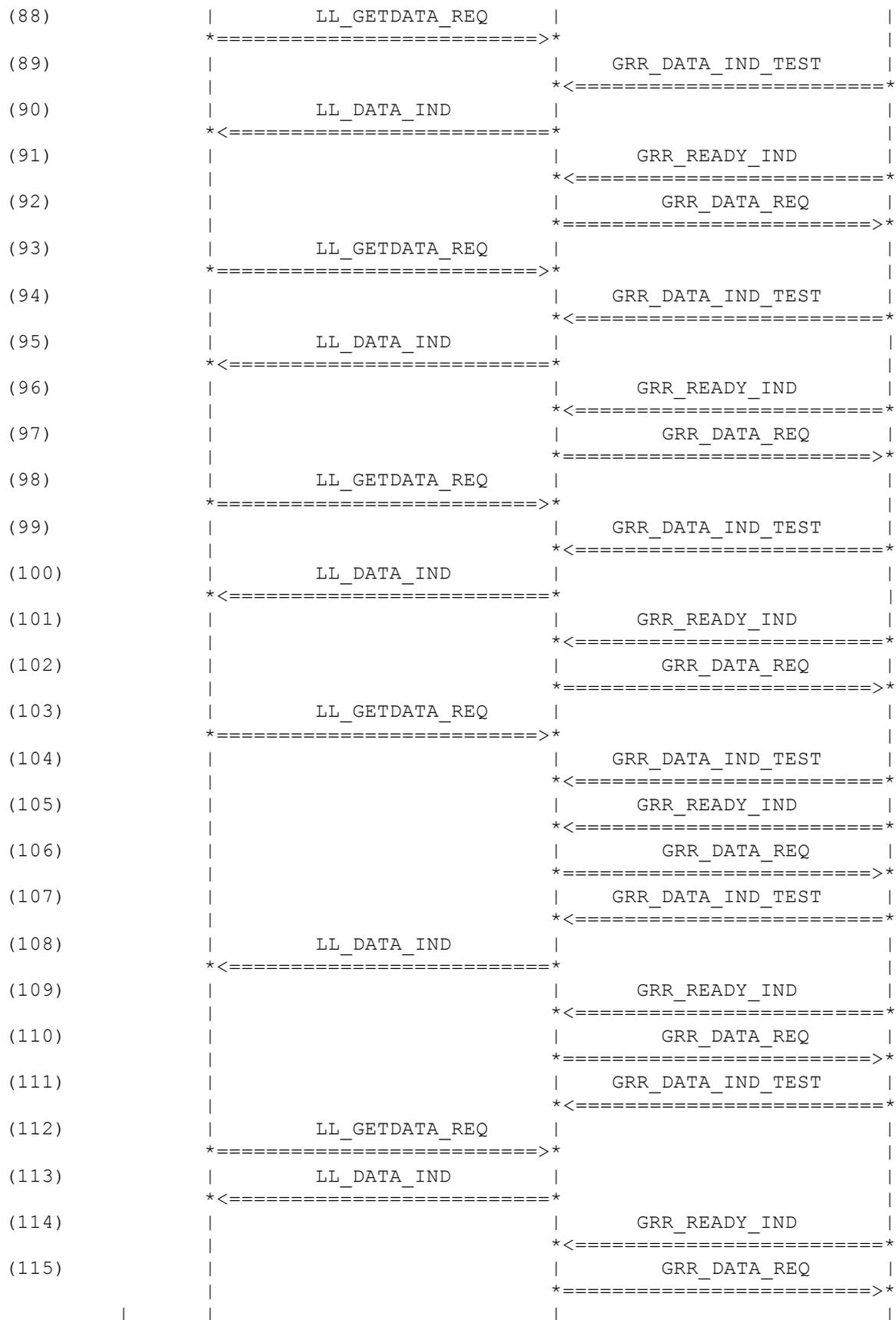
LLC receives frames from $N(S) = 0$ to $N(S) = 17$. The A bit is set to 1 for all frames. All the frames are acknowledged using RR. LLC receives the frame with $N(S) = 20$ with the A bit set to 1. LLC negatively acknowledges the frames 18 and 19 by using SACK. After reception of frame 18 LLC sends an ACK acknowledging frame 18 and 20. LLC gets frame 19 and acknowledges this frame by sending a RR with $NR=21$.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_I 520)			
(1)		LLGMM_ASSIGN_REQ		
		*****>*		
(2)		LL_UNITREADY_IND		
		*<*****		
(3)		LL_UNITREADY_IND		
		*<*****		
(4)		LL_UNITREADY_IND		
		*<*****		
(5)		LL_UNITREADY_IND		
		*<*****		
(6)		LL_UNITREADY_IND		
		*<*****		
(7)		LL_ESTABLISH_REQ		
		*****>*		
(8)			GRR_READY_IND	
			*<*****	
(9)			GRR_DATA_REQ	
			*****>*	
(10)			GRR_DATA_IND_TEST	
			*<*****	
(11)		LL_ESTABLISH_CNF		
		*<*****		
(12)		LL_READY_IND		
		*<*****		
(13)		LL_GETDATA_REQ		
		*****>*		
(14)			GRR_DATA_IND_TEST	
			*<*****	
(15)		LL_DATA_IND		
		*<*****		
(16)			GRR_READY_IND	
			*<*****	
(17)			GRR_DATA_REQ	
			*****>*	
(18)		LL_GETDATA_REQ		
		*****>*		
(19)			GRR_DATA_IND_TEST	
			*<*****	
(20)		LL_DATA_IND		
		*<*****		
(21)			GRR_READY_IND	
			*<*****	
(22)			GRR_DATA_REQ	
			*****>*	
(23)		LL_GETDATA_REQ		
		*****>*		
(24)			GRR_DATA_IND_TEST	
			*<*****	
(25)		LL_DATA_IND		
		*<*****		
(26)			GRR_READY_IND	
			*<*****	
(27)			GRR_DATA_REQ	
			*****>*	
(28)		LL_GETDATA_REQ		
		*****>*		





Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi	LL_SAPI_5
	tlli	LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi	LL_SAPI_7
	tlli	LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi	LL_SAPI_9
	tlli	LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi	LL_SAPI_11
	tlli	LL_TLLI_INVALID
(7) LL_ESTABLISH_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	sdu	EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_SABM_N201I_520_SAPI3
(10) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_N201_I520_SAPI3
(11) LL_ESTABLISH_CNF	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	n201_u	NOT_USED
	n201_i	N201_I_520
	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(12) LL_READY_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

(13) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(14) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS0_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(15) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N0A1_SAPI3
(16) GRR_READY_IND		
(17) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR1_A0_SAPI3
(18) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(19) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS1_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(20) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N1A1_SAPI3
(21) GRR_READY_IND		
(22) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR2_A0_SAPI3

(23) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(24) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS2_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(25) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N2A1_SAPI3
(26) GRR_READY_IND		
(27) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR3_A0_SAPI3
(28) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(29) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS3_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(30) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N3A1_SAPI3
(31) GRR_READY_IND		
(32) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR4_A0_SAPI3

(33) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(34) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS4_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(35) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N4A1_SAPI3
(36) GRR_READY_IND		
(37) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR5_A0_SAPI3
(38) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(39) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS5_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(40) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N5A1_SAPI3
(41) GRR_READY_IND		
(42) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR6_A0_SAPI3

(43) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(44) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS6_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(45) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N6A1_SAPI3
(46) GRR_READY_IND		
(47) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR7_A0_SAPI3
(48) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(49) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS7_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(50) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N7A1_SAPI3
(51) GRR_READY_IND		
(52) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR8_A0_SAPI3

(53) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(54) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS8_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(55) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N8A1_SAPI3
(56) GRR_READY_IND		
(57) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR9_A0_SAPI3
(58) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(59) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS9_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(60) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N9A1_SAPI3
(61) GRR_READY_IND		
(62) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR10_A0_SAPI3

(63) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(64) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS10_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(65) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N10A1_SAPI3
(66) GRR_READY_IND		
(67) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR11_A0_SAPI3
(68) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(69) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS11_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(70) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N11A1_SAPI3
(71) GRR_READY_IND		
(72) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR12_A0_SAPI3

(73) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(74) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS12_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(75) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N12A1_SAPI3
(76) GRR_READY_IND		
(77) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR13_A0_SAPI3
(78) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(79) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS13_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(80) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N13A1_SAPI3
(81) GRR_READY_IND		
(82) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR14_A0_SAPI3

(83) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(84) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS14_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(85) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N14A1_SAPI3
(86) GRR_READY_IND		
(87) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR15_A0_SAPI3
(88) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(89) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS15_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(90) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N15A1_SAPI3
(91) GRR_READY_IND		
(92) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR16_A0_SAPI3

(93) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(94) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS16_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(95) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N16A1_SAPI3
(96) GRR_READY_IND		
(97) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR17_A0_SAPI3
(98) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(99) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS17_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(100) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N17A1_SAPI3
(101) GRR_READY_IND		
(102) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR18_A0_SAPI3

(103) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(104) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS20_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(105) GRR_READY_IND		
(106) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_SACK_NR18_R2_SAPI3
(107) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS18_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(108) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3 reserved_data_ind4 reserved_data_ind5 reserved_data_ind6 sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED NOT_USED LLDATAIND_SDU100_N18A1_SAPI3
(109) GRR_READY_IND		
(110) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_ACK_NR19_SAPI3
(111) GRR_DATA_IND_TEST	tlli sdu GRRIND_NS19_NR0_SDU100_A1_SAPI3	TLLI_LOCAL_1
(112) LL_GETDATA_REQ	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(113) LL_DATA_IND	sapi tlli reserved_data_ind1 reserved_data_ind2 reserved_data_ind3	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED NOT_USED NOT_USED

	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N19A1_SAPI3
(114) GRR_READY_IND		
(115) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRRIND_RR_NR21_A0_SAPI3

History:

04-Mar-2003	UT	Initial
10-Apr-2003	UT	LL_GETDATA_REQ added

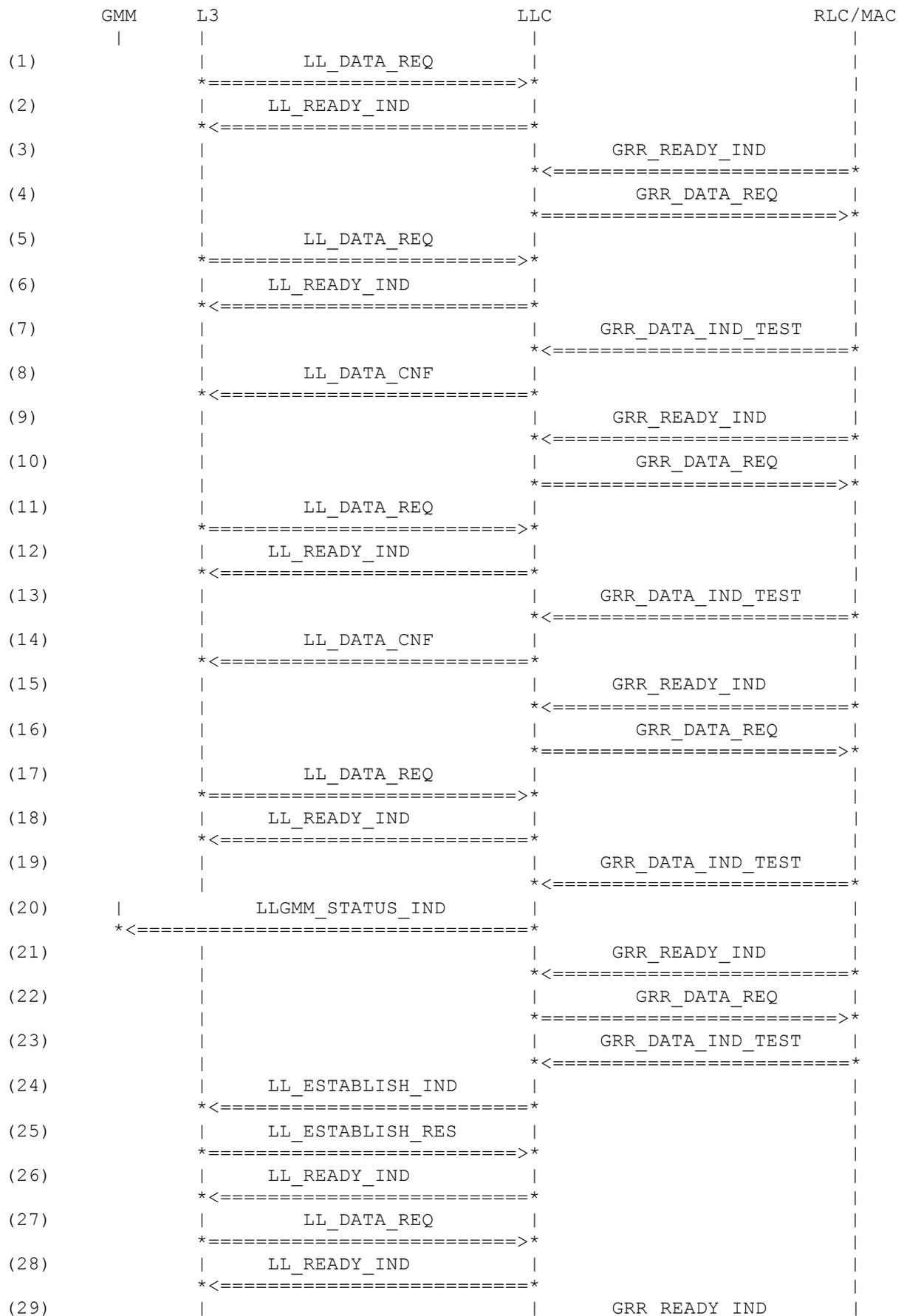
3.19.8 LLC017: GCF390 – TC 46.1.2.2.4.3 Reestablishment due to reception of DM

Description:

After establishing a link, LLC receives the primitive LL_DATA_REQ from SMDCP. The I+S frame is sent to GRR within the primitive GRR_DATA_REQ. This frame is acknowledged with RR. After sending the second frame to GRR and receiving RR, LLC sends a 3rd frame and receives a DM with F=0 from the GRR. LLC responds with SABM. After the link is re-established, LLC resumes data transmission starting with NS=0.

Preamble:

LLC010B



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(30)          |                                     *<=====
              |                                     |   GRR_DATA_REQ   |
              |                                     *=====>*
              |                                     |

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Parametrization:

Primitive	Parameter	Value
(1) LL_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(2) LL_READY_IND	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_NS0_NR0_A1_500_SAPI9
(5) LL_DATA_REQ	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(6) LL_READY_IND	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(7) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_RR_NR1_A0_SAPI9

(8) LL_DATA_CNF	sapi tlli reference1	LL_SAPI_9 TLLI_LOCAL_1 NOT_USED
(9) GRR_READY_IND		
(10) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu GRR_DATAREQ_NS1_NR0_A1_500_SAPI9	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED
(11) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_9 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU500
(12) LL_READY_IND	sapi tlli	LL_SAPI_9 TLLI_LOCAL_1
(13) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_RR_NR2_A0_SAPI9
(14) LL_DATA_CNF	sapi tlli reference1	LL_SAPI_9 TLLI_LOCAL_1 NOT_USED
(15) GRR_READY_IND		
(16) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu GRR_DATAREQ_NS2_NR0_A1_500_SAPI9	LL_SAPI_9 TLLI_LOCAL_1 GRRREQ_QOS_PEAQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED
(17) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1	LL_SAPI_9 TLLI_LOCAL_1 LLREQ_QOS_DEL4_REL5 LL_RADIO_PRIO_3 NOT_USED

	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(18) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(19) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	SGSN_DM0_SAPI9
(20) LLGMM_STATUS_IND		
	error_cause	
	LLGMM_ERRCS_DM0_RECEIVED_REEST	
(21) GRR_READY_IND		
(22) GRR_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	MS_SABM_NOL3_N201I_520_SAPI9
(23) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	SGSN_UA1_NOXID_SAPI9
(24) LL_ESTABLISH_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	n201_u	N201_U_400
	n201_i	N201_I_520
	xid_valid	LL_XID_INVALID
	sdu	EMPTY_SDU
(25) LL_ESTABLISH_RES		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	xid_valid	LL_XID_INVALID
	sdu	EMPTY_SDU
(26) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(27) LL_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE

	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU500
(28) LL_READY_IND		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
(29) GRR_READY_IND		
(30) GRR_DATA_REQ		
	sapi	LL_SAPI_9
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEQSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	
	GRR_DATAREQ_NS0_NR0_A1_500_SAPI9	

History:

04-Mar-2003	UT	Initial
27-May-2003	UT	LL_READY_IND added

3.19.9 LLC018: GCF390 – TC 46.1.2.4.1 Unsolicited DM

Description:

LLC receives the primitive LL_ESTABLISH_REQ from SMDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. The peer responds with UA : containing N201 below default, ABM is established.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_I 520)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)		LL_ESTABLISH_REQ		
		=====>		
(8)			GRR_READY_IND	
			<=====	
(9)			GRR_DATA_REQ	
			=====>	
(10)			GRR_DATA_IND_TEST	
			<=====	
TIMEOUT (5000)				
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	
(13)			GRR_DATA_IND_TEST	
			<=====	
(14)		LL_ESTABLISH_CNF		
		<=====		
(15)		LL_READY_IND		
		<=====		
(16)		LL_GETDATA_REQ		
		=====>		
(17)			GRR_DATA_IND_TEST	
			<=====	
(18)		LL_DATA_IND		
		<=====		
(19)			GRR_READY_IND	
			<=====	
(20)			GRR_DATA_REQ	
			=====>	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM
(2) LL_UNITREADY_IND	sapi	LL_SAPI_3
	tlli	LL_TLLI_INVALID

(3) LL_UNITREADY_IND	sapi tlli	LL_SAPI_5 LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi tlli	LL_SAPI_7 LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi tlli	LL_SAPI_9 LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi tlli	LL_SAPI_11 LL_TLLI_INVALID
(7) LL_ESTABLISH_REQ	sapi tlli sdu	LL_SAPI_3 TLLI_LOCAL_1 EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_SABM_N201I_520_SAPI3
(10) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_DM0_SAPI3
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_SABM_N201I_520_SAPI3
(13) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_EMPTY_L3_XID_SAPI3
(14) LL_ESTABLISH_CNF	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 NOT_USED N201_I_520 LL_XID_VALID EMPTY_SDU
(15) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1

(16) LL_GETDATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(17) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS0_NR0_SDU100_A1_SAPI3
(18) LL_DATA_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_SDU100_N0A1_SAPI3
(19) GRR_READY_IND		
(20) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRRIND_RR_NR1_A0_SAPI3

History:

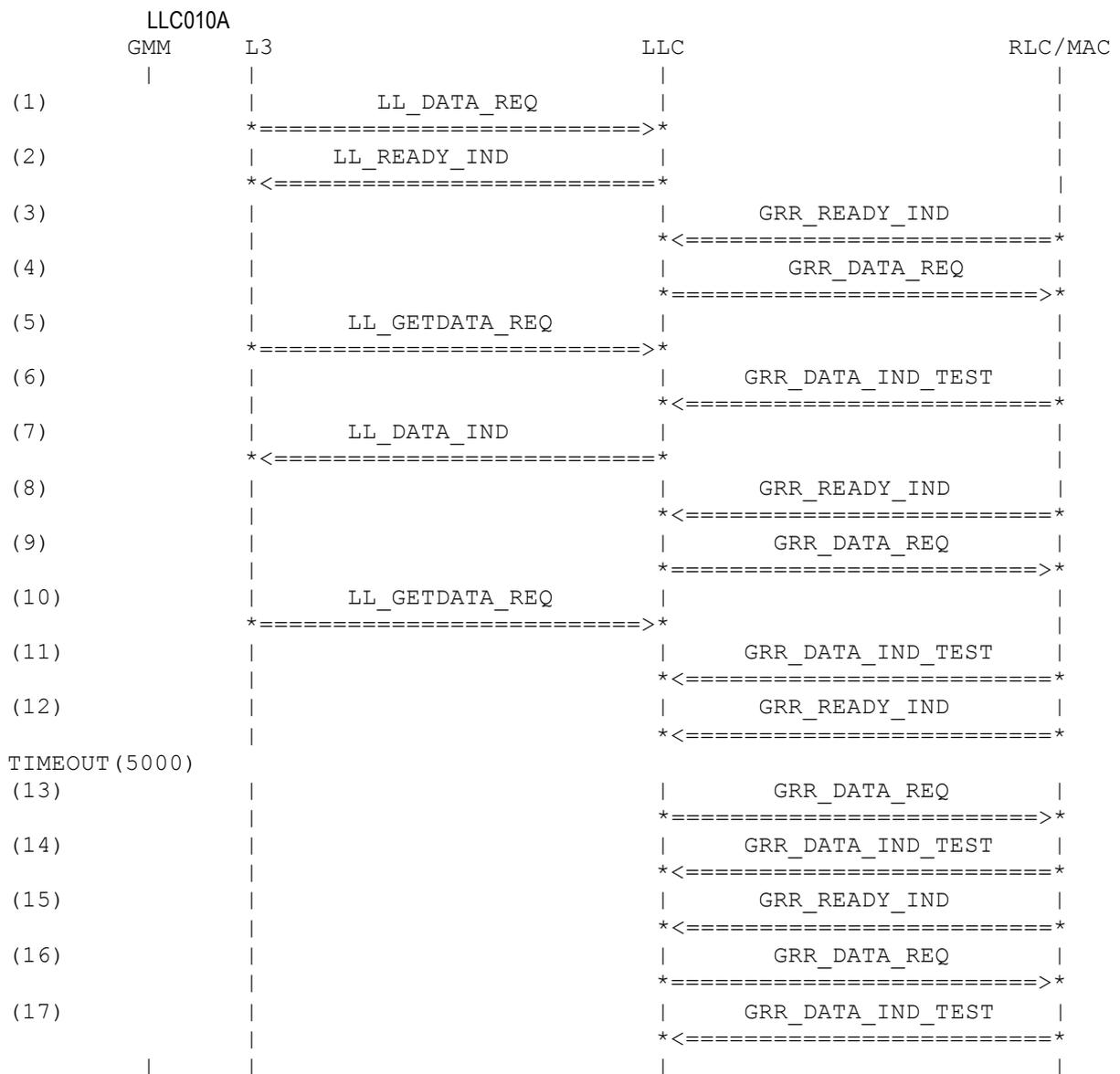
06-Mar-2003 UT

3.19.10 LLC019: GCF390 – TC 46.1.2.2.3.2 MS handling busy condition during bi-directional data transfer

Description:

LLC sends 1 octet of data to the peer. LLC receives 1 I+S frame from the peer, containing one octet of data. The received I+S frame is acknowledged with RR. After reception of RNR with NS=0, LLC sends a RR with NR=1. LLC receives a RR with NR=0 from the peer indicating that the peer is ready to receive data. LLC sends a combined I+S frame with 1 octet of data, NS=0 and NR=1. The peer responds with RR indicating that it received the frame.

Preamble:



Parametrization:

Primitive	Parameter	Value
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(1) LL_DATA_REQ

	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU1
(2) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_1OCT_NS0_NR0_SAPI3
(5) LL_GETDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(6) GRR_DATA_IND_TEST		
	tlli	TLLI_LOCAL_1
	sdu	GRRIND_NS0_NR0_A1_SDU1_SAPI3
(7) LL_DATA_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	reserved_data_ind1	NOT_USED
	reserved_data_ind2	NOT_USED
	reserved_data_ind3	NOT_USED
	reserved_data_ind4	NOT_USED
	reserved_data_ind5	NOT_USED
	reserved_data_ind6	NOT_USED
	sdu	LLDATAIND_1OCT_N0A1_SAPI3
(8) GRR_READY_IND		
(9) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRRIND_RR_NR1_A0_SAPI3
(10) LL_GETDATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1

(11) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_RNR_NR0_SAPI3
(12) GRR_READY_IND		
(13) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED GRRIND_RR_NR1_A1_SAPI3
(14) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_RR_NR0_A0_SAPI3
(15) GRR_READY_IND		
(16) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu GRR_DATAREQ_1OCT_NS0_NR1_SAPI3	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF NOT_USED
(17) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 GRRIND_RR_NR1_A0_SAPI3

History:

07-Mar-2003	UT	Initial
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3.19.11 LLC020: GCF390 – TC 46.1.2.2.1.4 Total loss of UA frame

Description:

LLC receives the primitive LL_ESTABLISH_REQ from SNDCP. LLC is in state 'ADM' and thus ready to establish ABM mode of operation. LLC transmits the SABM command to its peer to establish ABM. T200 timeouts, LLC retransmits the SABM command. After N200 retransmissions the peer does not respond. LLGMM_STATUS_IND is send to GMM and LL_RELEASE_IND to SNDCP.

NOTE:

All T200 timer values are set to 5 seconds in simulation.

Preamble:

LLC000

Variants:

<A>....<D>

	GMM	L3	LLC	RLC/MAC
COMMAND	(LLC CONFIG SAPI 3 N201_I 520)			
COMMAND	(LLC CONFIG SAPI 5 N201_I 520)			
COMMAND	(LLC CONFIG SAPI 9 N201_I 520)			
COMMAND	(LLC CONFIG SAPI 11 N201_I 520)			
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)		LL_ESTABLISH_REQ		
		=====>		
(8)			GRR_READY_IND	
			<=====	
(9)			GRR_DATA_REQ	
			=====>	
(10)			GRR_READY_IND	
			<=====	
TIMEOUT (5000)				
(11)			GRR_DATA_REQ	
			=====>	
(12)			GRR_READY_IND	
			<=====	
TIMEOUT (5000)				
(13)			GRR_DATA_REQ	
			=====>	
(14)			GRR_READY_IND	
			<=====	
TIMEOUT (5000)				
(15)			GRR_DATA_REQ	
			=====>	
(16)			GRR_READY_IND	
			<=====	
TIMEOUT (5000)				
(17)		LLGMM_STATUS_IND		
		<=====		
(18)		LL_RELEASE_IND		
		<=====		
MUTE (6000)				

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM

(2) LL_UNITREADY_IND	sapi tlli	LL_SAPI_3 LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi tlli	LL_SAPI_5 LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi tlli	LL_SAPI_7 LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi tlli	LL_SAPI_9 LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi tlli	LL_SAPI_11 LL_TLLI_INVALID
(7) LL_ESTABLISH_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi tlli sdu	LL_SAPI_11 TLLI_LOCAL_1 EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi tlli grr_qos radio_prio cause reserved_data_req	LL_SAPI_11 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED
<A>	sdu	MS_SABM_N201I_520_SAPI3
	sdu	MS_SABM_N201I_520_SAPI5
<C>	sdu	MS_SABM_N201I_520_SAPI9
<D>	sdu	MS_SABM_N201I_520_SAPI11
(10) GRR_READY_IND		
(11) GRR_DATA_REQ		
<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi tlli grr_qos radio_prio cause reserved_data_req	LL_SAPI_11 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED
<A>	sdu	MS_SABM_N201I_520_SAPI3
	sdu	MS_SABM_N201I_520_SAPI5
<C>	sdu	MS_SABM_N201I_520_SAPI9
<D>	sdu	MS_SABM_N201I_520_SAPI11
(12) GRR_READY_IND		

(13) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_N201I_520_SAPI3
	sdu	MS_SABM_N201I_520_SAPI5
<C>	sdu	MS_SABM_N201I_520_SAPI9
<D>	sdu	MS_SABM_N201I_520_SAPI11

(14) GRR_READY_IND

(15) GRR_DATA_REQ

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAQSUB
	radio_prio	GRR_RADIO_PRIO_1
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
<A>	sdu	MS_SABM_N201I_520_SAPI3
	sdu	MS_SABM_N201I_520_SAPI5
<C>	sdu	MS_SABM_N201I_520_SAPI9
<D>	sdu	MS_SABM_N201I_520_SAPI11

(16) GRR_READY_IND

(17) LLGMM_STATUS_IND

error_cause
LLGMM_ERRCS_SABM_NO_PEER_RES

(18) LL_RELEASE_IND

<A>	sapi	LL_SAPI_3
	sapi	LL_SAPI_5
<C>	sapi	LL_SAPI_9
<D>	sapi	LL_SAPI_11
	tlli	TLLI_LOCAL_1
	cause	LL_RELCS_NO_PEER_RES

History:

07-Mar-2003 UT Initial

3.19.12 LLC021: GCF390 – TC 46.1.2.3.1 Collision of SABM

Description:

Initiate link establishment from SNDCP by sending the primitive LL_ESTABLISH_REQ. An SABM with Layer-3 xid parameter present, is send to the peer. Upon reception of SABM from the peer with Layer-3 Parameters present, LLC ignores the SABM sent by the peer. After T200 seconds LLC resends the SABM. The peer responds with a UA. After ABM establishment 1 octet of data is send to the peer wich is acknowledged with RR.

Preamble:

LLC000

	GMM	L3	LLC	RLC/MAC
COMMAND				
(1)		LLGMM_ASSIGN_REQ		
		=====>		
(2)		LL_UNITREADY_IND		
		<=====		
(3)		LL_UNITREADY_IND		
		<=====		
(4)		LL_UNITREADY_IND		
		<=====		
(5)		LL_UNITREADY_IND		
		<=====		
(6)		LL_UNITREADY_IND		
		<=====		
(7)		LL_ESTABLISH_REQ		
		=====>		
(8)			GRR_READY_IND	
			<=====	
(9)			GRR_DATA_REQ	
			=====>	
(10)			GRR_DATA_IND_TEST	
			<=====	
TIMEOUT (5000)				
(11)			GRR_READY_IND	
			<=====	
(12)			GRR_DATA_REQ	
			=====>	
(13)			GRR_DATA_IND_TEST	
			<=====	
(14)		LL_ESTABLISH_CNF		
		<=====		
(15)		LL_READY_IND		
		<=====		
(16)		LL_DATA_REQ		
		=====>		
(17)		LL_READY_IND		
		<=====		
(18)			GRR_READY_IND	
			<=====	
(19)			GRR_DATA_REQ	
			=====>	
(20)			GRR_DATA_IND_TEST	
			<=====	

Parametrization:

Primitive	Parameter	Value
(1) LLGMM_ASSIGN_REQ	old_tlli	LLGMM_TLLI_INVALID
	new_tlli	TLLI_LOCAL_1
	llgmm_kc	LLGMM_KC_NULL
	ciphering_algorithm	LLGMM_CIPHER_NO_ALGORITHM

(2) LL_UNITREADY_IND	sapi tlli	LL_SAPI_3 LL_TLLI_INVALID
(3) LL_UNITREADY_IND	sapi tlli	LL_SAPI_5 LL_TLLI_INVALID
(4) LL_UNITREADY_IND	sapi tlli	LL_SAPI_7 LL_TLLI_INVALID
(5) LL_UNITREADY_IND	sapi tlli	LL_SAPI_9 LL_TLLI_INVALID
(6) LL_UNITREADY_IND	sapi tlli	LL_SAPI_11 LL_TLLI_INVALID
(7) LL_ESTABLISH_REQ	sapi tlli sdu	LL_SAPI_3 TLLI_LOCAL_1 EMPTY_SDU
(8) GRR_READY_IND		
(9) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_SABM_N201I_520_SAPI3
(10) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1
	SGSN_SABM_L30_N201I_520_XID_SAPI3	
(11) GRR_READY_IND		
(12) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEKSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_SABM_N201I_520_SAPI3
(13) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_N201U_400_SAPI3
(14) LL_ESTABLISH_CNF	sapi tlli n201_u n201_i	LL_SAPI_3 TLLI_LOCAL_1 N201_U_400 N201_I_520

	xid_valid	LL_XID_VALID
	sdu	EMPTY_SDU
(15) LL_READY_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(16) LL_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU1
(17) LL_READY_IND	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(18) GRR_READY_IND		
(19) GRR_DATA_REQ	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	GRR_DATAREQ_1OCT_NS0_NR0_SAPI3
(20) GRR_DATA_IND_TEST	tlli	TLLI_LOCAL_1
	sdu	GRRIND_RR_NR1_A0_SAPI3

History:

10-Mar-2003 UT

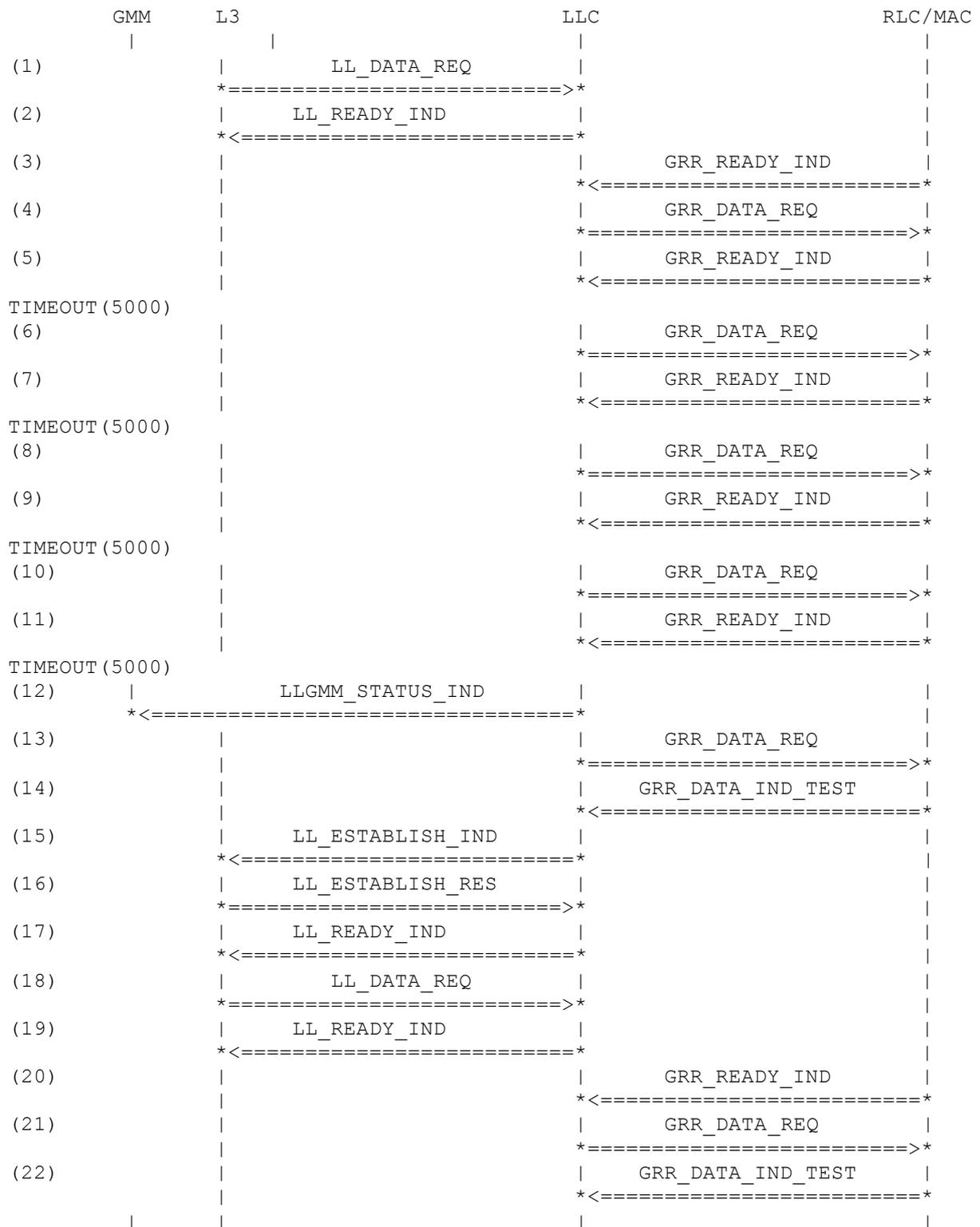
3.19.13 LLC022: GCF390 – TC 46.1.2.2.4.2 Reestablishment due to N200 failures of I+S data transfer

Description:

T201 expires N200 times while LLC was waiting for a frame acknowledge. Finally LLC initiate a re-establishment of the connection and sends an SABM command. A status indication including the re-establish cause is send to GMM. After receiving the UA the re-establish is indicated to L3. After receiving the response LLC is back in ABM.

Preamble:

LLC010A



Parametrization:

Primitive	Parameter	Value
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(1) LL_DATA_REQ

sapi	LL_SAPI_3
tlli	TLLI_LOCAL_1

	ll_qos	LLREQ_QOS_DEL4_REL5
	radio_prio	LL_RADIO_PRIO_3
	reserved_data_req1	NOT_USED
	reference1	NOT_USED
	seg_pos	NOT_USED
	attached_counter	LLC_NO_ATTACHE
	reserved_data_req4	NOT_USED
	reserved_data_req5	NOT_USED
	sdu	LLREQ_SDU1
(2) LL_READY_IND		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
(3) GRR_READY_IND		
(4) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	
	GRR_DATAREQ_1OCT_NS0_NR0_SAPI3	
(5) GRR_READY_IND		
(6) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	
	GRR_DATAREQ_1OCT_NS0_NR0_SAPI3	
(7) GRR_READY_IND		
(8) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	
	GRR_DATAREQ_1OCT_NS0_NR0_SAPI3	
(9) GRR_READY_IND		
(10) GRR_DATA_REQ		
	sapi	LL_SAPI_3
	tlli	TLLI_LOCAL_1
	grr_qos	GRRREQ_QOS_PEAKEKSUB
	radio_prio	GRR_RADIO_PRIO_3
	cause	GRR_DTACS_DEF
	reserved_data_req	NOT_USED
	sdu	
	GRR_DATAREQ_1OCT_NS0_NR0_SAPI3	
(11) GRR_READY_IND		

(12) LLGMM_STATUS_IND	error_cause LLGMM_ERRCS_ACK_NO_PEER_RES_REEST	
(13) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause reserved_data_req sdu	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_1 GRR_DTACS_DEF NOT_USED MS_SABM_NOL3_N201I_520_SAPI3
(14) GRR_DATA_IND_TEST	tlli sdu	TLLI_LOCAL_1 SGSN_UA1_NOL3_N201_I520_SAPI3
(15) LL_ESTABLISH_IND	sapi tlli n201_u n201_i xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 N201_U_400 N201_I_520 LL_XID_INVALID EMPTY_SDU
(16) LL_ESTABLISH_RES	sapi tlli xid_valid sdu	LL_SAPI_3 TLLI_LOCAL_1 LL_XID_INVALID EMPTY_SDU
(17) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(18) LL_DATA_REQ	sapi tlli ll_qos radio_prio reserved_data_req1 reference1 seg_pos attached_counter reserved_data_req4 reserved_data_req5 sdu	LL_SAPI_3 TLLI_LOCAL_1 LLREQ_QOS_DELE4_REL5 LL_RADIO_PRIO_3 NOT_USED NOT_USED NOT_USED LLC_NO_ATTACHE NOT_USED NOT_USED LLREQ_SDU1
(19) LL_READY_IND	sapi tlli	LL_SAPI_3 TLLI_LOCAL_1
(20) GRR_READY_IND		
(21) GRR_DATA_REQ	sapi tlli grr_qos radio_prio cause	LL_SAPI_3 TLLI_LOCAL_1 GRRREQ_QOS_PEAKEQSUB GRR_RADIO_PRIO_3 GRR_DTACS_DEF

			reserved_data_req	NOT_USED
			sdu	
			GRR_DATAREQ_1OCT_NS0_NR0_SAPI3	
(22) GRR_DATA_IND_TEST				
			tli	TLLI_LOCAL_1
			sdu	GRRIND_RR_NR1_A0_SAPI3
History:				
	11-Mar-2003	UT	Initial.	